

MINC-11

MNCAD DIAGNOSTIC
CVMNAA0

AH-B086A-MC

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FICHE 1 OF 1

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MADE IN USA

IDENTIFICATION

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SEQ 0001

Product Code: AC-B085A-MC
Product Name: CVMNAA0 - MNCAD Performance Test
Date: August 1978
Maintainer: Diagnostic Group

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Digital Equipment Corporation, Maynard, Mass.

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1.0 ABSTRACT

This diagnostic has three starting addresses:

200 Normal start
204 Restart
210 Starting address with tester

This diagnostic tests the MNCAD/MNCAM with or without the optional test module(s).

When starting the diagnostic, a set of tests are listed. The operator selects the test by the 'TEST CHARACTER' and then depresses the 'RETURN' key on the console. The following chart indicates which letter corresponds to which test loop or function to execute:

W: The entire wraparound test

Analog subtests
Noise test
Interchannel Settling test
Differential Linearity and Relative Accuracy test

C: Calibration loop for the mncad

P: Print converted analog values loop only

L: Logic subtests

A: Auto test

Logic subtests
Analog subtests
Noise test
Interchannel Settling test
Differential Linearity and Relative Accuracy test

N: Noise tests on selected channels

V: Same as W for video bit map console terminal (I.E. VT105, VT55)

B: Base or vector address change

G: Get new switch register value

H: Help the operator and re-type the test list

2.0 REQUIREMENTS

2.1 Equipment

LSI-11 computer with 12K of memory
I/O Terminal (LA36, VT100, etc.)
MNCAD/MNCAM Module
MNCAD-TA test module
Bit map for graphic output (I.E. VT105, VT55) <optional>

2.2 Storage

This program uses 12K of memory.

3.0 LOADING PROCEDURE

Procedure for loading normal binary tapes should be followed.

4.0 STARTING PROCEDURE

4.1 Control Switch Settings

Standard PDP-11 Format

SW15=1	100000	Halt on error
SW14=1	040000	Loop on test
SW13=1	020000	Inhibit error typeouts
SW12=1	010000	Inhibit sizing the number of MNCAD (A/D)'S
SW11=1	004000	Inhibit iterations
SW10=1	002000	Halt for video bit map display
SW9 =1	001000	Loop on error
SW8 =1	000400	Loop on test in SWR <7:0>

200 is the starting address of the diagnostic for standard tolerances. 204 is the restart address. 210 is the starting address of the diagnostic when the tester is connected and tighter tolerances are used.

5.0 OPERATING PROCEDURE

Start the diagnostic at 200 or 210. The program heading, request for initial switch register value. The operator will normally depress the 'RETURN' key. The program now request if the MNCAD-TA test module is connected. The operator responds by typing a 'Y' or 'N' followed by depressing the 'RETURN' key. A list of tests, loops, or functions available will be printed out. The operator selects the character, according to the table listed, and depresses the 'RETURN' key.

A control character (^C) is set aside for interrupting a test and transferring control to the beginning of the diagnostic. During the logic tests, while a reset is being performed, control C will not be executed. Therefore, continue typing control C until it is successful.

For machines without a hardware switch register, location SWREG (176) is used as a software switch register. To modify the contents of SWREG, depress 'CTRL' and 'G' together. The program responds with the current contents of SWREG and a slash. Type the desired new contents of SWREG followed by a carriage return.

If 'W' is selected, the program will ask that the MNCAD (A/D)'S front panel switches be set to 'TEST'. If the test module is connected, the program will also ask that the test module(s) switches be set to single-ended. Set the switches and depress 'RETURN'. The program will then give a channel table for the MNCAD (A/D) under test. If the test module is connected, the program will then ask for channels to test. The channels under test must be contiguous. The program will run through the analog subtests, the noise test, the interchannel settling test, and the differential linearity and relative accuracy test.

If 'C' is typed, the program will run the calibration routine and loop on the test until it is calibrated and a 'RETURN' is typed. If a certain MNCAD (A/D) is to be calibrated, use the 'B' command to inform the program of its base and vector address.

If 'P' is typed, the program will run the print values routine and will loop on that test until the operator type 'CTRL C'. If a certain MNCAD is to be tested, use the 'B' command to inform the program of its base and vector address.

If 'A' is typed, the program will execute the logic tests, analog tests, noise, settle, and differential linearity. At the beginning of the test, the program will ask that the A/D switches be set to 'TEST'. If the test module is connected, the program will also ask that the test module(s) switches be set to single-ended. Set switches and depress 'RETURN'. The program will then give a channel table for the MNCAD (A/D) under test. If the test module is connected, the program will then ask for channels to test. The channels under test must be contiguous. The program will run through the analog subtests, the noise test, the interchannel settling test, and the differential linearity and relative accuracy test.

If 'L' is typed, the program will then size the number of MNCAD (A/D)'S and report the number of units found. The program will then execute the logic tests, printing 'END PASS' when it has completed an entire pass. If additional MNCAD (A/D)'S are detected, the test will be run successively on each MNCAD. If the test module is connected, the program will ask the operator to depress the test module 'EXTERNAL START' switch on the first pass.

5.1 Inhibiting auto-size feature

Logic, auto and wraparound tests will automatically auto-size and report the number of MNCAD'S it detects on the system. To inhibit this feature, set switch register bit 12 to a one. Another way to inhibit this feature is to set bit 15 of location \$ENVM (1214). Also, use the program 'B' command to modify the default base and vector addresses for other than the first MNCAD.

5.2 End of pass typeouts

At end of pass, the following typeout will occur:

```
'END PASS 12 ;TOTAL ERROR COUNT = 5 ;BAD UNITS 000000000000100'
```

This indicates that:

Twelve passes thru the program have been made.
A total of 5 errors have been detected.
Unit # 3 was the unit with errors.

6.0 ERRORS

This program uses the diagnostic 'SYSMAC' package for error reporting and typeout. The error information consists of the following:

UNIT: Unit number
ERRPC: Location at which an error was detected.
STREG: Address of the status register.
ADBUFF: Address of the buffer
CHANL: Channel value
NOMINAL: Expected correct data
TOLERANCE: The acceptable deviation from the nominal
ACTUAL: Actual data
EXPECTED: Expected correct data

7.0 MISCELLANEOUS

7.1 Execution time

Execution time for each of the tests is:

Calibration:	5 conversions/min @110 baud
Print values:	8 conversions/8 seconds @ 110 baud
Wraparound test:	7 minutes first pass; 22 minutes for successive passes
Logic test:	30 seconds
Auto test:	8 minutes first pass, 23 minutes for successive passes

7.2 Status register and vector addresses

When testing more than one MNCAD, the difference in addresses is 4 for bus address and 10 for vector address. These values are in VADR (bus address) (1352) and VVCT (vector address) (1354). The first MNCAD'S status register address must be in \$BASE (1244), its vector address must be in the low byte of \$VECT1 (1240). The operator may use the 'B' program command to change the default values.

7.3 Switch register

If a hardware switch register is present and the operator desires to use a software switch register and the control G feature, it is necessary to load the starting address, set the hardware switch register to all ones (-1), and then start. The program will then run with the software switch register.

7.4 Bit map graphic output terminal available

The operator may inform the program that the console is a bit map terminal (I.E. VT105 or VT55) by using the 'V' command. the program will then display the results of the differential linearity test on the bit map terminal screen.

8.0 RESTRICTIONS

8.1 Testing

8.2 Starting restriction

If a free-running clock, such as 60Hz from the power supply, is attached to the BEVNT bus line on both Rev level C/D and E systems, an interrupt to location 100 will occur when using the 'G' and 'L' commands prior to executing the first instruction. Therefore this program can not disable the BEVNT bus line by inhibiting interrupts.

User systems requiring a free-running clock attached to the BEVNT bus line can temporarily avoid this situation by setting the PSW(RS) to 200, instead of using the 'G' command, load the PC (R7) with the starting address and use the proceed 'P' command. Before using the 'L' command, the PSW(RS) can be set to 200 to avoid receiving the BEVNT interrupt after loading the ABS loader.

8.3 Possible program 'BOMBS'

The first two tests of this program check to see if the MNCAD responds to the expected address. If the MNCAD does not respond, a buss error occurs. Also bus errors can occur during the time the program sizes to see how many MNCAD'S are on your system.

For more information on the next subject, see Jan. 1976 LSI-11 ENGINEERING BULLETIN issued by the Digital Components Group.

Bus errors may alter the preset contents of location 4 before the trap is executed, thereby transferring program control to an area in the program that was not set up to handle the trap. If this happens, the program will 'BOMB' and possibly rewrite parts of itself.

9.0 PROGRAM DESCRIPTION

9.1 Logic tests

These 24 logic subtests run sequentially without further operator intervention. its purpose is to check that each of the status register bits that are read/write can be loaded and properly read back; that initialize clears the external start enable bit, the done bit, the interrupt enable bit, the overflow bit, the error flag, and the A/D start bit. It also checks that the A/D done flag sets at end of conversion and clears when the converted value is read. It checks the interrupt logic and the correct setting of the error flag.

9.2 Calibration routine

If 'C' is typed, the program will ask for a channel. Type channel number followed by depressing 'RETURN'. The program will ask you if you want offset or gain. Apply voltage requested to selected channel. Adjust pot requested for 0.00 LSB typeout. Type carriage return when adjusted. The last typeout will be checked for 0.00 LSB with a tolerance of 0.04 LSB if outside, the program will ask you to re-adjust the same pot again.

9.3 Print values routine

This test begins when the operator types 'P'. It then loads the channel from the switch register bits 0-7 and does a conversion on that channel. If SWR bit 13 is down (0), it prints out the converted value on the console terminal; if SWR bit 13 is up (1), it puts the converted value in the 'DISPLAY REGISTER'. The operator may change the channel (using the switch register) at any time during the test. However, the new values from the new channel will not be printed until the next line of 8 values is printed. The 8 values on each line correspond to only one channel.

9.4 Differential linearity

This test determines the width of each state to within 0.01 LSB.

9.5 Settling test

The purpose of this test is to verify that the time allowed for settling to a new input value after switching channels does not result in an error that exceeds the expected amount for such a change.

9.6 Noise test

This test measures the short-term MINC-11 system noise. RMS noise equals 1 standard deviation of the Gaussian curve, PEAK noise equals 3 standard deviation of the Gaussian curve.

9.7 Analog tests

These 6 subtests check the channels and their output.

21	BASIC DEFINITIONS
22	OPERATIONAL SWITCH SETTINGS
29	TRAP CATCHER
56	ACT11 HOOKS
58	APT PARAMETER BLOCK
59	COMMON TAGS
(2)	APT MAILBOX-ETABLE
(1)	ERROR POINTER TABLE
113	MISCELLANEOUS, TEMPORARY, AND STORAGE LOCATIONS
176	INITIAL START-UP, HOUSEKEEPING, AND DIALOGUE
191	INITIALIZE THE COMMON TAGS
203	TYPE PROGRAM NAME
(2)	GET VALUE FOR SOFTWARE SWITCH REGISTER
230	OPERATOR INPUT DECODER
290	DETERMINE THE NUMBER OF MNCAD'S ON THE SYSTEM
344	T1 +15 VOLT TEST (IN-HOUSE TESTER ONLY)
370	T2 -15 VOLT TEST (IN-HOUSE TESTER ONLY)
388	T3 FLOAT A ONE THRU MULTIPLEXER BITS
400	T4 LOAD AND READ BACK ERROR I.E. BIT14
404	T5 LOAD AND READ BACK INTERRUPT ENABLE BIT6
410	T6 LOAD AND READ BACK CLOCK OVERFLOW START ENABLE BITS
414	T7 LOAD AND READ BACK EXTERNAL START ENABLE BIT4
418	T10 LOAD AND READ BACK MAINT. TST BIT2
423	T11 LOAD AND READ BACK ENABLE I.D. BIT3
428	T12 LOAD AND READ BACK ERROR FLAG BIT15
432	T13 TEST INIT CLEARS BITS 2-6,8-14
440	T14 TEST INIT CLEARS ERROR FLAG
447	T15 TEST DONE FLAG SETS AND BIT0 CLEARS ON END OF CONV.
457	T16 TEST INIT CLEARS DONE FLAG
467	T17 TEST A/D DONE FLAG CLEARS WHEN READ CONVERTED VALUE
476	T20 TEST ALL '0'S RESULTS USING MAINT. ADTST. BIT
486	T21 TEST ALL '1'S RESULT USING MAINT. ADTST. BIT
497	T22 GENERATE INTERRUPT WHEN DONE FLAG SETS AFTER CONVERSION
524	T23 TEST INTERRUPT OCCURS WHEN ERROR AND I.E.E. IS SET
549	T24 TEST ERROR FLAG SETS IF 2ND CONVERSION ENDS BEFORE READING BUFFER
564	T25 TEST ERROR FLAG SETS IF START 2ND CONV. BEFORE DONE FLAG SETS
577	T26 TEST CHANNELS 0-7 FOR SINGLE ENDED
590	T27 TEST CLOCK OVERFLOW STARTS A/D (TESTER ONLY)
605	T30 TEST EXTERNAL START STARTS A/D (TEST MODULE OR TESTER)
649	WRAPAROUND TEST SECTION
651	T31 TEST CH0 GROUND
661	T32 TEST CH1 +4.5 VOLT
669	T33 TEST CH2 -4.5 VOLT
676	T34 TEST CH5 GROUND (DWARF OR TESTER)
687	T35 TEST CH4 +2.6 VOLTS (DWARF OR TESTER)
695	T36 TEST CH6 -2.2 VOLTS (DWARF OR TESTER)
704	T37 TEST VOLTAGE ON CHANNELS (DWARF OR TESTER)
723	T40 TEST CHANNEL FOR +- 2.2 VOLTS IN DIFFERENTIAL MODE
772	T41 TEST VERNIER OFFSET DAC ON CH0
785	T42 OFFSET ON CH0
807	T43 TEST RAMP RANGE, CH3
835	T44 NOISE TEST, 1 EDGE
936	T45 INTERCHANNEL SETTLING TEST, 1 EDGE
984	T46 DIFFERENTIAL LINEARITY AND RELATIVE ACCURACY TEST (CHANNEL 3)

1053	CALIBRATION SECTION
1107	SWITCH GAIN MANUAL INTERVENTION TEST
1152	PRINT VALUES ROUTINE
1186	LOGIC TEST SECTION START-UP
1196	AUTO TEST START-UP
1211	WRAPAROUND TEST START-UP
1222	NOISE TEST START-UP
1432	DETERMINE IF MORE MNCAD'S TO BE TESTED
2130	END OF PASS ROUTINE
2240	ASCII MESSAGES
2363	ASCII TEXT MESSAGES
2402	TTY INPUT ROUTINE
2404	READ AN OCTAL NUMBER FROM THE TTY
2406	SCOPE HANDLER ROUTINE
2419	ERROR HANDLER ROUTINE
2420	ERROR MESSAGE TIMEOUT ROUTINE
2421	POWER DOWN AND UP ROUTINES
2424	TYPE ROUTINE
2425	CONVERT BINARY TO DECIMAL AND TYPE ROUTINE
2426	APT COMMUNICATIONS ROUTINE
2428	BINARY TO OCTAL (ASCII) AND TYPE
2429	BINARY TO ASCII AND TYPE ROUTINE
2431	TRAP DECODER
(3)	TRAP TABLE

```
20      .TITLE CVMNA-A MNCAD/MNCAM    DIAGNOSTIC
(1)    : *COPYRIGHT (C) 1978
(1)    : *DIGITAL EQUIPMENT CORP.
(1)    : *MAYNARD, MASS. 01754
(1)    : *
(1)    : *PROGRAM BY GEORGE STEVENS
(1)    : *
(1)    : *THIS PROGRAM WAS ASSEMBLED USING THE PDP-11 MAINDEC SYSMAC
(1)    : *PACKAGE (MAINDEC-11-DZQAC-C3), JAN 19, 1977.
(1)    : *
21      .SBTTL BASIC DEFINITIONS
(1)
(1)    : *INITIAL ADDRESS OF THE STACK POINTER *** 1100 ***
(1)    001100 STACK= 1100
(1)    .EQUIV EMT,ERROR      ;;BASIC DEFINITION OF ERROR CALL
(1)    .EQUIV IOT,SCOPE      ;;BASIC DEFINITION OF SCOPE CALL
(1)
(1)    : *MISCELLANEOUS DEFINITIONS
(1)    000011 HT= 11          ;;CODE FOR HORIZONTAL TAB
(1)    000012 LF= 12          ;;CODE FOR LINE FEED
(1)    000015 CR= 15          ;;CODE FOR CARRIAGE RETURN
(1)    000200 CRLF= 200       ;;CODE FOR CARRIAGE RETURN-LINE FEED
(1)    177776 PS= 177776     ;;PROCESSOR STATUS WORD
(1)    .EQUIV PS,PSW
(1)    177774 STKLMT= 177774  ;;STACK LIMIT REGISTER
(1)    177772 PIRQ= 177772   ;;PROGRAM INTERRUPT REQUEST REGISTER
(1)    177570 DSWR= 177570   ;;HARDWARE SWITCH REGISTER
(1)    177570 DDISP= 177570  ;;HARDWARE DISPLAY REGISTER
(1)
(1)    : *GENERAL PURPOSE REGISTER DEFINITIONS
(1)    000000 R0= %0          ;;GENERAL REGISTER
(1)    000001 R1= %1          ;;GENERAL REGISTER
(1)    000002 R2= %2          ;;GENERAL REGISTER
(1)    000003 R3= %3          ;;GENERAL REGISTER
(1)    000004 R4= %4          ;;GENERAL REGISTER
(1)    000005 R5= %5          ;;GENERAL REGISTER
(1)    000006 R6= %6          ;;GENERAL REGISTER
(1)    000007 R7= %7          ;;GENERAL REGISTER
(1)    000006 SP= %6          ;;STACK POINTER
(1)    000007 PC= %7          ;;PROGRAM COUNTER
(1)
(1)    : *PRIORITY LEVEL DEFINITIONS
(1)    000000 PR0= 0          ;;PRIORITY LEVEL 0
(1)    000040 PR1= 40         ;;PRIORITY LEVEL 1
(1)    000100 PR2= 100        ;;PRIORITY LEVEL 2
(1)    000140 PR3= 140        ;;PRIORITY LEVEL 3
(1)    000200 PR4= 200        ;;PRIORITY LEVEL 4
(1)    000240 PR5= 240        ;;PRIORITY LEVEL 5
(1)    000300 PR6= 300        ;;PRIORITY LEVEL 6
(1)    000340 PR7= 340        ;;PRIORITY LEVEL 7
(1)
(1)    : *'SWITCH REGISTER' SWITCH DEFINITIONS
(1)    100000 SW15= 100000
(1)    040000 SW14= 40000
```

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(1) 020000 SW13= 20000
(1) 010000 SW12= 10000
(1) 004000 SW11= 4000
(1) 002000 SW10= 2000
(1) 001000 SW09= 1000
(1) 000400 SW08= 400
(1) 000200 SW07= 200
(1) 000100 SW06= 100
(1) 000040 SW05= 40
(1) 000020 SW04= 20
(1) 000010 SW03= 10
(1) 000004 SW02= 4
(1) 000002 SW01= 2
(1) 000001 SW00= 1
(1) .EQUIV SW09,SW9
(1) .EQUIV SW08,SW8
(1) .EQUIV SW07,SW7
(1) .EQUIV SW06,SW6
(1) .EQUIV SW05,SW5
(1) .EQUIV SW04,SW4
(1) .EQUIV SW03,SW3
(1) .EQUIV SW02,SW2
(1) .EQUIV SW01,SW1
(1) .EQUIV SW00,SW0
(1)
(1) ;*DATA BIT DEFINITIONS (BIT00 TO BIT15)
(1) 100000 BIT15= 100000
(1) 040000 BIT14= 40000
(1) 020000 BIT13= 20000
(1) 010000 BIT12= 10000
(1) 004000 BIT11= 4000
(1) 002000 BIT10= 2000
(1) 001000 BIT09= 1000
(1) 000400 BIT08= 400
(1) 000200 BIT07= 200
(1) 000100 BIT06= 100
(1) 000040 BIT05= 40
(1) 000020 BIT04= 20
(1) 000010 BIT03= 10
(1) 000004 BIT02= 4
(1) 000002 BIT01= 2
(1) 000001 BIT00= 1
(1) .EQUIV BIT09,BIT9
(1) .EQUIV BIT08,BIT8
(1) .EQUIV BIT07,BIT7
(1) .EQUIV BIT06,BIT6
(1) .EQUIV BIT05,BIT5
(1) .EQUIV BIT04,BIT4
(1) .EQUIV BIT03,BIT3
(1) .EQUIV BIT02,BIT2
(1) .EQUIV BIT01,BIT1
(1) .EQUIV BIT00,BIT0
(1)
(1) ;*BASIC 'CPU' TRAP VECTOR ADDRESSES
```

```

(1) 000004 ERRVEC= 4 ;;TIME OUT AND OTHER ERRORS
(1) 000010 RESVEC= 10 ;;RESERVED AND ILLEGAL INSTRUCTIONS
(1) 000014 TBITVEC=14 ;;'T' BIT
(1) 000014 TRTVEC= 14 ;;TRACE TRAP
(1) 000014 BPTVEC= 14 ;;BREAKPOINT TRAP (BPT)
(1) 000020 IOTVEC= 20 ;;INPUT/OUTPUT TRAP (IOT) **SCOPE**
(1) 000024 PWRVEC= 24 ;;POWER FAIL
(1) 000030 EMTVEC= 30 ;;EMULATOR TRAP (EMT) **ERROR**
(1) 000034 TRAPVEC=34 ;;'TRAP' TRAP
(1) 000060 TKVEC= 60 ;;TTY KEYBOARD VECTOR
(1) 000064 TPVEC= 64 ;;TTY PRINTER VECTOR
(1) 000240 PIRQVEC=240 ;;PROGRAM INTERRUPT REQUEST VECTOR
22 .SBTTL OPERATIONAL SWITCH SETTINGS
(1) *
(1) * SWITCH USE
(1) * -----
(1) * 15 HALT ON ERROR
(1) * 14 LOOP ON TEST
(1) * 13 INHIBIT ERROR TYPEOUTS
(1) * 12 INHIBIT SIZING # OF MNCAD'S
(1) * 11 INHIBIT ITERATIONS
(1) * 10 HALT FOR VIEWING BIT MAP TERMINAL DISPLAY
(1) * 9 LOOP ON ERROR
(1) * 8 LOOP ON TEST IN SWR<7:0>
23 171000 ABASE= 171000
24 000400 AVECT1= 400
25
26 000100 .=100
27 000100 000104 000200 000002 .WORD 104,200,2
28
29 .SBTTL TRAP CATCHER
30
31 000000 .=0
32 ;*ALL UNUSED LOCATIONS FROM 4-776 CONTAIN A '+2'
33 ;*AND 'JSR PC,R0' SEQUENCE TO CATCH ILLEGAL INTERRUPTS.
34 ;*AND INTERRUPTS TO THE WRONG VECTOR.
35 ;*LOCATION 0 CONTAINS A 0 TO CATCH IMPROPERLY LOADED
36 ;*VECTORS.
46 000004 .=4
47 000004 016612 000200 .WORD IOTRD,200 ;HANDLE BUSS ERROR.
48 000174 000174 .=174
49 000174 000000 DI:PREG: .WORD 0 ;;SOFTWARE DISPLAY REGISTER.
50 000176 000000 SWREG: .WORD 0 ;;SOFTWARE SWITCH REGISTER.
51
52 000200 000137 001556 JMP BEGIN ;START ADDRESS
53 000204 000137 001620 JMP @#RBEG2 ;RESTART ADDRESS
54 000210 000137 001626 JMP @#BEGIN2 ;START ADDRESS FOR OPTION TESTER

```



```
56 .SBTTL ACT11 HOOKS
(1)
(2) ::*****
(1) :HOOKS REQUIRED BY ACT11
(1) 000214 $SVPC=. ;SAVE PC
(1) 000046 .=46
(1) 000046 016500 $ENDAD ;;1)SET LOC.46 TO ADDRESS OF $ENDAD IN .$EOP
(1) 000052 000052 .=52
(1) 000052 000000 .WORD 0 ;;2)SET LOC.52 TO ZERO
(1) 000214 .=$SVPC ;; RESTORE PC
57 .=1000
58 .SBTTL APT PARAMETER BLOCK
(1)
(2) ::*****
(1) :SET LOCATIONS 24 AND 44 AS REQUIRED FOR APT
(2) ::*****
(1) 001000 .SX=. ;;SAVE CURRENT LOCATION
(1) 000024 .=24 ;;SET POWER FAIL TO POINT TO START OF PROGRAM
(1) 000024 000200 200 ;;FOR APT START UP
(1) 000044 .=44 ;;POINT TO APT INDIRECT ADDRESS PNTR.
(1) 000044 001000 $APTHDR ;;POINT TO APT HEADER BLOCK
(1) 001000 .=$X ;;RESET LOCATION COUNTER
(2) ::*****
(1) :SETUP APT PARAMETER BLOCK AS DEFINED IN THE APT-PDP11 DIAGNOSTIC
(1) :INTERFACE SPEC.
(1)
(1) 001000 $APTHD:
(1) 001000 000000 $HIBTS: .WORD 0 ;;TWO HIGH BITS OF 18 BIT MAILBOX ADDR.
(1) 001002 001170 $MBADR: .WORD $MAIL ;;ADDRESS OF APT MAILBOX (BITS 0-15)
(1) 001004 002260 $TSTM: .WORD 1200. ;;RUN TIM OF LONGEST TEST
(1) 001006 000764 $PASTM: .WORD 500. ;;RUN TIME IN SECS. OF 1ST PASS ON 1 UNIT (QUICK VERIFY)
(1) 001010 003244 $UNITM: .WORD 1700. ;;ADDITIONAL RUN TIME (SECS) OF A PASS FOR EACH ADDITIONAL UNIT
(1) 001012 000031 .WORD $ETEND-$MAIL/2 ;;LENGTH MAILBOX-ETABLE(WORDS)
```


(2)	001204	000000	\$MSGAD:	.WORD	AMSGAD	::MESSAGE ADDRESS
(2)	001206	000000	\$MSGLG:	.WORD	AMSGLG	::MESSAGE LENGTH
(2)	001210		\$ETABLE:			::APT ENVIRONMENT TABLE
(2)	001210	000	\$ENV:	.BYTE	AENV	::ENVIRONMENT BYTE
(2)	001211	000	\$ENVM:	.BYTE	AENVM	::ENVIRONMENT MODE BITS
(2)	001212	000000	\$SWREG:	.WORD	ASWREG	::APT SWITCH REGISTER
(2)	001214	000000	\$USWR:	.WORD	AUSWR	::USER SWITCHES
(2)	001216	000000	\$CPUOP:	.WORD	ACPUOP	::CPU TYPE,OPTIONS
(2)			:*			BITS 15-11=CPU TYPE
(2)			:*			11/04=01,11/05=02,11/20=03,11/40=04,11/45=05
(2)			:*			11/70=06,PDQ=07,Q=10
(2)			:*			BIT 10=REAL TIME CLOCK
(2)			:*			BIT 9=FLOATING POINT PROCESSOR
(2)			:*			BIT 8=MEMORY MANAGEMENT
(2)	001220	000	\$MAMS1:	.BYTE	AMAMS1	::HIGH ADDRESS,M.S. BYTE
(2)	001221	000	\$MTYP1:	.BYTE	AMTYP1	::MEM. TYPE,BLK#1
(2)			:*			MEM.TYPE BYTE -- (HIGH BYTE)
(2)			:*			900 NSEC CORE=001
(2)			:*			300 NSEC BIPOLAR=002
(2)			:*			500 NSEC MOS=003
(2)	001222	000000	\$MADR1:	.WORD	AMADR1	::HIGH ADDRESS,BLK#1
(2)			:*			MEM.LAST ADDR.=3 BYTES,THIS WORD AND LOW OF 'TYPE' ABOVE
(2)	001224	000	\$MAMS2:	.BYTE	AMAMS2	::HIGH ADDRESS,M.S. BYTE
(2)	001225	000	\$MTYP2:	.BYTE	AMTYP2	::MEM. TYPE,BLK#2
(2)	001226	000000	\$MADR2:	.WORD	AMADR2	::MEM.LAST ADDRESS,BLK#2
(2)	001230	000	\$MAMS3:	.BYTE	AMAMS3	::HIGH ADDRESS,M.S.BYTE
(2)	001231	000	\$MTYP3:	.BYTE	AMTYP3	::MEM. TYPE,BLK#3
(2)	001232	000000	\$MADR3:	.WORD	AMADR3	::MEM.LAST ADDRESS,BLK#3
(2)	001234	000	\$MAMS4:	.BYTE	AMAMS4	::HIGH ADDRESS,M.S.BYTE
(2)	001235	000	\$MTYP4:	.BYTE	AMTYP4	::MEM. TYPE,BLK#4
(2)	001236	000000	\$MADR4:	.WORD	AMADR4	::MEM.LAST ADDRESS,BLK#4
(2)	001240	000400	\$VECT1:	.WORD	AVECT1	::INTERRUPT VECTOR#1,BUS PRIORITY#1
(2)	001242	000000	\$VECT2:	.WORD	AVECT2	::INTERRUPT VECTOR#2BUS PRIORITY#2
(2)	001244	171000	\$BASE:	.WORD	ABASE	::BASE ADDRESS OF EQUIPMENT UNDER TEST
(2)	001246	000000	\$DEVN:	.WORD	ADEVN	::DEVICE MAP
(2)	001250	000000	\$CDW1:	.WORD	ACDW1	::CONTROLLER DESCRIPTION WORD#1
(2)	001252		\$ETEND:			
(2)			.MEXIT			

```

(1) .SBTTL ERROR POINTER TABLE
(1)
(1) ;*THIS TABLE CONTAINS THE INFORMATION FOR EACH ERROR THAT CAN OCCUR.
(1) ;*THE INFORMATION IS OBTAINED BY USING THE INDEX NUMBER FOUND IN
(1) ;*LOCATION $ITEMB. THIS NUMBER INDICATES WHICH ITEM IN THE TABLE IS PERTINENT.
(1) ;*NOTE1: IF $ITEMB IS 0 THE ONLY PERTINENT DATA IS ($ERRPC).
(1) ;*NOTE2: EACH ITEM IN THE TABLE CONTAINS 4 POINTERS EXPLAINED AS FOLLOWS:
(1)
(1) ;* EM ;:POINTS TO THE ERROR MESSAGE
(1) ;* DH ;:POINTS TO THE DATA HEADER
(1) ;* DT ;:POINTS TO THE DATA
(1) ;* DF ;:POINTS TO THE DATA FORMAT
(1)
(1) $ERRTB:
(1)
61
62
63
72 ;ITEM 1
73 001252 023733 EM1 ;MNCAD STATUS REG. ERROR
74 001254 024312 DH1 ;ERRPC STREG EXPECTED ACTUAL
75 001256 024552 DT1 ;$ERRPC, STREG, $GDDAT, $BDDAT
76 001260 024642 DF1
77
78 ;ITEM 2
79
80 001262 023771 EM2 ;MNCAD FAILED TO INTERRUPT
81 001264 024442 DH3 ;ERRPC STREG ACTUAL
82 001266 024606 DT3 ;$ERRPC, STREG, $BDDAT
83 001270 024642 DF1
84
85 ;ITEM 3
86 001272 024031 EM3 ;MNCAD UNEXPECTED INTERRUPT
87 001274 024442 DH3 ;ERRPC STREG
88 001276 024606 DT3 ;$ERRPC, STREG
89 001300 024642 DF1
90
91 ;ITEM 4
92 001302 024072 EM4 ;MNCAD ERROR ON A/D CHANNEL
93 001304 024356 DH2 ;ERRPC STREG CHAN NOMINAL TOL ACTUAL
94 001306 024566 DT2 ;$ERRPC, STREG, CHANL, $GDDAT, SPREAD, $BDDAT
95 001310 024642 DF1
96
97 ;ITEM 5
98 001312 024133 024476 024620 EM5, DH5, DT5, DF1 ;EXISTING MNCAD NOW FAILS TO RESPOND
99 001320 024642
100
101 ;ITEM 6
101 001322 024214 024522 024632 EM6, DH6, DT6, DF1 ;BUS ERROR ON SPECIFIED DEFAULT ADDRESS
101 001330 024642

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C
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103				
104	001332	171000	MNCAD0: ABASE	:ADDRESS OF MNCAD #0
105	001334	000400	AVECT1	:VECTOR OF MNCAD #0
106	001336	171004	ABASE+4	: #1
107	001340	000410	AVECT1+10	: #1
108	001342	171010	ABASE+10	: #2
109	001344	000460	AVECT1+60	: #2
110	001346	171014	ABASE+14	: #3
111	001350	000470	AVECT1+70	: #3
112				
113			.SBTTL MISCELLANEOUS, TEMPORARY, AND STORAGE LOCATIONS	
114	001352	171000	STREG: ABASE	:ADDRESS OF STATUS REGISTER
115	001354	171001	ADST1: ABASE+1	:UPPER BYTE OF STATUS REG.
116	001356	171002	ADBUFF: ABASE+2	:ADDRESS OF A/D BUFFER
117	001360	000400	VECTOR: AVECT1	:VECTOR ADDRESS
118	001362	000402	VECTR1: AVECT1+2	
119	001364	000404	VECTR2: AVECT1+4	:ERROR VECTOR ADDRESS
120	001366	000406	VECTR3: AVECT1+6	
121	001370	000000	BASECH: 0	:BASE CHANNEL
122	001372	000000	BASEND: 0	:END CHANNEL
123	001374	000060	KBVECT: 60	
124	001376	170400	GSTREG: 170400	:KNOWN GOOD A/D CSR
125	001400	170402	GADBUF: 170402	:KNOWN GOOD A/D DBR
126	001402	000410	GVECT: 410	:KNOWN GOOD A/D VECTOR
127	001404	170430	CLKCSR: 170430	:CLOCK CSR
128	001406	170432	CLKBPR: 170432	:CLOCK BPR
129	001410	167770	DRVCSR: 167770	:DRV11 CSR
130	001412	167772	DRVDOR: 167772	:DRV11 DOR
131	001414	167774	DRVDIR: 167774	:DRV11 DIR
132	001416	000000	WIDE: 0	:NO. OF WIDE STATES
133	001420	000000	NARROW: 0	:NO. OF NARROW STATES
134	001422	000000	FIRST: 0	
135	001424	000000	SKIPST: 0	:NO. OF SKIPPED STATES
136	001426	000000	TEMP: 0	:WORK AREA
137	001430	000000	TEMP1: 0	:RESTART INDICATOR
138	001432	000000	CH1: 0	:FIRST CHANNEL
139	001434	000000	CH2: 0	:SECOND CHANNEL
140	001436	000000	NBEXT: 0	:NO. OF MNCAD'S TO BE TESTED
141	001440	000000	NMBEXT: 0	:NO. OF MNCAD'S TO BE TESTED
142	001442	000000	DUMMY: 0	:DUMMY CHANNEL
143	001444	000000	CHANL: 0	:CHANNEL VALUE
144	001446	000000	RNA: 0	:RANDOM
145	001450	000000	RNB: 0	:NUMBER
146	001452	000000	RNC: 0	:VALUES
147	001454	000000	RMS: 0	:RMS NOISE VALUE
148	001456	000000	PEAK: 0	:PEAK NOISE VALUE
149	001460	000000	FLAG: 0	:BIT MAP TERMINAL FLAG
150	001462	000000	SPREAD: 0	:DEVIATION FROM THE NOMINAL
151	001464	000000	DAC: 0	:SAR VALUE
152	001466	000000	DELAY: 0	:TIME DELAY COUNTER
153	001470	000000	EDGE: 0	:EDGE VALUE
154	001472	000000	BITPNT: 0	
155	001474	000000	MIN: 0	:MIN VALUE
156	001476	000000	WFTTEST: 0	:0= NO DWARF ;BIT15 =1 TESTER; NON-ZERO = DWARF

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157 001500 000000      MAX: 0      ;MAX VALUE
158 001502 000000      PERCNT: 0    ;PERCENT FOR SAR ROUTINE
159 001504 000000      OUT: 0
160 001506 000000      EVER: 0
161 001510 000000      BADUNT: 0    ;BAD UNIT MAP
162 001512 000001      MASKNM: 1    ;CURRENT UNIT MAP
163 001514 000000      UNITBD: 0
164
165 001516      UNEXP:
(1) 001516 012737 001532 001162      MOV #1$, $ESCAPE    ;;ESCAPE TO 1$ ON ERROR
166 001524 005237 001103      INC $ERFLG
167 001530 104003      ERROR 3
168 001532 005037 001162      1$: CLR $ESCAPE    ;RETURN ESCAPE TO NORMAL
169 001536 000002      RTI    ;UNEXPECTED INTERRUPT
170 001540 022776 000001 000000      RETURN: CMP #1, @0(SP) ;DOES IT RETURN TO A WAIT?
171 001546 001002      BNE RET2    ;NO
172 001550 062716 000002      RET1: ADD #2, (SP)  ;BUMP RETURN ADDRESS
173 001554 000002      RET2: RTI
174
175
176      .SBTTL INITIAL START-UP, HOUSEKEEPING, AND DIALOGUE
177 001556 005037 001476      BEGIN: CLR WFTST
178 001562 000424      BR RBEG
179 001564 012700 000200      BEG2: MOV #200, R0    ;LOAD STARTING ADDRESS POINTER
180 001570 012720 000137      MOV #137, (R0)+    ;LOAD JUMP
181 001574 012720 001556      MOV #BEGIN, (R0)+
182 001600 012720 000137      MOV #137, (R0)+
183 001604 012720 001620      MOV #RBEG2, (R0)+
184 001610 012720 000137      MOV #137, (R0)+
185 001614 012720 001626      MOV #BEGIN2, (R0)+
186 001620 005237 001430      RBEG2: INC TEMP1    ;SET RESTART FLAG
187 001624 000405      BR RBEG1
188 001626 012737 100000 001476      BEGIN2: MOV #BIT15, WFTST ;INDICATE TESTER IS CONNECTED
189 001634 005037 001430      RBEG: CLR TEMP1    ;CLEAR RESTAT FLAG
190 001640 000005      RBEG1: RESET
191
(1)      .SBTTL INITIALIZE THE COMMON TAGS
(1)      ;;CLEAR THE COMMON TAGS ($CMTAG) AREA
(1) 001642 012706 001100      MOV # $CMTAG, R6    ;;FIRST LOCATION TO BE CLEARED
(1) 001646 005026      CLR (R6)+    ;;CLEAR MEMORY LOCATION
(1) 001650 022706 001140      CMP #SWR, R6    ;;DONE?
(1) 001654 001374      BNE -6    ;;LOOP BACK IF NO
(1) 001656 012706 001100      MOV #STACK, SP    ;;SETUP THE STACK POINTER
(1)      ;;INITIALIZE A FEW VECTORS
(1) 001662 012737 026304 000020      MOV # $SCOPE, @#IOTVEC ;;IOT VECTOR FOR SCOPE ROUTINE
(1) 001670 012737 000340 000022      MOV #340, @#IOTVEC+2 ;;LEVEL 7
(1) 001676 012737 026626 000030      MOV # $ERROR, @#EMTVEC ;;EMT VECTOR FOR ERROR ROUTINE
(1) 001704 012737 000340 000032      MOV #340, @#EMTVEC+2 ;;LEVEL 7
(1) 001712 012737 030666 000034      MOV # $TRAP, @#TRAPVEC ;;TRAP VECTOR FOR TRAP CALLS
(1) 001720 012737 000340 000036      MOV #340, @#TRAPVEC+2 ;LEVEL 7
(1) 001726 012737 027172 000024      MOV # $PWRDN, @#PWRVEC ;;POWER FAILURE VECTOR
(1) 001734 012737 000340 000026      MOV #340, @#PWRVEC+2 ;;LEVEL 7
(1) 001742 013737 016446 016440      MOV $ENDCT, $EOPCT ;;SETUP END-OF-PROGRAM COUNTER
(1) 001750 005037 001160      CLR $TIMES    ;;INITIALIZE NUMBER OF ITERATIONS
(1) 001754 005037 001162      CLR $ESCAPE    ;;CLEAR THE ESCAPE ON ERROR ADDRESS

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(1) 001760 112737 000001 001115      MOVB    #1,$ERMAX      ;;ALLOW ONE ERROR PER TEST
(1) 001766 012737 001766 001106      MOV     #,$SLP:ADR     ;;INITIALIZE THE LOOP ADDRESS FOR SCOPE
(1) 001774 012737 00 774 001110      MOV     #,$SLPERR     ;;SETUP THE ERROR LOOP ADDRESS
(2)                                     ;;SIZE FOR A HARDWARE SWITCH REGISTER. IF NOT FOUND OR IT IS
(2)                                     ;;EQUAL TO A '-1', SETUP FOR A SOFTWARE SWITCH REGISTER.
(2) 002002 013746 000004                MOV     @WERRVEC,-(SP)  ;;SAVE ERROR VECTOR
(2) 002006 012737 002042 000004      MOV     #64$,@WERRVEC  ;;SET UP ERROR VECTOR
(2) 002014 012737 177570 001140      MOV     #DSWR,SWR     ;;SETUP FOR A HARDWARE SWICH REGISTER
(2) 002022 012737 177570 001142      MOV     #DDISP,DISPLAY ;;AND A HARDWARE DISPLAY REGISTER
(2) 002030 022777 177777 177102      CMP     #-1,@SWR      ;;TRY TO REFERENCE HARDWARE SWR
(2) 002036 001012                        BNE     66$           ;;BRANCH IF NO TIMEOUT TRAP OCCURRED
(2)                                     ;;AND THE HARDWARE SWR IS NOT = -1
(2) 002040 000403                        BR      65$           ;;BRANCH IF NO TIMEOUT
(2) 002042 012716 002050 64$:        MOV     #65$, (SP)    ;;SET UP FOR TRAP RETURN
(2) 002046 000002                        RTI
(2) 002050 012737 000176 001140 65$:  MOV     #SWREG,SWR    ;;POINT TO SOFTWARE SWR
(2) 002056 012737 000174 001142      MOV     #DISPREG,DISPLAY
(2) 002064 012637 000004 66$:        MOV     (SP)+,@WERRVEC ;;RESTORE ERROR VECTOR
(1)
(2) 002070 005037 001176                CLR     $PASS         ;;CLEAR PASS COUNT
(2) 002074 132737 000200 001211      BITB   #APTSIZE,$ENVM ;;TEST USER SIZE UNDER APT
(2) 002102 001403                        BEQ     67$           ;;YES,USE NON-APT SWITCH
(2) 002104 012737 001212 001140      MOV     #SSWREG,SWR   ;;NO,USE APT SWITCH REGISTER
(2) 002112
192                                     67$:
;ROUTINE TO OVERLAY THE '$TYPE' ROUTINE
193 002112 012737 005046 027410      MOV     #5046,$TYPE   ;CLR -(SP)
194 002120 012737 012746 027412      MOV     #12746,$TYPE+2 ;MOV #TYPE+12,-(SP)
195 002126 012737 027422 027414      MOV     #TYPE+12,$TYPE+4
196 002134 012737 000002 027416      MOV     #RTI,$TYPE+6  ;RTI
197 002142 004737 024720                JSR     PC,$TKINT     ;ENABLE TKB INTR.
198 002146 005737 001430                TST     TEMP1        ;TEST IF RESTART
199 002152 001005                        BNE     20$           ;BR IF YES
200 002154 005737 000042                TST     @#42         ;TEST IF CHAIN MODE
201 002160 001002                        BNE     20$           ;BR IF CHAIN MODE
202 002162 104401 023075                TYPE   ,INITVT       ;INITILIZE THE TERMINAL
203 002166
(1)                                     20$:
;SBTTL TYPE PROGRAM NAME
(1)                                     ;;TYPE THE NAME OF THE PROGRAM IF FIRST PASS
(1) 002166 005227 177777                INC     #-1           ;;FIRST TIME?
(1) 002172 001050                        BNE     68$           ;;BRANCH IF NO
(1) 002174 022737 016500 000042      CMP     #SENDAD,@#42  ;;ACT-11?
(1) 002202 001444                        BEQ     68$           ;;BRANCH IF YES
(1) 002204 104401 002252                TYPE   ,69$          ;;TYPE ASCIZ STRING
(2)                                     .SBTTL GET VALUE FOR SOFTWARE SWITCH REGISTER
(2) 002210 005737 000042                TST     @#42         ;;ARE WE RUNNING UNDER XXDP/ACT?
(2) 002214 001012                        BNE     70$           ;;BRANCH IF YES
(2) 002216 123727 001210 000001      CMPB   $ENV,#1       ;;ARE WE RUNNING UNDER APT?
(2) 002224 001406                        BEQ     70$           ;;BRANCH IF YES
(2) 002226 023727 001140 000176      CMP     SWR,#SWREG   ;;SOFTWARE SWITCH REG SELECTED?
(2) 002234 001005                        BNE     71$           ;;BRANCH IF NO
(2) 002236 104407                        GTSWR                ;;GET SOFT-SWR SETTINGS
(2) 002240 000403                        BR      71$
(2) 002242 112737 000001 001134 70$:  MOVB   #1,$AUTOB     ;;SET AUTO-MODE INDICATOR
(2) 002250 71$:

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(1) 002250 000421 BR 68$ ::GET OVER THE ASCIZ
(1) ::69$: .ASCIZ <CRLF>#CVMNA-A MNCAD (A/D) DIAGNOSTIC#<CRLF>
(1) 002314 68$:
204 002314 013746 000010 MOV @#RESVEC,-(SP) ;SAVE RESERVED VECTOR
205 002320 012737 002360 000010 MOV #1$,RESVEC ;SET UP ILLEGAL INST. TRAP
206 002326 012700 000001 MOV #1,RO ;SET RO TO ONE
207 002332 077001 SOB RO ;TRY SOB INSTRUCTION
208 002334 012737 077001 013622 MOV #77001,DELAY1 ;SET UP FOR SOB
209 002342 012737 077001 013736 MOV #77001,DELAY2 ;
210 002350 012737 077001 014052 MOV #77001,DELAY3 ;
211 002356 000412 BR 2$
212 002360 022626 1$: CMP (SP)+,(SP)+ ;POP TWO WORDS OFF STACK
213 002362 012737 104420 013622 MOV #DELY,DELAY1 ;INSTRUCTION FAILED
214 002370 012737 104420 013736 MOV #DELY,DELAY2 ;
215 002376 012737 104420 014052 MOV #DELY,DELAY3 ;
216 002404 012637 000010 2$: MOV (SP)+,@#RESVEC ;RESTORE ERROR VECTOR
217 002410 004737 012510 3$: JSR PC,FXONE ;INITIALIZE ADDRESSES
218 002414 012737 062341 001446 MOV #62341,RNA ;RANDOM NO, VARIABLES
219 002422 012737 142315 001450 MOV #142315,RNB
220 002430 012737 127623 001452 MOV #127623,RNC
221 002436 004737 016266 JSR PC,WFADJ ;SET UP TOLLERANCES
222 002442 105737 001134 TSTB $AUTOB ;TEST IF CHAIN/APT
223 002446 001402 BEQ 4$
224 002450 000137 010776 JMP BEGL ;GO TO LOGIC TESTS
225 002454 005737 001430 4$: TST TEMP1 ;TEST IF RESTART
226 002460 001026 BNE MTEST1
227 002462 005737 001476 TST WFTST ;CHECK IF TESTER CONNECTED ?
228 002466 100421 BMI MTESTO ;BR IF YES
229
230 .SBTTL OPERATOR INPUT DECODER
231 002470 104401 001165 MTEST: TYPE ,SCLF
232 002474 104401 017745 TYPE ,YESNO ;ASK FOR INPUT
233 002500 104401 017267 TYPE ,DWRF ;ABOUT DWARF MODULE
234 002504 104412 RDLIN
235 002506 012600 MOV (SP)+,RO ;GET INPUT
236 002510 105037 001476 CLRB WFTST ;SET NO DWARF
237 002514 042710 000040 BIC #40,(RO) ;ENSURE UPPER CASE
238 002520 122710 000131 CMPB #'Y,(RO) ;TEST IF 1ST CHAR IS Y
239 002524 001002 BNE MTESTO ;BR IF NOT 'Y'
240 002526 105237 001476 INCB WFTST ;SET DWARF CONNECTED FLAG
241 002532 104401 023127 MTESTO: TYPE ,PRIME1 ;TELL THE OPERATOR THE STORY
242 002536 000005 MTEST1: RESET
243 002540 052777 000100 176376 BIS #BIT6,@$TKS ;ENABLE TKB INTR.
244 002546 005046 CLR -(SP)
245 002550 012746 002556 MOV #1$,-(SP)
246 002554 000002 RTI ;LOWER PS
247 002556 005037 001176 1$: CLR $PASS ;INIT
248 002562 005037 001112 CLR $ERTL ;
249 002566 005037 001506 CLR EVER ; THINGS
250 002572 104401 023643 TYPE ,DOT ;TYPE THE 'DOT'
251 002576 104412 RDLIN
252 002600 012600 MOV (SP)+,RO ;READ ANSWER
253 002602 142710 000040 BICB #40,(RO)
254 002606 121027 000101 CMPB (RO),#'A ;IS IT A?

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255 002612 001002      BNE      2$      ;;NO, TRY C
256 002614 000137 011040  JMP      BEGINA  ;GO TO AUTO TEST
257 002620 121027 000103  2$: CMPB   (R0),#'C  ;IS IT C?
258 002624 001002      BNE      3$      ;;NO, TRY P
259 002626 000137 010122  JMP      BEGINC  ;GO TO CALIBRATION LOOP
260 002632 121027 000120  3$: CMPB   (R0),#'P  ;IS IT P?
261 002636 001002      BNE      4$      ;;NO, TRY L
262 002640 000137 010614  JMP      BEGINP  ;GO TO TYPE/DISPLAY CONVERSIONS TEST
263 002644 121027 000114  4$: CMPB   (R0),#'L  ;IS IT L?
264 002650 001002      BNE      5$      ;;NO, TRY M
265 002652 000137 010776  JMP      BEGL    ;GO TO LOGIC TESTS
266 002656 121027 000127  5$: CMPB   (R0),#'W  ;IS IT W?
267 002662 001002      BNE      6$      ;;NO, TRY AGAIN
268 002664 000137 011116  JMP      BEGINW  ;GO TO WRAPAROUND TEST
269 002670 121027 000102  6$: CMPB   (R0),#'B  ;IS IT B?
270 002674 001002      BNE      7$      ;;NO TRY AGAIN
271 002676 000137 012306  JMP      BASEXC  ;GO CHANGE BASE AND VECTOR ADDRESS
272 002702 121027 000110  7$: CMPB   (R0),#'H  ;IF IT H?
273 002706 001670      BEQ      MTEST   ;YES, HELP THE OPERATOR
274 002710 121027 000107  10$: CMPB   (R0),#'G  ;IS IT G
275 002714 001002      BNE      11$     ;;NO, TRY AGAIN
276 002716 104407      GTSWR
277 002720 000706      BR       MTEST1
278 002722 121027 000126  11$: CMPB   (R0),#'V  ;IS IT V?
279 002726 001004      BNE      12$     ;;NO, TRY AGAIN
280 002730 005237 001460  INC      FLAG    ;SET BIT MAP AVAILABLE FLAG
281 002734 000137 011116  JMP      BEGINW  ;AND RUN WRAP TEST'S
282 002740 121027 000116  12$: CMPB   (R0),#'N  ;IS IT N?
283 002744 001002      BNE      13$     ;;NO, TRY AGAIN
284 002746 000137 011156  JMP      BEGINW  ;RUN NOISE TESTS
285 002752 121027 000106  13$: CMPB   (R0),#'F  ;IS IT F
286 002756 001002      BNE      77$     ;;NO, TRY AGAIN
287 002760 000137 010376  JMP      BEGINM  ;RUN SWITCH GAIN/PREAMP MANUAL TEST
288 002764 104401 017345  77$: TYPE   ,QUEST
289 002770 000662      BR       MTEST1  ;WAIT FOR CHARACTER
290      .SBTTL  DETERMINE THE NUMBER OF MNCAD'S ON THE SYSTEM
291 002772 013737 001244 001126  TESTAD: MOV    $BASE,$BDDAT ;GET BASE ADDRESS
292 003000 005037 001202      CLR    $UNIT    ;CLR UNIT NUMBER
293 003004 012737 003060 000004  MOV    #2$,ERRVEC ;LOAD RETURN ADDRESS
294 003012 005777 176110      TST   @ $BDDAT  ;TEST IF ADDRESS EXISTS
295 003016 062737 000004 001126  ADD    #4,$BDDAT ;UPDATE BUS ADDRESS
296 003024 005237 001202      INC   $UNIT    ;UPDATE UNIT COUNT
297 003030 005737 001210      TST   $ENV     ;TEST IF 'DO NOT SIZE'
298 003034 100424      BMI   3$      ;BR IF NO SIZEING
299 003036 032777 010000 176074  BIT    #SW12,@SWR ;TEST IF INHIBIT SIZING IS SET
300 003044 001020      BNE   3$      ;BR IF SET
301 003046 022737 000004 001202  CMP    #4,,$UNIT ;TEST IF MAX NUMBER
302 003054 001356      BNE   1$      ;BR IF NOT
303 003056 000413      BR    3$      ;BR IF MAX
304 003060 022626      CMP   (SP)+,(SP)+ ;RESTORE STACK
305 003062 005737 001202      TST   $UNIT    ;TEST IF ANY EXIST
306 003066 001007      BNE   3$      ;BR IF ANY ARE THERE
307 003070 005737 000042      TST   @#42     ;TEST IF XXDP CHAIN MODE
308 003074 001004      BNE   3$      ;BR IF YES

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309 003076 104006          ERROR 6          ;BASE ADDRESS CAUSED A BUS TRAP
310 003100 005726          TST      (SP)+      ;POP 1 ARG.
311 003102 000137 016412    JMP      $EOP
312 003106 012737 016612 000004 3$:  MOV      #IOTRD,ERRVEC
313 003114 012737 000200 000006    MOV      #200,ERRVEC+2
314 003122 005737 001506          TST      EVER
315 003126 100427          BMI      4$
316 003130 005737 001476          TST      WFTST
317 003134 100415          BMI      7$
318 003136 104401 022074          TYPE     ,FOUND1
319 003142 013746 001202          MOV      $UNIT,-(SP)
320 003146 104405          TYPDS
321 003150 104401 022117          TYPE     ,FOUND2
322 003154 005737 001202          TST      $UNIT
323 003160 001003          BNE      7$
324 003162 005726          TST      (SP)+
325 003164 000137 016412    JMP      $EOP
326 003170 013737 001202 001506 7$:  MOV      $UNIT,EVER
327 003176 052737 100000 001506    BIS      #BIT15,EVER
328 003204 000410          BR       5$
329 003206 123737 001506 001202 4$:  CMPB    EVER,$UNIT
330 003214 001404          BEQ      5$
331 003216 113737 001506 001426    MOVB    EVER,TEMP
332 003224 104005          ERROR   5
333 003226 005037 001202          CLR     $UNIT
334 003232 113737 001506 001440    MOVB    EVER,NMBEXT
335 003240 005337 001440          DEC     NMBEXT
336 003244 004737 012510          JSR    PC,FIXONE
337 003250 005037 001510          CLR     BADUNT
338 003254 005046          CLR     -(SP)
339 003256 012746 003264          MOV     #6$,-(SP)
340 003262 000002          RTI
341 003264 000207          RTS     PC

```

```

;BASE ADDRESS CAUSED A BUS TRAP
;POP 1 ARG.
;TEST IF # HAS BEEN REPORTED
;IF YES BRANCH
;TEST IF IN TESTER MODE
;BR IF TESTER
;TELL OPERATOR # OF MNCAD'S FOUND
;PUT # TO BE TYPED ON STACK
;FINISH MESSAGE
;TEST IF ANY UNITS
;ANY UNIT
;POP 1 ARG. OFF STACK
;REPORT EOP
;SAVE THE # OF MNCAD'S FOR LATER
;SET 'REPORTED # FLAG'
;TEST IF ANY HAVE GONE AWAY
;BR IF ALL ARE STILL THERE
;SAVE FOR ERROR REPORT
;EXISTING DEVICE FAILED TO RESPOND
;RESET UNIT POINTER
;GET # OF UNITS
;ADJUST IT
;FIX BUS AND VECTOR ADDRESSES
;RESET BAD UNIT INDICATOR
;LOWER PRIORITY LEVEL 0
;EXIT

```

343 003266
344
(3)
(3)
(2) 003266 012737 003266 001106
(1) 003274 012737 000001 001160
345 003302 012737 000001 001102
346 003310 012737 003266 001110
347 003316 005737 001476
348 003322 100075
349 003324 005737 001176
350 003330 001072
351 003332 005046
352 003334 012746 003342
353 003340 000002
354 003342 104401 020424
355 003346 004537 015550
356 003352 000012
357 003354 013703 001426
358 003360 004737 015664
359 003364 104401 021014
360 003370 004537 015502
361 003374 006020
362 003376 016334
363 003400 000403
364 003402 104401 021132
365 003406 000406
366 003410 104401 021606
367 003414 004737 026564
368 003420 005237 001112
369
370
(3)
(3)
(2) 003424 000004
(1) 003426 012737 000001 001160
371 003434 104401 020433
372 003440 004537 015550
373 003444 000011
374 003446 013703 001426
375 003452 004737 015664
376 003456 104401 021014
377 003462 004537 015502
378 003466 001760
379 003470 016334
380 003472 000403
381 003474 104401 021132
382 003500 000406
383 003502 104401 021606
384 003506 004737 026564
385 003512 005237 001112
386

```
BEGINL:
:*****
:*TEST 1      +15 VOLT TEST (IN-HOUSE TESTER ONLY)
:*****
TST1:  MOV      #TST1,$LPADR
        MOV      #1,$TIMES      ;;DO 1 ITERATION
        MOV      #STN-1,$STNM    ;;SET UP TEST NUMBER
        MOV      #TST1,$LPERR
        TST      WFTEST          ;;IS PROGRAM RUNNING IN WESTFIELD MODE?
        BPL      TST3           ;;NO, SKIP FIRST 2 TESTS
        TST      $PASS          ;;DO FIRST 2 TESTS ON 1ST PASS ONLY
        BNE      TST3
        CLR      -(SP)          ;;RESET PRIORITY
        MOV      #1$,-(SP)
        RTI
1$:    TYPE      ,TP15           ;;TYPE '+15 = '
        JSR      R5,GCONVT      ;;CONVERT CHANNEL 12
        MOV      TEMP,R3        ;;GET TEMP
        JSR      PC,CONV15      ;;TYPE VOLTAGE
        TYPE      ,SPACE        ;;TYPE 4 SPACES
        JSR      R5,COMPAR      ;;TEST RESULTS
        BR       6020
        V100D
        BR       2$            ;;ERROR
        TYPE      ,OKMSG        ;;TYPE 'OK'
        BR       TST2          ;;GOTO NEXT TEST
2$:    TYPE      ,ERMSG         ;;TYPE '**ERROR**'
        JSR      PC,WHICHV      ;;INDICATE ERROR UNIT
        INC      $ERTTL        ;;UPDATE ERROR COUNT

:*****
:*TEST 2      -15 VOLT TEST (IN-HOUSE TESTER ONLY)
:*****
TST2:  SCOPE
        MOV      #1,$TIMES      ;;DO 1 ITERATION
        TYPE      ,TM15         ;;TYPE '-15 = '
        JSR      R5,GCONVT      ;;CONVERT CHANNEL 11
        MOV      TEMP,R3        ;;GET TEMP
        JSR      PC,CONV15      ;;TYPE VOLTAGE
        TYPE      ,SPACE        ;;TYPE 4 SPACES
        JSR      R5,COMPAR      ;;TEST RESULTS
        BR       1760
        V100D
        BR       1$            ;;ERROR
        TYPE      ,OKMSG        ;;TYPE 'OK'
        BR       TST3          ;;GOTO NEXT TEST
1$:    TYPE      ,ERMSG         ;;TYPE '**ERROR**'
        JSR      PC,WHICHV      ;;INDICATE BAD UNIT
        INC      $ERTTL        ;;UPDATE ERROR COUNT
```



```
423 ::*****  
(3) :*TEST 11 LOAD AND READ BACK ENABLE I.D. BIT3  
(3) :*****  
(2) 003714 000004 TST11: SCOPE  
424 003716 012737 000010 001124 MOV #BIT3,$GDDAT  
425 003724 104415 CHKIT  
426 003726 104001 ERROR 1 ;FAILED TO LOAD + READ ENABLE I.D. BIT  
427  
428 ::*****  
(3) :*TEST 12 LOAD AND READ BACK ERROR FLAG BIT15  
(3) :*****  
(2) 003730 000004 TST12: SCOPE  
429 003732 012737 100000 001124 MOV #BIT15,$GDDAT ;LOAD EXPECTED DATA  
430 003740 104415 CHKIT  
431 003742 104001 ERROR 1 ;FAILED TO LOAD + READ ERROR FLAG  
432 ::*****  
(3) :*TEST 13 TEST INIT CLEARS BITS 2-6,8-14  
(3) :*****  
(2) 003744 000004 TST13: SCOPE  
(1) 003746 012737 000300 001160 MOV #300,$TIMES ;;DO 300 ITERATIONS  
433 003754 005037 001124 CLR $GDDAT ;LOAD EXPECTED DATA  
434 003760 012777 077574 175364 2$: MOV #77574,@STREG ;SET STATUS REGISTER  
435 003766 000005 RESET ;INITIALIZE  
436 003770 052777 000100 175146 BIS #100,@$TKS ;SET INTRPT. ENABLE  
437 003776 104414 CHECK ;GO CHECK RESULTS  
438 004000 104001 ERROR 1 ;RESET FAILED TO CLEAR AD ST. REG. BITS
```

```
440      ::*****  
(3)      :*TEST 14      TEST INIT CLEARS ERROR FLAG  
(3)      ::*****  
(2) 004002 000004 TST14: SCOPE  
441 004004 012737 000300 001160      MOV      #300,$TIMES      ;DO 300 ITERATIONS  
442 004012 012777 100000 175332      MOV      #BIT15,@STREG      ;SET BIT 15  
443 004020 000005      RESET      ;ISSUE INIT  
444 004022 052777 000100 175114      BIS      #100,@$TKS      ;SET INTRPT. EN. FOR KEYBOARD  
445 004030 104414      CHECK  
446 004032 104001      ERROR      1  
447      ::*****  
(3)      :*TEST 15      TEST DONE FLAG SETS AND BIT0 CLEARS ON END OF CONV.  
(3)      ::*****  
(2) 004034 000004 TST15: SCOPE  
(1) 004036 012737 000100 001160      MOV      #100,$TIMES      ;;DO 100 ITERATIONS  
448 004044 012700 001000      MOV      #BIT9,R0      ;STALL TIME COUNTER  
449 004050 005277 175276      INC      @STREG      ;START CONVERSION  
450 004054 012737 000200 001124      MOV      #BIT7,$GDDAT      ;LOAD EXPECTED  
451 004062 005300 1$:      DEC      R0      ;STALL  
452 004064 001376      BNE      1$      ;TIME  
453 004066 042777 100000 175256      BIC      #BIT15,@STREG      ;MASK OUT ERROR BIT  
454 004074 104414      CHECK  
455 004076 104001      ERROR      1      ;A/D DONE FLAG FAILED TO SET;BIT0 FAILED TO CLEAR  
456 004100 017700 175252      MOV      @ADBUFF,R0      ;CLEAR DONE FLAG FOR ITERATIONS  
457      ::*****  
(3)      :*TEST 16      TEST INIT CLEARS DONE FLAG  
(3)      ::*****  
(2) 004104 000004 TST16: SCOPE  
(1) 004106 012737 000300 001160      MOV      #300,$TIMES      ;;DO 300 ITERATIONS  
458 004114 005037 001124      CLR      $GDDAT      ;CLEAR EXPECTED  
459 004120 005277 175226      INC      @STREG      ;START CONVERSION  
460 004124 105777 175222 2$:      TSTB      @STREG  
461 004130 100375      BPL      2$  
462 004132 000005      RESET  
463 004134 052777 000100 175002      BIS      #BIT6,@$TKS      ;ENABLE INTR.  
464 004142 104414      CHECK  
465 004144 104001      ERROR      1      ;DONE FLAG FAILED TO CLEAR  
466      ::*****  
467      :*TEST 17      TEST A/D DONE FLAG CLEARS WHEN READ CONVERTED VALUE  
(3)      ::*****  
(2) 004146 000004 TST17: SCOPE  
468 004150 005037 001124      CLR      $GDDAT      ;CLEAR EXPECTED  
469 004154 005277 175172      INC      @STREG      ;SET A/D START CONVERSION BIT  
470 004160 105777 175166 1$:      TSTB      @STREG      ;WAIT FOR FLAG  
471 004164 100375      BPL      1$  
472 004166 017700 175164      MOV      @ADBUFF,R0      ;READ CONVERTED VALUE  
473 004172 104414      CHECK  
474 004174 104001      ERROR      1      ;DONE FLAG FAILED TO CLEAR
```

```
476 ::*****  
(3) :*TEST 20 TEST ALL '0'S RESULTS USING MAINT. ADTST. BIT  
(3) :*****  
(2) 004176 000004 TST20: SCOPE  
477 004200 005037 001124 CLR $GDDAT ;CLEAR EXPECTED VALUE  
478 004204 005037 001444 CLR CHANL ;SET CHANL = 0  
479 004210 005037 001462 CLR SPREAD ;SET SPREAD = 0  
480 004214 012777 000005 175130 MOV #5,@STREG ;CONVERT EVEN CHANNEL WITH MAINT. BIT SET  
481 004222 105777 175124 1$: TSTB @STREG ;WAIT FOR DONE  
482 004226 100375 BPL 1$  
483 004230 017737 175122 001126 MOV @ADBUFF,$BDDAT ;RESULTS TO BDDAT FOR CHECKING  
484 004236 001401 BEQ TST21 ;GOTO NEXT TEST  
485 004240 104004 ERROR 4 ;DID NOT GET ALL '0'S RESULT WITH MAINT. ADTST  
486 :*****  
(3) :*TEST 21 TEST ALL '1'S RESULT USING MAINT. ADTST. BIT  
(3) :*****  
(2) 004242 000004 TST21: SCOPE  
487 004244 012737 007777 001124 MOV #7777,$GDDAT ;EXPECT ALL '1'S RESULT  
488 004252 012737 000001 001444 MOV #1,CHANL ;SET CHANL = 1  
489 004260 005037 001462 CLR SPREAD ;SET SPREAD = 0  
490 004264 012777 000405 175060 MOV #405,@STREG ;CONVERT ODD CHANNEL WITH MAINT. BIT SET  
491 004272 105777 175054 1$: TSTB @STREG ;WAIT FOR DONE  
492 004276 100375 BPL 1$  
493 004300 017737 175052 001126 MOV @ADBUFF,$BDDAT ;RESULTS TO BDDAT FOR CHECKING  
494 004306 023737 001124 001126 CMP $GDDAT,$BDDAT ;EQUAL?  
495 004314 001401 BEQ TST22 ;GOTO NEXT TEST  
496 004316 104004 ERROR 4 ;DID NOT GET ALL '1'S RESULT WITH MAINT. ADTST  
497 :*****  
(3) :*TEST 22 GENERATE INTERRUPT WHEN DONE FLAG SETS AFTER CONVERSION  
(3) :*****  
(2) 004320 000004 TST22: SCOPE  
(1) 004322 012737 000100 001160 MOV #100,$TIMES ;;DO 100 ITERATIONS  
498 004330 012737 004336 001106 MOV #10$,$LPADR ;LOAD RETURN ADDRESS  
499 004336 042777 000100 174600 10$: BIC #BIT6,@$TKS ;REMOVE TKB INTERRUPT  
500 004344 005046 CLR -(SP) ;RESET PRIORITY  
501 004346 012746 004354 MOV #1$,-(SP)  
502 004352 000002 RTI  
503 004354 004737 013166 1$: JSR PC,SETINT ;LOAD VECTOR AREA WITH TRAP CATCHER  
504 004360 012777 004442 174772 MOV #3$,@VECTOR ;INTERRUPT VECTOR ADDRESS  
505 004366 012777 000200 174766 MOV #200,@VECTR1 ;SET UP NEW PSW  
506 004374 012777 000101 174750 MOV #BIT6!BIT0,@STREG ;SET INTERRUPT ENABLE BIT + START CONVERSION  
507 004402 105777 174744 2$: TSTB @STREG ;WAIT FOR DONE  
508 004406 100375 BPL 2$ ;FLAG TO SET  
509 004410 017737 174736 001126 MOV @STREG,$BDDAT ;READ STATUS REGISTER  
510 004416 005077 174730 CLR @STREG ;ENSURE INTR. ENABLE IS CLEARED  
511 004422 017737 174730 001124 MOV @ADBUFF,$GDDAT ;READ TO CLEAR DONE FLAG  
512 004430 012737 000300 001124 MOV #BIT7!BIT6,$GDDAT ;LOAD EXPECTED GOOD DATA  
513 004436 104002 ERROR 2 ;FAILED TO INTERRUPT ON DONE  
514 004440 000401 BR 4$ ;;BRANCH TO NEXT TEST  
515 004442 022626 3$: CMP (SP)+,(SP)+ ;RESET STACK POINTER  
516 004444 013777 001362 174706 4$: MOV VECTR1,@VECTOR ;SET UP FOR UNEXPECTED INTERRUPT  
517 004452 012777 004700 174702 MOV #4700,@VECTR1  
518 004460 005046 CLR -(SP) ;CLEAR PSW  
519 004462 012746 004470 MOV #5$,-(SP)
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520 004466 000002          RTI
521 004470 005077 174656 5$: CLR @STREG
522 004474 005777 174656 TST @ADDBUFF ;CLEAR DONE BIT
523
524 ;*****
(3) ;*TEST 23 TEST INTERRUPT OCCURS WHEN ERROR AND I.E.E. IS SET
(3) ;*****
(2) 004500 000004 TST23: SCOPE
(1) 004502 012737 000100 001160 MOV #100,$TIMES ;DO 100 ITERATIONS
525 004510 012737 004516 001106 MOV #10$, $LPADR ;LOAD RETURN ADDRESS
526 004516 042777 000100 174420 10$: BIC #BIT6,@$TKS ;REMOVE TKB INTERRUPT
527 004524 005046 CLR -(SP) ;LOWER PRIORITY
528 004526 012746 004534 MOV #1$, -(SP)
529 004532 000002 RTI
530 004534 004737 013166 1$: JSR PC,SETINT ;LOAD VECTOR AREA WITH TRAP CATCHER
531 004540 012777 004612 174616 MOV #2$,@VECTR2 ;SETUP VECTOR ADDRESS
532 004546 012777 000200 174612 MOV #200,@VECTR3 ;SET UP NEW PSW
533 004554 012777 140000 174570 MOV #BIT15!BIT14,@STREG ;CAUSE AN INTERRUPT
534 004562 017737 174564 001126 MOV @STREG,$BDDAT ;BAD DATA
535 004570 012737 140000 001124 MOV #BIT15!BIT14,$GDDAT ;GOOD DATA
536 004576 005077 174550 CLR @STREG ;CLEAR STATUS
537 004602 005777 174550 TST @ADDBUFF ;AND CLEAR DONE
538 004606 104002 ERROR 2
539 004610 000401 BR 3$
540 004612 022626 2$: CMP (SP)+,(SP)+ ;POP STACK
541 004614 005077 174532 3$: CLR @STREG ;CLEAR STATUS REG.
542 004620 005777 174532 TST @ADDBUFF ;FALSE READ TO CLEAR DONE
543 004624 013777 001366 174532 MOV VECTR3,@VECTR2 ;RESET VECTOR
544 004632 012777 004700 174526 MOV #4700,@VECTR3
545 004640 005046 CLR -(SP) ;RESET PRIORITY
546 004642 012746 004650 MOV #4$, -(SP)
547 004646 000002 RTI
548 004650 005077 174476 4$: CLR @STREG
549 ;*****
(3) ;*TEST 24 TEST ERROR FLAG SETS IF 2ND CONVERSION ENDS BEFORE READING BUFFER
(3) ;*****
(2) 004654 000004 TST24: SCOPE
550 004656 012777 000001 174466 MOV #BIT0,@STREG ;START CONVERSION
551 004664 052777 000100 174252 BIS #BIT6,@$TKS ;ENABLE TKB INTERRUPT
552 004672 105777 174454 1$: TSTB @STREG ;WAIT FOR
553 004676 100375 BPL 1$
554 004700 012737 100200 001124 2$: MOV #BIT15!BIT7,$GDDAT ;LOAD EXPECTED VALUE
555 004706 012777 000001 174436 MOV #BIT0,@STREG ;START 2ND CONVERSION
556 004714 012700 001000 MOV #BIT9,R0 ;WAIT FOR 2ND
557 004720 005300 3$: DEC R0 ;CONVERSION TO END
558 004722 001376 BNE 3$
559 004724 104414 4$: CHECK
560 004726 104001 ERROR 1 ;ERROR FLAG NOT SET WHEN 2ND
561 ; CONVERT ENDS BEFORE READ BUFFER FROM FIRST
562 004730 017700 174422 MOV @ADDBUFF,R0 ;CLEAR DONE FLAG

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564 (3) *****
565 (3) *TEST 25 TEST ERROR FLAG SETS IF START 2ND CONV. BEFORE DONE FLAG SETS
566 (2) 004734 000004 TST25: SCOPE
567 004736 012737 100000 001124 MOV #BIT15,$GDDAT ;LOAD EXPECTED DATA
568 004744 012777 000001 174400 MOV #BIT0,@STREG ;START CONVERSION
569 004752 112777 000001 174372 MOVB #BIT0,@STREG ;START NEXT CONVERSION
570 004760 112777 000001 174364 MOVB #BIT0,@STREG ;ONCE AGAIN IN CASE REFRESH INTERVENED
571 004766 017737 174360 001126 MOV @STREG,$BDDAT ;READ STATUS REGISTER
572 004774 042737 077777 001126 BIC #77777,$BDDAT ;MASK OUT BIT 15
573 005002 023737 001124 001126 CMP $GDDAT,$BDDAT ;COMPARE RESULTS
574 005010 001401 BEQ 1$ ;BRANCH OVER ERROR
575 005012 104001 ERROR 1 ;ERROR FLAG NOT SET WHEN 2ND
;CONVERT BEGINS BEFORE FIRST DONE
576 005014 017700 174336 1$: MOV @ADBUFF,R0
577 005020 005077 174326 CLR @STREG ;CLEAR STATUS REGISTER
578 (3) *****
579 (3) *TEST 26 TEST CHANNELS 0-7 FOR SINGLE ENDED
580 (2) 005024 000004 TST26: SCOPE
581 005026 005037 001124 CLR $GDDAT
582 005032 013777 000010 174312 MOV BIT3,@STREG ;ENABLE PREAMP STATUS
583 005040 005277 174306 1$: INC @STREG ;START A CONVERSION
584 005044 105777 174302 2$: TSTB @STREG ;IS CONVERSION DONE?
585 005050 100375 BPL 2$ ;NO, WAIT TILL IT IS DONE
586 005052 017737 174300 001126 MOV @ADBUFF,$BDDAT ;GET PREAMP STATUS
587 005060 042737 007777 001126 BIC #7777,$BDDAT ;MASK OUT CONVERTED VALUE
588 005066 001401 BEQ 3$ ;SKIP OVER ERROR IF ZERO
589 005070 104001 ERROR 1
590 005072 062777 000400 174252 3$: ADD #BIT8,@STREG ;INCREMENT CHANNEL TO BE TESTED
591 005100 032777 004000 174244 BIT #BIT11,@STREG ;IS IT DONE?
592 005106 001754 BEQ 1$ ;NO
593 (3) *****
594 (3) *TEST 27 TEST CLOCK OVERFLOW STARTS A/D (TESTER ONLY)
595 (2) 005110 000004 TST27: SCOPE
596 005112 005737 001476 TST WFTST ;RUNNING IN NORMAL MODE?
597 005116 100022 BPL 2$ ;YES, GO TO NEXT TEST
598 005120 012737 000240 001124 MOV #BIT7!BITS,$GDDAT ;SET UP EXPECTED RESULT
599 005126 013777 001124 174216 MOV $GDDAT,@STREG ;ENABLE CLOCK OVERFLOW START
600 005134 012700 001000 MOV #BIT9,R0 ;STALL TIME COUNTER
601 005140 012777 177777 174240 MOV #177777,@CLKBPR ;SET CLOCK NEAR OVERFLOW
602 005146 012777 000011 174230 MOV #11,@CLKCSR ;START CLOCK AT LINE RATE
603 005154 005300 1$: DEC R0 ;STALL
604 005156 001376 BNE 1$ ;TIME
605 005160 104414 CHECK ;CHECK RESULT
606 005162 104001 ERROR 1 ;DONE FLAG FAILED TO SET
607 005164 005777 174166 2$: TST @ADBUFF ;CLEAR DONE FLAG
608 005170 005077 174156 CLR @STREG ;INHIBIT CLOCK OVERFLOW START

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```

605      ;*****
(3)      ;*TEST 30      TEST EXTERNAL START STARTS A/D (TEST MODULE OR TESTER)
(3)      ;*****
(2) 005174 000004 TST30: SCOPE
(1) 005176 012737 000001 001160      MOV #1,$TIMES      ;;DO 1 ITERATION
606 005204 005737 001476      TST WFTST      ;;RUNNING IN DWARF OR TESTER MODE?
607 005210 001453      BEQ 4$      ;;NO, GO TO NEXT TEST
608 005212 012737 000220 001124      MOV #BIT7!BIT4,$GDDAT      ;;SET UP EXPECTED RESULT
609 005220 013777 001124 174124      MOV $GDDAT,@STREG      ;;ENABLE EXTERNAL START
610 005226 005737 001476      TST WFTST      ;;RUNNING IN TESTER MODE?
611 005232 100013      BPL 2$      ;;NO
612 005234 012700 001000      MOV #BIT9,R0      ;;STALL TIME COUNTER
613 005240 052777 000400 174144      BIS #BIT8,@DRVDR      ;;GENERATE EXTERNAL START
614 005246 042777 000400 174136      BIC #BIT8,@DRVDR      ;;RESET BIT
615 005254 005300      1$: DEC R0      ;;STALL
616 005256 001376      BNE 1$      ;;TIME
617 005260 000425      BR 3$      ;;TEST RESULTS
618 005262 105737 001134      2$: TSTB $AUTOB      ;;IS IT UNDER A MONITOR ?
619 005266 001024      BNE 4$      ;;YES
620 005270 005737 001176      TST $PASS      ;;IS IT THE FIRST PASS?
621 005274 001021      BNE 4$      ;;NO, DON'T RUN TEST
622 005276 104401 020330      TYPE ,EXTST      ;;TYPE MESSAGE ABOUT EXT. START
623 005302 004737 026572      JSR PC,WHICHU      ;;DETERMINE UNIT #
624 005306 013746 001514      MOV UNITBD,-(SP)      ;;SAVE UNITBD FOR TYPEOUT
(1) 005312 104403      TYPOS      ;;GO TYPE--OCTAL ASCII
(1) 005314 001      .BYTE 1      ;;TYPE 1 DIGIT(S)
(1) 005315 000      .BYTE 0      ;;SUPPRESS LEADING ZEROS
625 005316 104401 021463      TYPE ,CRWR      ;;TYPE 'TYPE CR WHEN READY'
626 005322 104412      RDLIN      ;;WAIT FOR CR
627 005324 005726      TST (SP)+      ;;POP WORD OFF STACK
628 005326 042777 100000 174016      BIC #BIT15,@STREG      ;;CLEAR A/D ERROR
629 005334 104414      3$: CHECK      ;;CHECK RESULT
630 005336 104001      ERROR 1      ;;DONE FLAG FAILED TO SET
631 005340 005777 174012      4$: TST @ADB,F      ;;CLEAR DONE FLAG
632 005344 005077 174002      CLR @STREG      ;;INHIBIT EXTERNAL START
633
634
635      ;*****
636 005350 000004      SCOPE
637 005352 000207      RTS PC      ;;RETURN TO TEST SECTION
638      ;*****
639
640
641      ;;SUBROUTINE FOR LOGIC TESTS;;
642 005354 013777 001124 173770 TESTIT: MOV $GDDAT,@STREG      ;;LOAD EXPECTED VALUE
643 005362 017737 173764 001126 TEST: MOV @STREG,$BDDAT      ;;READ ST. REG.
644 005370 023737 001124 001126      CMP $GDDAT,$BDDAT      ;;COMPARE RESULTS
645 005376 001002      BNE RETERR      ;;ERROR RETURN
646 005400 062716 000002      ADD #2,(SP)      ;;BUMP RETURN ADDRESS TO GET AROUND ERROR
647 005404 000002      RETERR: RTI

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```
649 .SBTTL WRAPAROUND TEST SECTION
650 005406 WRAP:
651 :*****
(3) :*TEST 31 TEST CHO GROUND
(3) :*****
(2) 005406 012737 000031 001102 TST31: MOV #STN,$STNM
(1) 005414 012737 000010 001160 MOV #10,$TIMES ;;DO 10 ITERATIONS
652 005422 012737 005406 001106 MOV #TST31,$LPADR ;*SET UP LOOP ADDRESS
(2) 005430 012737 005406 001110 MOV #TST31,$LPERR ;*SET UP ERROR LOOP ADDRESS
653 005436 005737 001370 TST BASECH ;TESTING CHANNEL 0-7?
654 005442 001111 BNE WRAPX ;NO, DON'T TEST
655 005444 004537 015352 JSR R5,CONVRT ;CONVERT 8 TIMES
656 005450 000000 0 JSR R5,CONVRT ;CONVERT 8 TIMES
657 005452 004537 015502 JSR R5,COMPAR ;COMPARE RESULTS
658 005456 004000 4000 ;NOMINAL
659 005460 016330 V12 ;TOLLERANCE
660 005462 104004 ERROR 4 ;ERROR ON A/D CHANNEL
661 :*****
(3) :*TEST 32 TEST CH1 +4.5 VOLT
(3) :*****
(2) 005464 000004 TST32: SCOPE
(1) 005466 012737 000010 001160 MOV #10,$TIMES ;;DO 10 ITERATIONS
662 005474 004537 015352 JSR R5,CONVRT ;CONVERT 8 TIMES
663 005500 000001 1 ;CHANNEL 1
664 005502 004537 015502 JSR R5,COMPAR ;COMPARE RESULTS
665 005506 007344 7344 ;NOMINAL
666 005510 016336 V326 ;TOLLERANCE
667 005512 104004 ERROR 4 ;ERROR ON A/D CHANNEL
668 :*****
669 :*TEST 33 TEST CH2 -4.5 VOLT
(3) :*****
(2) 005514 000004 TST33: SCOPE
(1) 005516 012737 000010 001160 MOV #10,$TIMES ;;DO 10 ITERATIONS
670 005524 004537 015352 JSR R5,CONVRT ;CONVERT 8 TIMES
671 005530 000002 2 ;CHANNEL 2
672 005532 004537 015502 JSR R5,COMPAR ;COMPARE RESULTS
673 005536 000434 434 ;NOMINAL
674 005540 016336 V326 ;TOLLERANCE
675 005542 104004 ERROR 4 ;ERROR ON A/D CHANNEL
676 :*****
(3) :*TEST 34 TEST CH5 GROUND (DWARF OR TESTER)
(3) :*****
(2) 005544 000004 TST34: SCOPE
(1) 005546 012737 000010 001160 MOV #10,$TIMES ;;DO 10 ITERATIONS
677 005554 005737 001476 TST WFTST ;RUNNING WITHOUT TESTER OR DWARF
678 005560 001002 BNE 1$ ;NO
679 005562 000137 006240 JMP STD ;YES, BYPASS DWARF/TESTER CHECKS
680 005566 004537 015352 1$: JSR R5,CONVRT ;CONVERT 8 TIMES
681 005572 000005 5 ;CHANNEL 5
682 005574 004537 015502 JSR R5,COMPAR ;COMPARE RESULTS
683 005600 004000 4000 ;NOMINAL
684 005602 016330 V12 ;TOLLERANCE
685 005604 104004 ERROR 4 ;ERROR ON A/D CHANNEL
```

686
687
(3)
(3)
(2) 005606 000004
(1) 005610 012737 000010 001160
688 005616 004537 015352
689 005622 000004
690 005624 004537 015502
691 005630 006020
692 005632 016336
693 005634 104004
694
695
(3)
(3)
(2) 005636 000004
(1) 005640 012737 000010 001160
696 005646 004537 015352
697 005652 000006
698 005654 004537 015502
699 005660 001760
700 005662 016336
701 005664 104004
702
703 005666
704
(3)
(3)
(2) 005666 000004
(1) 005670 012737 000010 001160
705 005676 012737 000037 001102
706 005704 013737 001370 001444
707 005712 001003
708 005714 012737 000010 001444
709 005722 012705 016340
710 005726 012537 005752
711 005732 023737 001372 001444
712 005740 103415
713 005742 004537 015360
714 005746 004537 015502
715 005752 005560
716 005754 016336
717 005756 104004
718 005760 005237 001444
719 005764 020527 016360
720 005770 001356
721 005772 000753

*TEST 35 TEST CH4 +2.6 VOLTS (DWARF OR TESTER)

TST35: SCOPE
MOV #10,\$TIMES ;:DO 10 ITERATIONS
JSR R5,CONVRT ;:CONVERT 8 TIMES
4 ;:CHANNEL 4
JSR R5,COMPAR ;:COMPARE RESULTS
6020 ;:NOMINAL
V326 ;:TOLLERANCE
ERROR 4 ;:ERROR ON A/D CHANNEL

*TEST 36 TEST CH6 -2.2 VOLTS (DWARF OR TESTER)

TST36: SCOPE
MOV #10,\$TIMES ;:DO 10 ITERATIONS
JSR R5,CONVRT ;:CONVERT 8 TIMES
6 ;:CHANNEL 6
JSR R5,COMPAR ;:COMPARE RESULTS
1760 ;:NOMINAL
V326 ;:TOLLERANCE
ERROR 4 ;:ERROR ON A/D CHANNEL

WRAPX:

*TEST 37 TEST VOLTAGE ON CHANNELS (DWARF OR TESTER)

TST37: SCOPE
MOV #10,\$TIMES ;:DO 10 ITERATIONS
MOV #STN-1,\$STNM ;:SET UP TEST NUMBER
MOV BASECH,CHANL ;:SET UP CHANNEL
BNE 1\$;:CHANNEL OK
MOV #10,CHANL ;:ON CHANNEL 10
1\$: MOV #VTABLE,R5 ;:POINT TO VOLTAGE TABLE
2\$: MOV (R5)+,4\$;:SET UP EXPECTED VALUE
CMP BASEEND,CHANL ;:DONE?
BLO TST40 ;:YES, GO TO NEXT TEST
3\$: JSR R5,CONVRT ;:CONVERT 8 TIMES
JSR R5,COMPAR ;:COMPARE RESULTS
4\$: 5560 ;:VOLTAGE
V326 ;:TOLLERANCE
ERROR 4 ;:ERROR ON A/D CHANNEL
INC CHANL ;:GET NEXT CHANNEL
CMP R5,#VTABLE+20 ;:IS VOLTAGE TABLE POINTER AT END OF TABLE?
BNE 2\$;:NO, GET NEXT EXPECTED VALUE
BR 1\$;:

```
723      ::*****  
(3)      :*TEST 40      TEST CHANNEL FOR +- 2.2 VOLTS IN DIFFERENTIAL MODE  
(3)      :*****  
(2) 005774 000004      TST40: SCOPE  
(1) 005776 012737 000001 001160      MOV #1,$TIMES      ;;DO 1 ITERATION  
724 006004 005737 001476      TST WFTST      ;;RUNNING IN TESTER MODE?  
725 006010 100007      BPL 1$      ;;BR IF NO  
726 006012 012737 000010 001160      MOV #10,$TIMES      ;;DO 10 ITERATIONS  
727 006020 052777 000200 173364      BIS #BIT7,@DRVDR      ;;SET A/D AND MUX TO DIFFERENTIAL  
728 006026 000423      BR 3$      ;;  
729 006030 005737 001176      1$: TST $PASS      ;;IS THIS THE FIRST PASS?  
730 006034 001101      BNE TST41      ;;NO, DON'T RUN TEST  
731 006036 023727 001372 000010      CMP BASEND,#10      ;;TESTING ANY DIFFERENTIAL CHANNELS?  
732 006044 103475      BLO TST41      ;;NO  
733 006046 104401 020235      TYPE ,DIFM      ;;TYPE SET DWARF TO DIFFERENTIAL MESSAGE  
734 006052 104401 021463      TYPE ,CRWR      ;;TYPE 'TYPE CR WHEN READY'  
735 006056 104412      RDLIN      ;;WAIT FOR CARRIDGE RETURN  
736 006060 005726      TST (SP)+      ;;POP WORD OFF STACK  
737 006062 012737 006076 001106 2$: MOV #3$,$LPADR      ;;SET UP LOOP ADDRESS  
738 006070 012737 006076 001110      MOV #3$,$LPERR      ;;SET UP ERROR LOOP ADDRESS  
739 006076 012737 002220 006156 3$: MOV #2220,6$      ;;SET UP INITIAL EXPECTED VALUE -2.2 V  
740 006104 013700 001370      MOV BASECH,R0      ;;GET FIRST CHANNEL TO TEST  
741 006110 020027 000010      CMP R0,#10      ;;IS R0 >= 10  
742 006114 103002      BHIS 4$      ;;YES  
743 006116 012700 000010      MOV #10,R0      ;;SET R0 = 10  
744 006122 013705 001372      4$: MOV BASEND,R5      ;;GET LAST CHANNEL TO TEST  
745 006126 160005      SUB R0,R5      ;;GET DIFFERENCE BETWEEN FIRST AND LAST  
746 006130 006205      ASR R5      ;;DIVIDE IT IN HALF  
747 006132 060005      ADD R0,R5      ;;ADD FIRST CHANNEL GIVING LAST CHANNEL  
748      TO TEST  
749 006134 010037 001444      MOV R0,CHANL      ;;SET UP FIRST CHANNEL TO TEST  
750 006140 020537 001444      5$: CMP R5,CHANL      ;;DONE?  
751 006144 103417      BLO 7$      ;;YES, GO TO NEXT TEST  
752 006146 004537 015360      JSR R5,CONVTC      ;;CONVERT 8 TIMES  
753 006152 004537 015502      JSR R5,COMPAR      ;;TEST RESULTS  
754 006156 002220      6$: 2220      ;;NOMINAL  
755 006160 016336      V326      ;;TOLLERANCE  
756 006162 104004      ERROR 4      ;;ERROR ON A/D CHANNEL  
757 006164 005237 001444      INC CHANL      ;;POINT TO NEXT CHANNEL TO TEST  
758 006170 005437 006156      NEG 6$      ;;REVERSE SIGN OF EXPECTED VALUE  
759 006174 042737 170000 006156      BIC #170000,6$      ;;CLEAR EXTRA BITS  
760 006202 000756      BR 5$  
761 006204 005737 001476      7$: TST WFTST      ;;RUNNING IN TESTER MODE?  
762 006210 100005      BPL 8$      ;;BR IF DWARF  
763 006212 000005      RESET  
764 006214 052777 000100 172722      BIS #100,@$TKS      ;;ENABLE INTERRUPTS  
765 006222 000406      BR TST41      ;;GO TO NEXT TEST  
766 006224 104401 020125      8$: TYPE ,SDSE      ;;TYPE SET DWARF TO SINGLE ENDED MESSAGE  
767 006230 104401 021463      TYPE ,CRWR      ;;TYPE 'TYPE CR WHEN READY'  
768 006234 104412      RDLIN      ;;WAIT FOR CR  
769 006236 005726      TST (SP)+      ;;POP WORD OFF STACK
```

```
771 006240
772
(3)
(3)
(2) 006240 000004
(1) 006242 012737 000001 001160
773 006250 012737 000041 001102
774 006256 005077 173074
775 006262 005037 001444
776 006266 004537 015366
777 006272 013704 001426
778 006276 012777 000377 173052 1$:
779 006304 004537 015366
780 006310 160437 001426
781 006314 004537 015502
782 006320 000005
783 006322 016324
784 006324 104004
785
(3)
(3)
(2) 006326 000004
(1) 006330 012737 000001 001160
786 006336 104401 017237
787 006342 004737 026572
788 006346 013746 001514
789 006352 104403
790 006354 001 000
791 006356 005037 001444
792 006362 005037 001442
793 006366 004737 007670
794 006372 104401 022775
795 006376 004737 007772
796 006402 004537 015502
797 006406 000000
798 006410 016332
799 006412 000401
800 006414 000407
801 006416 104401 021606
802 006422 004737 026564
803 006426 005237 001112
804 006432 000402
805 006434 104401 021132

STD:
:*****
:*TEST 41 TEST VERNIER OFFSET DAC ON CHO
:*****
TST41: SCOPE
MOV #1,$TIMES ;;DO 1 ITERATION
MOV #STN-1,$STNM ;;SET UP TEST NUMBER
CLR @ADBUFF ;;SET VERNIER DAC = 0
CLR CHANL ;;SET UP TO CONVERT ON CHANNEL 0
JSR R5,CONVCD ;;CONV. CHO, DIRECT VERNIER DAC
MOV TEMP,R4 ;;SAVE VALUE IN R4
MOV #377,@ADBUFF ;;SET VERNIER DAC = 377
JSR R5,CONVCD ;;CONVERT IT
SUB R4,TEMP ;;TEMP=DIFF. BETWEEN VALUE & PREVIOUS
JSR R5,COMPAR ;;COMPARE RESULTS
5
V2
ERROR 4
:*****
:*TEST 42 OFFSET ON CHO
:*****
TST42: SCOPE
MOV #1,$TIMES ;;DO 1 ITERATION
TYPE ,OFSET ;;INFORM OPER. TEST NAME
JSR PC,WHICHU ;;GET UNIT #
MOV UNITBD,-(SP) ;;PUSH IT
TYPOS ;;TELL OPER.
.BYTE 1,0
CLR CHANL ;;LOAD CHANNEL
CLR DUMMY ;;LOAD DUMMY
JSR PC,OFFSET ;;FIND OFFSET
TYPE ,MOFSET ;;TYPE 'OFFSET='
JSR PC,TOFF ;;TYPE OFFSET
JSR R5,COMPAR ;;IS RESULT WITHIN LIMITS?
0
V50D
BR OFFERR ;;NO-ERROR
BR OFFOK ;;YES-OK
OFFERR: TYPE ,ERMSG
JSR PC,WHICHV ;;INDICATE BAD UNIT
INC $ERTTL ;;UPDATE ERROR COUNT
BR TST43 ;;GO TO NEXT TEST
OFFOK: TYPE ,OKMSG
```

```
807 (3) *****  
(3) *TEST 43 TEST RAMP RANGE, CH3  
(2) 006440 000004 TST43: SCOPE  
808 006442 012737 000001 001160 MOV #1,$TIMES ;DO THIS ONCE  
809 006450 012703 007777 MOV #7777,R3 ;INIT R3 VALUE  
810 006454 005004 CLR R4 ;AND R4  
811 006456 012777 001400 172666 MOV #1400,@STREG ;SETUP FOR CH3  
812 006464 012702 047040 MOV #20000.,R2 ;SETUP FOR 20,000 CONVERSIONS  
813 006470 105277 172656 1$: INCB @STREG  
814 006474 105777 172652 2$: TSTB @STREG  
815 006500 100375 BPL 2$  
816 006502 027704 172650 CMP @ADBUFF,R4  
817 006506 003402 BLE 3$  
818 006510 017704 172642 MOV @ADBUFF,R4 ;HIT A NEW HIGH  
819 006514 027703 172636 3$: CMP @ADBUFF,R3  
820 006520 002002 BGE 4$  
821 006522 017703 172630 MOV @ADBUFF,R3 ;HIT A NEW LOW  
822 006526 005302 4$: DEC R2  
823 006530 001357 BNE 1$  
824 006532 010337 001426 MOV R3,TEMP  
825 006536 004537 015502 JSR R5,COMPAR  
826 006542 000000 0  
827 006544 016322 V0  
828 006546 104004 ERROR 4 ;RAMP DIDN'T REACH LOW END OF RANGE  
829 006550 010437 001426 MOV R4,TEMP  
830 006554 004537 015502 JSR R5,COMPAR  
831 006560 007777 7777  
832 006562 016322 V0  
833 006564 104004 ERROR 4 ;RAMP DIDN'T REACH HIGH END OF RANGE
```

```

835      ::*****
(3)      ::*TEST 44      NOISE TEST, 1 EDGE
(3)      ::*****
(2) 006566 000004      TST44: SCOPE
(1) 006570 012737 000001 001160      MOV #1,$TIMES      ;;DO 1 ITERATION
836 006576 005037 001416      CLR WIDE      ;;CLEAR ENTRY FLAG
837 006602 004737 006612      JSR PC,NOITST      ;;RUN NOISE TEST
838 006606 000137 007366      JMP NOIJMP      ;;NEXT TEST
839 006612 104401 017154      NOITST: TYPE ,NOIMSG
840 006616 004737 026572      JSR PC,WHICHU      ;;DETERMINE UNIT #
841 006622 013746 001514      MOV UNITBD,-(SP)
842 006626 104403      TYPOS      ;;TELL OPER.
843 006630 001 000      .BYTE 1,0
844 006632 104401 001165      TYPE , $CRLF
845 006636 005737 001476      TST WFTST      ;;RUNNING ON THE TESTER
846 006642 100007      BPL 1$      ;;BR IF NOT
847 006644 012737 000020 001444      MOV #20,CHANL      ;;ASSUME TESTING AM
848 006652 022737 000057 001372      CMP #57,BASEND      ;;TESTING AM?
849 006660 001403      BEQ 2$      ;;YES
850 006662 013737 001370 001444 1$: MOV BASECH,CHANL      ;;LOAD CHANNEL 0
851 006670 012737 006700 007770 2$: MOV #3$,ERRADR      ;;SET UP ERROR RETRY ADDRESS
852 006676 005005      CLR R5      ;;CLEAR RETRY COUNT
853 006700 005205      3$: INC R5      ;;INCREMENT COUNT
854 006702 022705 000006      CMP #6,R5      ;;IS COUNT = 6?
855 006706 001450      BEQ 5$      ;;YES, CHANNEL TOO WIDE OR NOISY
856 006710 013737 001444 001442      MOV CHANL,DUMMY      ;;LOAD DUMMY CHANNEL
857 006716 004537 015360      JSR R5,CONVTC      ;;GET EDGE VALUE
858 006722 013737 001426 001470      MOV TEMP,EDGE      ;;SET UP EDGE VALUE
859 006730 004537 013224      JSR R5,SARSUB      ;;DO SAR ROUTINE AT 16%
860 006734 000020      16.
861 006736 004737 007744      JSR PC,TSTDAC      ;;CHECK VERNIER DAC SETTING
862 006742 013737 001464 001454      MOV DAC,RMS      ;;ADD RESULT TO RMS
863 006750 004537 013224      JSR R5,SARSUB      ;;DO SAR ROUTINE AT 84%
864 006754 000124      84.
865 006756 004737 007744      JSR PC,TSTDAC      ;;CHECK VERNIER DAC SETTING
866 006762 163737 001464 001454      SUB DAC,RMS      ;;SUBTRACT RESULT FROM RMS
867 006770 012737 000001 013164      MOV #1,EDGFLG
868 006776 104401 021063      4$: TYPE ,RM:NOI
869 007002 013702 001454      MOV RMS,R2
870 007006 004737 015314      JSR PC,TYPEPRP      ;;TYPE RMS VALUES
871 007012 023737 001454 016360      CMP RMS,VNR      ;;WITHIN LIMITS?
872 007020 003007      BGT 6$      ;;NO
873 007022 104401 021132      TYPE ,OKMSG
874 007026 000412      BR 7$      ;;
875 007030 012737 000377 001454 5$: MOV #255.,RMS      ;;SET RMS TO MAX ERROR
876 007036 000757      BR 4$      ;;
877 007040 104401 021606      6$: TYPE ,ERMSG
878 007044 004737 026564      JSR PC,WHICHV      ;;INDICATE BAD UNIT
879 007050 005237 001112      INC $ERTTL      ;;UPDATE ERROR TOTAL
880 007054 012737 007064 007770 7$: MOV #8$,ERRADR      ;;SET UP ERROR RETRY ADDRESS
881 007062 005005      CLR R5      ;;CLEAR RETRY COUNT
882 007064 005205      8$: INC R5      ;;INCREMENT COUNT
883 007066 022705 000006      CMP #6,R5      ;;IS COUNT = 6?
884 007072 001450      BEQ 10$      ;;YES, CHANNEL TOO WIDE OR NOISY

```



```

885 007074 013737 001444 001442      MOV  CHANL,DUMMY      ;LOAD DUMMY CHANNEL
886 007102 004537 015360                JSR  R5,CONVTC        ;GET EDGE VALUE
887 007106 013737 001426 001470      MOV  TEMP,EDGE        ;SET UP EDGE VALUE
888 007114 004537 013224                JSR  R5,SARSUB        ;DO SAR ROUTINE AT 1%
889 007120 000001 1                      1
890 007122 004737 007744                JSR  PC,TSTDAC        ;CHECK VERNIER DAC SETTING
891 007126 013737 001464 001456      MOV  DAC,PEAK         ;ADD RESULT TO PEAK
892 007134 004537 013224                JSR  R5,SARSUB        ;DO SAR ROUTINE AT 99%
893 007140 000143 99.
894 007142 004737 007744                JSR  PC,TSTDAC        ;CHECK VERNIER DAC SETTING
895 007146 163737 001464 001456      SUB  DAC,PEAK         ;SUBTRACT RESULT FROM PEAK
896 007154 012737 000001 013164      MOV  #1,EDGFLG
897 007162 104401 021077 9$:        TYPE ,PKNOI
898 007166 013702 001456                MOV  PEAK,R2
899 007172 004737 015314                JSR  PC,TYPRP         ;TYPE PEAK VALUES
900 007176 023737 001456 016362      CMP  PEAK,VNP         ;WITHIN LIMITS?
901 007204 003007 11$                BGT  11$              ;NO
902 007206 104401 021132                TYPE ,OKMSG
903 007212 000412 12$                BR   12$              ;;
904 007214 012737 000377 001456 10$:  MOV  #255.,PEAK        ;SET PEAK TO MAX ERROR
905 007222 000757 9$                BR   9$                ;;
906 007224 104401 021606 11$:        TYPE ,ERMSG
907 007230 004737 026564                JSR  PC,WHICHV        ;INDICATE BAD UNIT
908 007234 005237 001112                INC  $ERTTL           ;UPDATE ERROR COUNT
909 007240 104401 001165 12$:        TYPE ,$CRLF          ;LEAVE A BLANK LINE
910 007244 005237 001444                INC  CHANL           ;GET NEXT CHANNEL
911 007250 005737 001416                TST  WIDE            ;CHECK ENTRY FLAG
912 007254 001023 15$                BNE  15$             ;BR IF MANUAL ENTRY
913 007256 022737 000003 001444      CMP  #3,CHANL        ;CHANNEL 3 (RAMP CHANNEL)?
914 007264 001404 13$                BEQ  13$             ;;YES
915 007266 022737 000007 001444      CMP  #7,CHANL        ;CHANNEL 7 (EDC INPUT CHANNEL)?
916 007274 001002 14$                BNE  14$             ;;NO
917 007276 005237 001444 13$:        INC  CHANL           ;CHANNELS 3 AND 7 ARE SKIPPED
918 007302 005737 001476 14$:        TST  WFTST           ;RUNNING WITHOUT DWARF/TESTER ?
919 007306 001006 15$                BNE  15$             ;;BR IF DWARF/TESTER
920 007310 022737 000004 001444      CMP  #4,CHANL        ;DONE?
921 007316 001422 17$                BEQ  17$             ;;YES,GO TO NEXT TEST
922 007320 000137 006670 2$                JMP  2$
923 007324 15$:
(1) 007324 100406 16$                BMI  16$             ;;BR IF TESTER MODE
924 007326 023737 001372 001444      CMP  BASEND,CHANL   ;DONE?
925 007334 103413 17$                BLO  17$             ;;YES
926 007336 000137 006670 2$                JMP  2$             ;NO, CONTINUE TESTING
927 007342 013705 001372 001372      ;ON TESTER - DON'T RUN GOOD MNCAM
928 007342 013705 001372 001372 16$:  MOV  BASEND,R5       ;GET LAST CHANNEL
929 007346 162705 000017 000017      SUB  #17,R5         ;GET LAST CHANNEL TO TEST
930 007352 020537 001444                CMP  R5,CHANL        ;DONE?
931 007356 001402 17$                BEQ  17$             ;;YES, GO TO NEXT TEST
932 007360 000137 006670 2$                JMP  2$             ;NO, CONTINUE TESTING
933 007364 000207 17$:        RTS  PC              ;EXIT
934 007366 000207 NOIJMP:

```

```
936 (3) (3) (2) 007366 000004 (1) 007370 012737 000001 001160 937 007376 104401 017204 938 007402 004737 026572 939 007406 013746 001514 940 007412 104403 941 007414 001 000 942 007416 104401 001165 943 007422 005737 001476 944 007426 100006 945 007430 012700 000024 946 007434 022737 000057 001372 947 007442 001405 948 007444 013700 001370 1$: 949 007450 001410 950 007452 062700 000004 951 007456 010037 001432 2$: 952 007462 005200 953 007464 010037 001434 954 007470 000406 955 007472 012737 000001 001432 3$: 956 007500 012737 000002 001434 4$: 957 007506 005005 5$: 958 007510 005205 959 007512 022705 000006 960 007516 001444 961 007520 013737 001434 001444 962 007526 004537 015360 963 007532 013737 001426 001470 964 007540 005002 965 007542 004737 013036 966 007546 000760 967 007550 004737 013036 968 007554 000755 969 007556 005702 970 007560 100001 971 007562 005402 972 007564 010204 6$: 973 007566 012737 000001 013164 974 007574 004737 012704 975 007600 023737 001434 001432 976 007606 103413 977 007610 013702 001432 978 007614 013737 001434 001432 979 007622 010237 001434 980 007626 000727 981 007630 012702 000377 7$: 982 007634 000753
```

```
*****  
: *TEST 45 INTERCHANNEL SETTling TEST, 1 EDGE  
: *****  
TST45: SCOPE  
MOV #1,$TIMES ;:DO 1 ITERATION  
TYPE ,SETMSG ;:TYPE 'SETTLING TEST'  
JSR PC,WHICHU ;:DETERMINE THE UNIT #  
MOV UNITBD,-(SP) ;:SAVE IT  
TYPOS ;:TYPE IT  
 .BYTE 1,0  
TYPE ,$CRLF  
TST WFTST ;:RUNNING ON THE TESTER  
BPL 1$ ;:BR IF NOT  
MOV #24,R0 ;:GET MUX CHANNEL INCASE TESTING MNCAM  
CMP #57,BASEND ;:TESTING MNCAM?  
BEQ 2$ ;:NO  
MOV BASECH,R0 ;:IS CHANNEL ZERO?  
BEQ 3$ ;:YES  
ADD #4,R0 ;:SET UP CHANNELS TO SETTLE BETWEEN  
MOV R0,CH1 2$:  
INC R0  
MOV R0,CH2  
BR 4$ ;:  
MOV #1,CH1 ;:DO TEST BETWEEN CHANNEL 1 AND 2  
MOV #2,CH2  
4$: CLR R5 ;:CLEAR RETRY COUNT  
5$: INC R5 ;:INCREMENT COUNT  
CMP #6,R5 ;:IS COUNT = 6?  
7$: BEQ 7$ ;:YES  
MOV CH2,CHANL ;:GET EDGE VALUES  
JSR R5,CONVTC ;:SET UP EDGE VALUE  
MOV TEMP,EDGE  
CLR R2  
JSR PC,SET1A ;:SCALING = .02 LSB  
BR 5$ ;:ERROR RECOVERY JUMP  
JSR PC,SET1A ;:MAKE IT .01 LSB  
BR 5$ ;:ERROR RECOVERY JUMP  
TST R2 ;:TEST RESULTS  
BPL 6$  
NEG R2 ;:MAKE IT POSITIVE  
6$: MOV R2,R4  
MOV #1,EDGFLG  
JSR PC,TYPSET ;:TYPE SETTling INFORMATION  
CMP CH2,CH1 ;:DONE?  
BLO TST46 ;:YES  
MOV CH1,R2 ;:SETTLE THE OTHER WAY  
MOV CH2,CH1  
MOV R2,CH2  
BR 4$ ;:  
7$: MOV #255.,R2 ;:SET SETTling TO MAX ERROR  
BR 6$ ;:
```

```
984      ::*****  
(3)      *TEST 46      DIFFERENTIAL LINEARITY AND RELATIVE ACCURACY TEST (CHANNEL 3)  
(3)      ::*****  
(2) 007636 000004  
(1) 007640 012737 000001 001160  
985 007646 005737 001370  
986 007652 001005  
987 007654 005737 001176  
988 007660 001402  
989 007662 004737 013424  
990 007666 000207  
991  
992      :*****  
993  
994 007670 012737 004001 001470 OFFSET: MOV #4001,EDGE ;4000,4001 EDGE  
995 007676 004537 013224 JSR R5,SARSUB  
996 007702 000062 50.  
997 007704 013737 001464 001426 MOV DAC,TEMP  
998 007712 012737 004000 001470 MOV #4000,EDGE ;3777,4000 EDGE  
999 007720 004537 013224 JSR R5,SARSUB  
1000 007724 000062 50.  
1001 007726 063737 001464 001426 ADD DAC,TEMP  
1002 007734 162737 000400 001426 SUB #400,TEMP  
1003 007742 000207 RTS PC  
1004  
1005  
1006      :*****  
1007      : ROUTINE TO TEST DAC SETTING FROM SARSUB  
1008      : JUMPS TO ADDRESS IN ERRADR IF DAC SETTING IS EITHER 0 OR 377  
1009      : OTHERWISE RETURNS TO CALL+1  
1010 007744 005737 001464 TSTDAC: TST DAC ;IS DAC = 0 ?  
1011 007750 001405 BEQ 1$ ;:YES  
1012 007752 022737 000377 001464 CMP #377,DAC ;IS DAC = 377 ?  
1013 007760 001401 BEQ 1$ ;:YES  
1014 007762 000207 RTS PC  
1015 007764 005726 1$: TST (SP)+ ;POP CALL OFF STACK  
1016 007766 000137 JMP @(PC)+ ;JUMP TO ADDRESS IN ERRADR  
1017 007770 000000 ERRADR: 0
```

```

1019      ;SUBROUTINE TO INSERT '+' AND TYPE # ON THE STACK
1020
1021 007772 013702 001426      TOFF:  MOV    TEMP,R2
1022 007776 100402              BMI    1$      ;;IS THE NUMBER POSITIVE?
1023 010000 104401 021604              TYPE  ,POSITV
1024 010004 104416              1$:   TYPDC
1025 010006 104401 023010              TYPE  ,MLSB      ;TYPE ASCIZ STRING
1026 010012 000207              RTS    PC
1027
1028      ;SUBROUTINE TO WAIT FOR OPERATOR'S 'RETURN' THEN CHECK TOLERANCES
1029
1030 010014 005303      TCHK:  DEC    R3      ;DECREMENT COUNT
1031 010016 001005              BNE    1$      ;;
1032 010020 012703 000005              MOV    #5,R3      ;RESET COUNT
1033 010024 104401 001165              TYPE  ,$CRLF      ;TYPE A CARRIAGE RETURN AND LINE FEED
1034 010030 000402              BR     2$      ;;
1035 010032 104401 021014              1$:   TYPE  ,SPACE      ;TYPE FOUR (4) SPACES
1036 010036 005037 001466              2$:   CLR    DELAY      ;CLEAR DELAY
1037 010042 005077 171076              CLR    @STKS      ;CLEAR INTERRUPT ENABLE
1038 010046 105777 171072              3$:   TSTB   @STKS      ;IS KEYBOARD FLAG SET?
1039 010052 100404              BMI    4$      ;;YES
1040 010054 005237 001466              INC    DELAY      ;IS DELAY ZERO?
1041 010060 001372              BNE    3$      ;;NO
1042 010062 000416              BR     4$      ;;
1043 010064 005777 171056              4$:   TST    @STKB      ;CLEAR FLAG
1044 010070 012777 000100 171046              MOV    #100,@STKS ;SET INTERRUPT ENABLE
1045 010076 004537 015502              JSR    R5,COMPAR   ;TEST LAST CONVERSION
1046 010102 000000              0
1047 010104 016326              V10
1048 010106 000402              BR     5$      ;TOLERANCE .10 LSB
1049 010110 062716 000002              ADD    #2,(SP)    ;BUMP RETURN ADDRESS
1050 010114 062716 000002              5$:   ADD    #2,(SP) ;BUMP RETURN ADDRESS 2 WORDS
1051 010120 000207              6$:   RTS    PC

```

```

1053 .SBTTL CALIBRATION SECTION
1054 010122 104401 021143 BEGINC: TYPE ,CCHAN ;ASK FOR CHANNEL
1055 010126 104413 RDOCT ;READ CHANNEL NUMBER
1056 010130 012637 001444 MOV (SP)+,CHANL ;STORE CHANNEL NUMBER
1057 010134 013737 001444 001442 MOV CHANL,DUMMY ;LOAD DUMMY
1058 010142 104401 021231 1$: TYPE ,SEL ;SELECT OFFSET OR GAIN ADJUST
1059 010146 104412 RDLIN ;GET TEST
1060 010150 012600 MOV (SP)+,RO ;MOVE POINTER TO RO
1061 010152 121027 000117 CMPB (RO),#'0 ;IS IT '0'?
1062 010156 001406 BEQ AJOFF ;:YES, GO TO ADJUST OFFSET
1063 010160 121027 000107 CMPB (RO),#'G ;IS IT 'G'?
1064 010164 001430 BEQ AJGAIN ;:YES, GO TO ADJUST GAIN
1065 010166 104401 001164 TYPE ,SQUES ;TYPE '?'
1066 010172 000763 BR 1$ ;;
1067
1068 ;SUBROUTINE TO CHECK OFFSET ADJUSTMENT VALUES
1069 010174 104401 021424 AJOFF: TYPE ,IGND ;GROUND CHANNEL
1070 010200 104412 RDLIN ;WAIT FOR CR
1071 010202 005726 TST (SP)+ ;POP 1 WORD OFF STACK
1072 010204 104401 021322 1$: TYPE ,XADJ ;ADJUST MESSAGE
1073 010210 012703 000005 MOV #5,R3 ;SET UP COUNT
1074 010214 004737 007670 2$: JSR PC,OFFSET ;TEST AND TYPE OFFSET ERROR
1075 010220 004737 007772 JSR PC,TOFF ;TYPE OFFSET
1076 010224 004737 010014 JSR PC,TCHK ;CHECK FOR A CHARACTER AND DELAY
1077 010230 000771 BR 2$ ;;
1078 010232 000402 BR 3$ ;:NOT WITHIN TOLLERANCE, TRY AGAIN
1079 010234 000137 001564 JMP BEG2
1080 010240 104401 021606 3$: TYPE ,ERMSG ;TELL OPER. 'ERROR'
1081 010244 000757 BR 1$
1082 ;SUBROUTINE TO CHECK THE GAIN ADJUSTMENT
1083 010246 104401 021523 AJGAIN: TYPE ,IVOLT ;INPUT +5.115 VOLTS ON CHANNEL
1084 010252 104401 021463 TYPE ,CRWR
1085 010256 104412 RDLIN ;WAIT FOR CR
1086 010260 005726 TST (SP)+ ;POP 1 WORD OFF STACK
1087 010262 104401 021567 1$: TYPE ,YADJ ;ADJUST MESSAGE
1088 010266 104401 021336 TYPE ,MOLSB ;TYPE '' FOR 0.00 LSB ERROR''
1089 010272 012703 000005 MOV #5,R3 ;SET UP COUNT
1090 010276 012737 007777 001470 2$: MOV #7777,EDGE ;LOOK FOR 7776,7777 EDGE
1091 010304 004537 013224 JSR R5,SARSUB
1092 010310 000062 50.
1093 010312 013737 001464 001426 MOV DAC,TEMP ;SAVE DAC
1094 010320 012737 007776 001470 MOV #7776,EDGE ;LOOK FOR 7775,7776 EDGE
1095 010326 004537 013224 JSR R5,SARSUB
1096 010332 000062 50.
1097 010334 063737 001464 001426 ADD DAC,TEMP ;ADD RESULTS
1098 010342 162737 000400 001426 SUB #400,TEMP ;OFFSET RESULT
1099 010350 004737 007772 JSR PC,TOFF ;TYPE GAIN
1100 010354 004737 010014 JSR PC,TCHK ;CHECK FOR CHARACTER AND DELAY
1101 010360 000746 BR 2$ ;;
1102 010362 000402 BR 3$ ;:NOT WITHIN TOLLERANCE, TRY AGAIN
1103 010364 000137 001564 JMP BEG2
1104 010370 104401 021606 3$: TYPE ,ERMSG ;TELL OPER. 'ERROR'
1105 010374 000732 BR 1$

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1107 .SBTTL SWITCH GAIN MANUAL INTERVENTION TEST
1108 010376 104401 021143 BEGINM: TYPE .CCHAN ;ASK FOR CHANNEL
1109 010402 104413 RDOCT ;READ CHANNEL NUMBER
1110 010404 012600 MOV (SP)+,RO ;GET CHANNEL NUMBER
1111 010406 000300 SWAB RO ;PUT CHANNEL NUMBER IN HIGH BYTE
1112 010410 052700 000010 BIS #BIT3,RO ;SET STATUS ENABLE BIT
1113 010414 010077 170732 MOV RO,@STREG ;LOAD CHANNEL AND STATUS ENABLE
1114 010420 104401 020551 TYPE .SCM ;ASK MODE BE SET TO CURRENT
1115 010424 012737 030000 001124 MOV #BIT13!BIT12,$GDDAT ;SET UP EXPECTED
1116 010432 104401 020656 1$: TYPE .GHLF ;ASK GAIN BE SET TO .5
1117 010436 104417 TESTID ;GO TEST FOR ID CODE
1118 010440 104001 ERROR 1
1119 010442 104401 020673 TYPE .GAIN5 ;ASK GAIN BE SET TO 5
1120 010446 104417 TESTID ;GO TEST ID CODE
1121 010450 104001 ERROR 1
1122 010452 104401 020713 TYPE .GAIN50 ;ASK GAIN BE SET TO 50
1123 010456 104417 TESTID ;GO TEST ID CODE
1124 010460 104001 ERROR 1
1125 010462 104401 020734 TYPE .GAIN5M ;ASK GAIN BE SET TO 500
1126 010466 104417 TESTID ;GO TEST ID CODE
1127 010470 104001 ERROR 1
1128 010472 022737 070000 001124 CMP #70000,$GDDAT ;READY TO DO RESISTANCE?
1129 010500 001003 BNE 2$ ;:NO
1130 010502 104401 020577 TYPE .SRM ;ASK MODE BE SET TO RESISTANCE
1131 010506 000751 BR 1$
1132 010510 022737 130000 001124 2$: CMP #130000,$GDDAT ;READY TO DO VOLTS?
1133 010516 001003 BNE 3$ ;:NO, DONE WITH TEST
1134 010520 104401 020630 TYPE .SVM ;ASK MODE BE SET TO VOLTS
1135 010524 000742 BR 1$
1136 010526 000137 001564 3$: JMP BEG2
1137
1138 010532 062737 010000 001124 TPRMP: ADD #BIT12,$GDDAT ;INDEX EXPECTED ID
1139 010540 104401 021463 TYPE .CRWR ;ASK FOR CR WHEN READY
1140 010544 104412 RDLIN ;WAIT FOR CR
1141 010546 005726 TST (SP)+ ;POP 1 WORD OFF STACK
1142 010550 005277 170576 INC @STREG ;START A CONVERSION
1143 010554 105777 170572 1$: TSTB @STREG ;WAIT TILL DONE
1144 010560 100375 BPL 1$ ;
1145 010562 017737 170570 001126 MOV @ADDBUFF,$BDDAT ;GET RESULTS
1146 010570 042737 007777 001126 BIC #7777,$BDDAT ;CLEAR CONVERTED VALUE
1147 010576 023737 001124 001126 CMP $GDDAT,$BDDAT ;IS ID RIGHT?
1148 010604 001002 BNE 2$ ;:NO, TAKE ERROR RETURN
1149 010606 062716 000002 ADD #2,(SP) ;BUMP RETURN ADDRESS
1150 010612 000002 2$: RTI

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1152          .SBTTL      PRINT VALUES ROUTINE
1153 010614 005077 170532      BEGINP: CLR      @STREG      ;CLEAR STATUS REGISTER
1154 010620 104401 022642      TYPE      .HEAD5      ;TYPE OUT HEADING
1155 010624 005046              CLR      -(SP)      ;CLEAR PSW
1156 010626 012746 010634      MOV      #1$,-(SP)
1157 010632 000002              RTI
1158 010634 017700 170300      1$:  MOV      @SWR,R0      ;READ CHANNEL FROM SWITCH REG.
1159 010640 042700 177700      BIC      #177700,R0      ;ISOLATE MUX BITS
1160 010644 032777 020000 170266  BIT      #BIT13,@SWR      ;IS BIT 13 SET?
1161 010652 001005              BNE      2$      ;;YES,SKIP TYPEOUT
1162 010654 104401 021011      TYPE      ,CH
1163 010660 010046              MOV      R0,-(SP)      ;;SAVE R0 FOR TYPEOUT
(1)              ;;TYPE CHANNEL
(1) 010662 104403              TYPOS      ;;GO TYPE--OCTAL ASCII
(1) 010664 002              .BYTE      2      ;;TYPE 2 DIGIT(S)
(1) 010665 000              .BYTE      0      ;;SUPPRESS LEADING ZEROS
1164 010666 012777 001540 170464 2$:  MOV      #RETURN,@VECTOR ;ADDRESS AFTER INTRPT.
1165 010674 000300              SWAB      R0      ;SWITCH BYTES
1166 010676 052700 000100      BIS      #BIT6,R0
1167 010702 010077 170444      MOV      R0,@STREG      ;LOAD THE CHANNEL
1168 010706 012702 000010      MOV      #10,R2      ;TYPEOUT COUNTER
1169 010712 005277 170434      3$:  INC      @STREG      ;START CONVERSION
1170 010716 000001              WAIT      ;WAIT FOR INTRPT.
1171 010720 017700 170432      MOV      @ADBUFF,R0      ;READ CONVERTED VALUE
1172 010724 032777 020000 170206  BIT      #BIT13,@SWR      ;IS BIT 13 SET?
1173 010732 001403              BEQ      4$      ;NOT SET, TYPE OUT LIST
1174 010734 010077 170202      MOV      R0,@DISPLAY      ;PUT VALUE IN DISPLAY FOR DISPLAY CONTROL
1175 010740 000735              BR      1$      ;REPEAT CONVERSION
1176 010742 104401 021014      4$:  TYPE      ,SPACE
1177 010746 010046              MOV      R0,-(SP)      ;;SAVE R0 FOR TYPEOUT
(1)              ;;PRINT OCTAL CONVERTED VALUE
(1) 010750 104403              TYPOS      ;;GO TYPE--OCTAL ASCII
(1) 010752 004              .BYTE      4      ;;TYPE 4 DIGIT(S)
(1) 010753 001              .BYTE      1      ;;TYPE LEADING ZEROS
1178 010754 012701 010000      MOV      #10000,R1
1179 010760 005301      5$:  DEC      R1
1180 010762 001376              BNE      5$
1181 010764 005302              DEC      R2      ;DECREMENT THE COUNTER
1182 010766 001351              BNE      3$      ;NO CARRIAGE RETURN
1183 010770 104401 001165      TYPE      ,$CRLF      ;CARRIAGE RETURN
1184 010774 000717              BR      1$      ;REPEAT CONVERSION

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1186          .SBTTL      LOGIC TEST SECTION START-UP
1187 010776 004737 011254          BEGL: JSR      PC,WFCIHK      ;CHECK I D CODE IF WESTFIELD MODE
1188 011002 012737 011010 016610 MOV      #2$,AGTST      ;LOAD EOP RETURN IF NO A/D
1189 011010 004737 002772          2$: JSR      PC,TESTAD      ;SIZE THE NUMBER OF MNCAD'S
1190 011014 004737 003266          1$: JSR      PC,BEGINL      ;LOGIC TESTS
1191 011020 004737 012410          JSR      PC,BUMPAD      ;MORE TO TEST?
1192 011024 000773          BR      1$              ;TEST NEXT A/D
1193 011026 012737 011014 016610 MOV      #1$,AGTST      ;ADDRESS FOR EOP
1194 011034 000137 016412          JMP      $EOP          ;TYPE END OF PASS
1195
1196          .SBTTL      AUTO TEST START-UP
1197 011040 004737 002772          BEGINA: JSR      PC,TESTAD      ;SIZE THE # OF MNCAD'S
1198 011044 004737 011254          JSR      PC,WFCHK      ;SET UP IF WESTFIELD MODE
1199 011050 004737 003266          1$: JSR      PC,BEGINL      ;LOGIC TESTS
1200 011054 004737 012410          JSR      PC,BUMPAD      ;UPDATE THE ADDRESSES IF ANY
1201 011060 000773          BR      1$              ;BR AND DO NEXT UNIT
1202 011062 104401 022045          TYPE      ,MEND        ;TELL OPER. END OF LOGIC TEST
1203 011066 004737 011446          2$: JSR      PC,TCHANL      ;REPORT A/D CONFIG. AND IF DWARF MODE
1204                                     ;ASK FOR THE CHANNELS TO TEST
1205                                     JSR      PC,WRAP
1206 011072 004737 005406          JSR      PC,BUMPAD      ;BUMP THE ADDRESSES
1207 011076 004737 012410          BR      2$              ;BR AND DO NEXT UNIT
1208 011102 000771          BR      2$
1209 011104 012737 011050 016610 MOV      #1$,AGTST      ;ADDRESS FOR EOP
1210 011112 000137 016412          JMP      $EOP          ;TYPE END OF PASS
1211
1212          .SBTTL      WRAPAROUND TEST START-UP
1213 011116 004737 002772          BEGINW: JSR      PC,TESTAD      ;SIZE THE # OF MNCAD'S
1214 011122 004737 011254          JSR      PC,WFCHK      ;SET UP IF WESTFIELD MODE
1215 011126 004737 011446          1$: JSR      PC,TCHANL      ;REPORT THE A/D CONFIG. AND IF DWARF
1216                                     ;MODE ASK FOR THE CHANNELS TO TEST
1217                                     JSR      PC,WRAP
1218 011132 004737 005406          JSR      PC,BUMPAD      ;WRAPAROUND TESTS
1219 011136 004737 012410          BR      1$              ;UPDATE BUS ADDRESSES
1220 011142 000771          BR      1$
1221 011144 012737 011126 016610 MOV      #1$,AGTST      ;BR AND TEST NEXT UNIT
1222 011152 000137 016412          JMP      $EOP          ;INCREMENTS $PASS
1223
1224          .SBTTL      NOISE TEST START-UP
1225 011156 004737 012510          BEGINN: JSR      PC,FIXONE      ;ENSURE BASE AND VECTOR SETUP
1226 011162 005037 001440          CLR      NMBEXT        ;CLEAR MULTIPLE UNIT FLAG
1227 011166 104401 017076          TYPE      ,SCHAN      ;ASK FOR STARTING NOISE CHANNEL
1228 011172 104413          RDOCT          ;GET OPER. CHANNEL INPUT
1229 011174 012637 001370          MOV      (SP)+,BASECH      ;SAVE 1ST CHANNEL
1230 011200 104401 017126          TYPE      ,ECHAN      ;ASK FOR END NOISE CHANNEL
1231 011204 104413          RDOCT          ;GET OPER. CHANNEL INPUT
1232 011206 012637 001372          MOV      (SP)+,BASEND      ;SAVE LAST CHANNEL
1233 011212 001006          BNE      1$              ;BR IF NON-ZERO
1234 011214 013737 001370 001372 MOV      BASECH,BASEND      ;TAKE CARE IF ONLY 1 CHANNEL
1235 011222 000240          NOP
1236 011224 000240          NOP
1237 011226 000240          NOP
1238 011230 012737 000001 001416 1$: MOV      #1,WIDE        ;SET MANUAL ENTRY FLAG
1239 011236 004737 006612          JSR      PC,NOITST      ;RUN NOISE TEST
1240 011242 012737 011230 016610 MOV      #1$,AGTST      ;LOAD RETRURN POINTER
1241 011250 000137 016412          JMP      $EOP          ;AND REPORT END OF PASS

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1241
1242
1243
1244 011254 005737 001476
1245 011260 100063
1246 011262 005037 001370
1247 011266 012777 020000 170056
1248 011274 032777 000002 170050
1249 011302 001414
1250 011304 012737 000037 001372
1251 011312 104401 020000
1252 011316 017700 170072
1253 011322 042700 177417
1254 011326 022700 000060
1255 011332 000413
1256 011334 012737 000057 001372 1$:
1257 011342 104401 020022
1258 011346 017700 170042
1259 011352 042700 177417
1260 011356 022700 000340
1261 011362
(1) 011362 001422
1262 011364 104401 020451
1263 011370 104401 020476 3$:
1264 011374 104412
1265 011376 052777 000100 167540
1266 011404 005046
1267 011406 012746 011414
1268 011412 000002
1269 011414 012600
1270 011416 142710 000040
1271 011422 121027 000131
1272 011426 001001
1273 011430 000207
1274 011432 121027 000116
1275 011436 001354
1276 011440 000000
1277 011442 000137 001564
1278

;*ROUTINE TO CHECK FOR PROPER I D CODE IF TESTER MODE
;*IF ON TESTER, SET UP BASECH AND BASEND FOR CHANNELS BEING TESTED

WFCHK: TST WFTST ;RUNNING ON TESTER?
        BPL 5$ ;:BR IF NOT
        CLR BASECH ;:CLEAR STARTING CHANNEL
        MOV #20000,@STREG ;:IS CHANNEL 40 PRESENT?
        BIT #BIT1,@STREG ;:IS THE NON-EXSISTENT CHANNEL BIT SET?
        BEQ 1$ ;:NO, TESTING A/D AND AM
        MOV #37,BASEND ;:SET UP LAST CHANNEL TO TEST
        TYPE ,TSTAD ;:TYPE TESTING A/D MESSAGE
        MOV @DRVDIR,R0 ;:GET I D BITS
        BIC #177417,R0 ;:CLEAR UNWANTED BITS
        CMP #60,R0 ;:IS THE I D CODE CORRECT?
        BR 2$ ;:
        MOV #57,BASEND ;:SET UP LAST CHANNEL TO TEST
        TYPE ,TSTADM ;:TYPE TESTING A/D AND AM MESSAGE
        MOV @DRVDIR,R0 ;:GET I D BITS
        BIC #177417,R0 ;:CLEAR UNWANTED BITS
        CMP #340,R0 ;:IS THE I D CODE CORRECT?
        BEQ 5$ ;:RETURN
        TYPE ,BADID ;:TYPE BAD I D CODE MESSAGE
        TYPE ,YORNO ;:TYPE CONTINUE TESTING MESSAGE
        RDLIN ;:GET RESPONSE
        BIS #100,@$TKS ;:ENABLE KEYBOARD INTERRUPTS
        CLR -(SP) ;:CLEAR PSW
        MOV #4$,-(SP)
        RTI
        MOV (SP)+,R0 4$: ;:READ ANSWER
        BICB #40,(R0) ;:CONVERT OT UPPER CASE
        CMPB (R0),#'Y ;:IS IT Y?
        BNE 6$ ;:NO, CHECK FOR 'N'
        RTS PC ;:RETURN
        CMPB (R0),#'N 5$: ;:IS IT N?
        BNE 3$ ;:NO, ASK AGAIN
        HALT
        JMP @#BEG2 ;:RESTART IF CONTINUED

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1280
1281
1282
1283 011446 005037 001370
1284 011452 005037 001372
1285 011456 104401 001165
1286 011462 005737 001176
1287 011466 001017
1288 011470 005737 001476
1289 011474 001406
1290 011476 100003
1291 011500 104401 020171
1292 011504 000402
1293 011506 104401 020125
1294 011512 104401 020044
1295 011516 104401 021463
1296 011522 104412
1297 011524 005726
1298 011526 104401 017524
1299 011532 004737 026572
1300 011536 013746 001514
1301 011542 104403
1302 011544 001 000
1303 011546 104401 001165
1304 011552 005001
1305 011554 005000
1306 011556 000407
1307 011560 005277 167566
1308 011564 105777 167562
1309 011570 100375
1310 011572 017700 167560
1311 011576
(1) 011576 010146
(1) 011600 104403
(1) 011602 002
(1) 011603 000
1312 011604 104401 017347
1313 011610 062701 000003
1314 011614 042700 007777
1315 011620 001002
1316 011622 062701 000004
1317 011626 022701 000100
1318 011632 101002
1319 011634 012701 000077
1320 011640
(1) 011640 010146
(1) 011642 104403
(1) 011644 002
(1) 011645 000
1321 011646 005700
1322 011650 001003
1323 011652 104401 017353
1324 011656 000410
1325 011660 032700 140000
  
```

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;*ROUTINE TO TYPE OUT A/D CONFIGURATION
;*IF RUNNING IN TEST MODULE MODE, ASK FOR CHANNELS TO TEST

TCHANL: CLR BASECH ;CLEAR FIRST CHANNEL TO TEST
          CLR BASEND ;CLEAR LAST CHANNEL TO TEST
          TYPE , $CRLF ;FRESH LINE
          TST $PASS ;TEST IF FIRST PASS
          BNE 22$ ;BR IF NOT
          TST WFTST ;TEST WESTFIELD FLAG
          BEQ 1$ ;:RUNNING WITH NO TEST MODULE/TESTER
          BPL 21$ ;:RUNNING IN TEST MODULE MODE BUT NO TESTER
          TYPE , SDDIF ;TYPE SET DWARF TO DIFFERENTIAL MESSAGE
          BR 1$ ;:
          21$: TYPE , SDSE ;TYPE SET DWARF TO SINGLE ENDED MESSAGE
          1$: TYPE , SADTST ;TYPE SET A/D TO TEST MESSAGE
          TYPE , CRWR ;TYPE CARRIDGE RETURN WHEN READY MESSAGE
          RDLIN ;WAIT FOR CARRIDGE RETURN
          TST (SP)+ ;POP 1 WORD OFF STACK
          22$: TYPE , VTMSG ;REPORT UNIT #
          JSR PC,WHICHU ;DETERMINE ASCII UNIT #
          MOV UNITBD,-(SP)

          .BYTE 1,0
          TYPE , $CRLF ;LEAVE A BLANK LINE
          CLR R1 ;SET UP STARTING CHANNEL
          CLR R0 ;SET UP FIRST I.D. STATUS
          BR 4$ ;GO TYPE RESULTS
          2$: INC @STREG ;START A CONVERSION
          3$: TSTB @STREG ;WAIT FOR CONVERSION TO FINISH
          BPL 3$
          MOV @ADBUFF,R0 ;GET RESULTS
          4$:

          MOV R1,-(SP) ;:SAVE R1 FOR TYPEOUT
          TYPOS ;:GO TYPE--OCTAL ASCII
          .BYTE 2 ;:TYPE 2 DIGIT(S)
          .BYTE 0 ;:SUPPRESS LEADING ZEROS
          TYPE , MDASH ;TYPE A DASH
          ADD #3,R1 ;ADD 3 TO CHANNEL FOR DIFFERENTIAL
          BIC #7777,R0 ;IS CHANNEL SINGLE ENDED
          BNE 5$ ;:CHANNEL IS NOT SINGLE ENDED
          ADD #4,R1 ;ADD 4 CHANNELS FOR SINGLE ENDED
          5$: CMP #100,R1 ;IS CHANNEL > LAST POSSIBLE CHANNEL
          BHI 6$ ;:NO
          MOV #77,R1 ;YES, SET TO LAST CHANNEL
          6$:

          MOV R1,-(SP) ;:SAVE R1 FOR TYPEOUT
          TYPOS ;:GO TYPE--OCTAL ASCII
          .BYTE 2 ;:TYPE 2 DIGIT(S)
          .BYTE 0 ;:SUPPRESS LEADING ZEROS
          TST R0 ;IS CHANNEL SINGLE ENDED?
          BNE 7$ ;:NO
          TYPE , MSE ;TYPE SINGLE ENDED MESSAGE
          BR 9$ ;:GO TEST MORE CHANNELS
          7$: BIT #BIT15!BIT14,R0 ;DOES CHANNEL HAVE PREAMP?
  
```

1326	011664	001003			BNE	8\$::YES, HAS PREAMP
1327	011666	104401	017373		TYPE	,MDIF	::TYPE DIFFERENTIAL MESSAGE
1328	011672	000402			BR	9\$::GO TEST MORE CHANNELS
1329	011674	104401	017413		8\$: TYPE	,MPRMP	::TYPE PREAMP MESSAGE
1330	011700	005201			9\$: INC	R1	::SET CHANNEL TO NEXT SET OF CHANNELS
1331	011702	022701	000100		CMP	#100,R1	::DONE?
1332	011706	001414			BEQ	10\$::YES
1333	011710	010100			MOV	R1,R0	::GET CHANNEL
1334	011712	000300			SWAB	R0	::PUT CHANNEL NUMBER IN HIGH BYTE
1335	011714	052700	000010		BIS	#BIT3,R0	::SET STATUS ENABLE BIT
1336	011720	010077	167426		MOV	R0,@STREG	::LOAD INTO A/D STATUS REGISTER
1337	011724	032777	000002	167420	BIT	#BIT1,@STREG	::IS NON-EXSISTENT CHANNEL BIT SET?
1338	011732	001712			BEQ	2\$::NO
1339	011734	104401	001165		TYPE	,\$CRLF	
1340					:IF USING TEST MODULE OR TESTER MODE, DO MORE TESTING		
1341					:IF NOT THEN EXIT		
1342	011740	022737	000001	001476	10\$: CMP	#1,WFTST	::RUNNING DWARF MODE?
1343	011746	001117			BNE	20\$::NO
1344	011750	005001			CLR	R1	::SET UP TO ASK FOR FIRST GROUP
1345	011752	004737	012210		11\$: JSR	PC,ASKC	::ASK TO TEST CHANNELS
1346	011756	000434			BR	14\$::YES
1347	011760	062701	000010		ADD	#10,R1	::INDEX TO NEXT CHANNEL BANK
1348	011764	010100			12\$: MOV	R1,R0	::PUT CHANNEL INTO R0
1349	011766	022700	000100		CMP	#100,R0	::ANY MORE CHANNELS?
1350	011772	001762			BEQ	10\$::NO
1351	011774	000300			SWAB	R0	::PUT CHANNEL IN HIGH BYTE
1352	011776	052700	000010		BIS	#BIT3,R0	::SET STATUS ENABLE BIT
1353	012002	010077	167344		MOV	R0,@STREG	::LOAD INTO A/D STATUS REGISTER
1354	012006	032777	000002	167336	BIT	#BIT1,@STREG	::IS THE NON-EXSISTENT CHANNEL BIT SET?
1355	012014	001351			BNE	10\$::YES
1356	012016	005277	167330		INC	@STREG	::START A CONVERSION
1357	012022	105777	167324		13\$: TSTB	@STREG	::WAIT FOR CONVERSION TO FINISH
1358	012026	100375			BPL	13\$::
1359	012030	017700	167322		MOV	@ADBUFF,R0	::GET RESULTS
1360	012034	042700	007777		BIC	#7777,R0	::IS CHANNEL SINGLE ENDED?
1361	012040	001744			BEQ	11\$::YES
1362	012042	062701	000004		ADD	#4,R1	::INDEX TO NEXT CHANNEL BANK
1363	012046	000746			BR	12\$::
1364	012050	010137	001370		14\$: MOV	R1,BASECH	::SAVE FIRST CHANNEL TO TEST
1365	012054	062701	000010		15\$: ADD	#10,R1	::INDEX TO NEXT BANK
1366	012060	010100			16\$: MOV	R1,R0	::PUT CHANNEL INTO R0
1367	012062	022700	000100		CMP	#100,R0	::ANY MORE BANKS?
1368	012066	001426			BEQ	19\$::NO
1369	012070	000300			SWAB	R0	::PUT CHANNEL INTO HIGH BYTE
1370	012072	052700	000010		BIS	#BIT3,R0	::SET STATUS ENABLE BIT
1371	012076	010077	167250		MOV	R0,@STREG	::LOAD INTO A/D STATUS REGISTER
1372	012102	032777	000002	167242	BIT	#BIT1,@STREG	::IS THE NON-EXSISTENT CHANNEL BIT SET?
1373	012110	001015			BNE	19\$::YES
1374	012112	005277	167234		INC	@STREG	::START A CONVERSION
1375	012116	105777	167230		17\$: TSTB	@STREG	::WAIT FOR CONVERSION TO FINISH
1376	012122	100375			BPL	17\$::
1377	012124	017700	167226		MOV	@ADBUFF,R0	::GET RESULTS
1378	012130	042700	007777		BIC	#7777,R0	::IS CHANNEL SINGLE ENDED?
1379	012134	001003			BNE	19\$::BR IF NOT

1380 012136 004737 012210
 1381 012142 000744
 1382 012144 005301
 1383 012146 010137 001372
 1384 012152 104401 017721
 1385 012156 013746 001370
 (1) 012162 104403
 (1) 012164 002
 (1) 012165 000
 1386 012166 104401 017347
 1387 012172 013746 001372
 (1) 012176 104403
 (1) 012200 002
 (1) 012201 000
 1388 012202 104401 001165
 1389 012206 000207
 1390
 1391
 1392
 1393 012210 104401 017721
 1394 012214 010146
 (1) 012216 104403
 (1) 012220 002
 (1) 012221 000
 1395 012222 104401 017347
 1396 012226 010100
 1397 012230 062700 000007
 1398 012234 010046
 (1) 012236 104403
 (1) 012240 002
 (1) 012241 000
 1399 012242 104401 017345
 1400 012246 104412
 1401 012250 012600
 1402 012252 142710 000040
 1403 012256 122710 000131
 1404 012262 001410
 1405 012264 122710 000116
 1406 012270 001403
 1407 012272 104401 017745
 1408 012276 000744
 1409 012300 062716 000002
 1410 012304 000207

```

18$: JSR PC,ASKC ;ASK TO TEST CHANNELS
      BR 15$ ;
19$: DEC R1 ;DECREMENT CHANNEL
      MOV R1,BASEND ;SAVE LAST CHANNEL TO TEST
      TYPE ,TCHAN ;TYPE 'TESTING CHANNELS '
      MOV BASECH,-(SP) ;SAVE BASECH FOR TYPEOUT
      TYPOS ;GO TYPE--OCTAL ASCII
      .BYTE 2 ;TYPE 2 DIGIT(S)
      .BYTE 0 ;SUPPRESS LEADING ZEROS
      TYPE ,MDASH ;TYPE ' - '
      MOV BASEND,-(SP) ;SAVE BASEND FOR TYPEOUT
      TYPOS ;GO TYPE--OCTAL ASCII
      .BYTE 2 ;TYPE 2 DIGIT(S)
      .BYTE 0 ;SUPPRESS LEADING ZEROS
      TYPE ,$CRLF ;TYPE A CARRIDGE RETURN, LINE FEED
20$: RTS PC ;RETURN

;*ROUTINE TO ASK CHANNELS TO TEST
ASKC: TYPE ,TCHAN ;TYPE 'TEST CHANNELS '
      MOV R1,-(SP) ;SAVE R1 FOR TYPEOUT
      TYPOS ;GO TYPE--OCTAL ASCII
      .BYTE 2 ;TYPE 2 DIGIT(S)
      .BYTE 0 ;SUPPRESS LEADING ZEROS
      TYPE ,MDASH ;TYPE ' - '
      MOV R1,R0 ;PUT CHANNEL INTO R0
      ADD #7,R0 ;GET LAST CHANNEL IN GROUP
      MOV R0,-(SP) ;SAVE R0 FOR TYPEOUT
      TYPOS ;GO TYPE--OCTAL ASCII
      .BYTE 2 ;TYPE 2 DIGIT(S)
      .BYTE 0 ;SUPPRESS LEADING ZEROS
      TYPE ,QUEST ;TYPE ' ? '
      RDLIN ;GET RESPONSE
      MOV (SP)+,R0 ;GET ADDRESS OF RESPONSE TEXT
      BICB #40,(R0) ;MAKE CHARACTER UPPER CASE
      CMPB #'Y,(R0) ;IS IT A Y?
      BEQ 2$ ;YES
      CMPB #'N,(R0) ;IS IT AN N?
      BEQ 1$ ;YES
      TYPE ,YESNO ;TYPE 'TYPE Y FOR YES, N FOR NO'
      BR ASKC ;
1$: ADD #2,(SP) ;SKIP OVER BRANCH
2$: RTS PC ;RETURN
  
```

```

1412
1413 ;SUBROUTINE TO CHANGE BASE AND VECTOR ADDRESSES
1414 012306 104401 017425 BASEXC: TYPE ,MADR ;ASK FOR MODULE ADDRESS
1415 012312 013746 001244 MOV $BASE,-(SP) ;;SAVE $BASE FOR TYPEOUT
(1) 012316 104402 TYPOC ;;GO TYPE--OCTAL ASCII(ALL DIGITS)
1416 012320 104401 017517 TYPE ,ENCOM
1417 012324 104413 RDOCT
1418 012326 005726 TST (SP)+ ;DEFAULT ADDRESS ?
1419 012330 001403 BEQ 5$ ;NO BRANCH
1420 012332 016637 177776 001244 MOV -2(SP), $BASE ;SAVE ADDRESS IN $BASE
1421 012340 104401 017461 5$: TYPE ,MVCT ;ASK FOR MODULE VECTOR
1422 012344 013701 001240 MOV $VECT1,R1 ;GET VECTOR
1423 012350 010146 MOV R1,-(SP) ;;SAVE R1 FOR TYPEOUT
(1) 012352 104403 TYPOS ;;GO TYPE--OCTAL ASCII
(1) 012354 003 .BYTE 3 ;;TYPE 3 DIGIT(S)
(1) 012355 001 .BYTE 1 ;;TYPE LEADING ZEROS
1424 012356 104401 017517 TYPE ,ENCOM
1425 012362 104413 RDOCT
1426 012364 005726 TST (SP)+ ;TAKE DEFAULT ?
1427 012366 001403 BEQ 7$
1428 012370 016637 177776 001240 MOV -2(SP), $VECT1
1429 012376 052737 100000 001240 7$: BIS #BIT15, $VECT1 ;SET PRIORITY LEVEL
1430 012404 000137 002536 JMP MTEST1 ;RESTART
  
```

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1432          .SBTTL      DETERMINE IF MORE MNCAD'S TO BE TESTED
1433 012410 005737 001436  BUMPAD: TST      NBEXT          ;ADDITIONAL AD'S?
1434 012414 001433          BEQ      FIXADR        ;NO-INITIALIZE ADDRESSES
1435 012416 006337 001512  ASL      MASKNM          ;MOVE BIT TO NEXT MODULE
1436 012422 005001          CLR      R1
1437 012424 013700 001512  MOV      MASKNM,R0      ;GET MASK NUMBER
1438 012430 006200 1$:    ASR      R0          ;MOVE RIGHT
1439 012432 001403          BEQ      2$          ;BR IF DONE
1440 012434 062701 000004  ADD      #4,R1          ;UPDATE INDEX VALUE
1441 012440 000773          BR      1$
1442 012442 016137 001332 001352 2$:    MOV      MNCAD0(R1),STREG ;GET NEW ADDRESS
1443 012450 062701 000002  ADD      #2,R1          ;NEW NEXT INDEX
1444 012454 016137 001332 001360  MOV      MNCAD0(R1),VECTOR ;GET NEW VECTOR
1445 012462 013737 001352 001354  MOV      STREG,ADST1    ;PRIME OTHER ADDRESSES
1446 012470 013737 001352 001356  MOV      STREG,ADBUFF
1447 012476 005337 001436  DEC      NBEXT          ;ONE LESS MNCAD
1448 012502 000427          BR      BYPASS
1449 012504 062716 000002  FIXADR: ADD     #2,(SP)
1450 012510 012737 016612 000004  FIXONE: MOV    #IOTRD,@#ERRVEC ;SET UP ERRVEC
1451 012516 012737 000001 001512  MOV      #1,MASKNM      ;INIT. MODULE ERROR TEST BIT
1452 012524 013737 001244 001352  MOV      $BASE,STREG    ;RELOAD INITIAL ADDRESSES
1453 012532 013737 001244 001354  MOV      $BASE,ADST1
1454 012540 013737 001244 001356  MOV      $BASE,ADBUFF
1455 012546 013737 001240 001360  MOV      $VECT1,VECTOR  ;GET DEFAULT VECTOR
1456 012554 013737 001440 001436  MOV      NMBEXT,NBEXT  ;RESET UNIT COUNTER
1457 012562 005237 001354  BYPASS: INC    ADST1
1458 012566 062737 000002 001356  ADD      #2,ADBUFF
1459 012574 042737 170000 001360  BIC      #170000,VECTOR
1460 012602 013737 001360 001362  MOV      VECTOR,VECTR1
1461 012610 062737 000002 001362  ADD      #2,VECTR1
1462 012616 013737 001360 001364  MOV      VECTOR,VECTR2
1463 012624 062737 000004 001364  ADD      #4,VECTR2
1464 012632 013737 001360 001366  MOV      VECTOR,VECTR3
1465 012640 062737 000006 001366  ADD      #6,VECTR3
1466          ;;LOAD .+2 AND JSR PC,R0 TRAP CATCHER;;
1467 012646 012700 000222  MOV      #222,R0        ;FILL .+2
1468 012652 012701 000220  MOV      #220,R1        ;LOAD JSR PC,R0
1469 012656 010021 1$:    MOV      R0,(R1)+
1470 012660 012721 004700  MOV      #4700,(R1)+
1471 012664 010100  MOV      R1,R0
1472 012666 005720  TST      (R0)+
1473 012670 020027 001002  CMP      R0,#1002
1474 012674 001370  BNE      1$
1475 012676 004737 026572  JSR      PC,WHICHU     ;DETERMINE UNIT #
1476 012702 090207  RTS      PC            ;TEST NEXT A/D

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1478 012704 104416          TYPSET: TYPDC
1479 012706 104401 021021   TYPE      ,LSB
1480 012712 013746 001434   MOV      CH2,-(SP)      ;;SAVE CH2 FOR TYPEOUT
(1)                                     ;;TYPE CH
(1) 012716 104403          TYPOS
(1) 012720      002        .BYTE    2              ;;GO TYPE--OCTAL ASCII
(1) 012721      000        .BYTE    0              ;;TYPE 2 DIGIT(S)
1481 012722 104401 021056   TYPE      ,ATMSG        ;;SUPPRESS LEADING ZEROS
1482 012726 004737 013122   JSR      PC,TYPEDG      ;TYPE ASCIZ STRING
1483 012732 104401 021034   TYPE      ,SETCH
1484 012736 013746 001432   MOV      CH1,-(SP)      ;;SAVE CH1 FOR TYPEOUT
(1)                                     ;;TYPE CH
(1) 012742 104403          TYPOS
(1) 012744      002        .BYTE    2              ;;GO TYPE--OCTAL ASCII
(1) 012745      000        .BYTE    0              ;;TYPE 2 DIGIT(S)
1485 012746 104401 021056   TYPE      ,ATMSG        ;;SUPPRESS LEADING ZEROS
1486 012752 013737 001432 012772   MOV      CH1,1$
1487 012760 012777 000200 166370   MOV      #200,@ADBUFF
1488 012766 004537 015352   JSR      R5,CONVRT
1489 012772 000000          1$:      0
1490 012774 013746 001426   MOV      TEMP,-(SP)     ;;SAVE TEMP FOR TYPEOUT
(1)                                     ;;TYPE VALUE
(1) 013000 104403          TYPOS
(1) 013002      004        .BYTE    4              ;;GO TYPE--OCTAL ASCII
(1) 013003      001        .BYTE    1              ;;TYPE 4 DIGIT(S)
1491 013004 020437 016364   CMP      R4,VSET        ;;TYPE LEADING ZEROS
1492 013010 003003          BGT      ERR
1493 013012 104401 021132   TYPE      ,OKMSG
1494 013016 000207          RTS      PC
1495 013020 104401 021606   ERR:    TYPE      ,ERMSG
1496 013024 004737 026564   JSR      PC,WHICHV      ;INDICATE BAD UNIT
1497 013030 005237 001112   INC      $ERTTL         ;UPDATE ERROR TOTAL
1498 013034 000207          RTS      PC
1499
1500          ;;SUBROUTINE FOR SETTLING TESTS;;
1501 013036 012737 013120 007770   SET1A:  MOV      #1$,ERRADR      ;SET UP ERROR RECOVERY ADDRESS
1502 013044 013737 001434 001442   MOV      CH2,DUMMY        ;LOAD DUMMY
1503 013052 004537 013224          JSR      R5,SARSUB        ;DO SAR ROUTINE AT 50%
1504 013056 000062          50.
1505 013060 004737 007744          JSR      PC,TSTDAC        ;CHECK VERNIER DAC SETTING
1506 013064 063702 001464          ADD      DAC,R2          ;ADD RESULT TO R2
1507 013070 013737 001432 001442   MOV      CH1,DUMMY        ;CHANGE DUMMY VALUE
1508 013076 004537 013224          JSR      R5,SARSUB        ;DO SAR ROUTINE AT 50%
1509 013102 000062          50.
1510 013104 004737 007744          JSR      PC,TSTDAC        ;CHECK VERNIER DAC SETTING
1511 013110 163702 001464          SUB      DAC,R2          ;SUBTRACT RESULT FROM R2
1512 013114 062716 000002          ADD      #2,(SP)        ;BUMP RETURN ADDRESS TO SKIP OVER BRANCH
1513 013120 000207          1$:      RTS      PC      ;RETURN

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1515      ;;SUBROUTINE TO TYPE EDGE VALUES;;
1516 013122 013703 001470  TYPEDG: MOV    EDGE,R3
1517 013126 010346          MOV    R3,-(SP)      ;;SAVE R3 FOR TYPEOUT
(1)      ;;TYPE OCTAL VALUE OF EDGE
(1) 013130 104403          TYPOS      ;;GO TYPE--OCTAL ASCII
(1) 013132    004          .BYTE    4      ;;TYPE 4 DIGIT(S)
(1) 013133    001          .BYTE    1      ;;TYPE LEADING ZEROS
1518 013134 023727 013164 000001  CMP    EDGFLG,#1
1519 013142 001407          BEQ    RET
1520 013144 062703 000007  ADD    #7,R3
1521 013150 104401 017343  TYPE    ,MINUS      ;TYPE ASCII STRING
1522 013154 010346          MOV    R3,-(SP)      ;;SAVE R3 FOR TYPEOUT
(1)      ;;TYPE EDGE VALUE
(1) 013156 104403          TYPOS      ;;GO TYPE--OCTAL ASCII
(1) 013160    004          .BYTE    4      ;;TYPE 4 DIGIT(S)
(1) 013161    001          .BYTE    1      ;;TYPE LEADING ZEROS
1523 013162 000207          RET:    RTS    PC
1524 013164 000000  EDGFLG: 0
1525      ;SUBROUTINE TO LOAD VECTOR AREA WITH TRAP CATCHER
1526 013166 012700 000222  SETINT: MOV    #222,R0      ;LOAD UP POINTER
1527 013172 012701 000220          MOV    #220,R1      ;LOAD ADDRESS
1528 013176 010021          2$:    MOV    R0,(R1)+    ;LOAD POINTER TO NEXT WORD
1529 013200 012721 004700          MOV    #4700,(R1)+   ;LOAD 'BAD' INSTRUCTION
1530 013204 010100          MOV    R1,R0      ;LOAD NEW ADDRESS POINTER
1531 013206 005720          TST    (R0)+      ;BUMP VALUE
1532 013210 022700 001002  CMP    #1002,R0     ;FINISHED?
1533 013214 001370          BNE    2$         ;BR IF NOT
1534 013216 000240          NOP
1535 013220 000240          NOP
1536 013222 000207          RTS    PC      ;EXIT

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1538
1539
1540      ;SUBROUTINE TO DO SUCCESSIVE APPROXIMATION ROUTINE
1541      ;CALL=JSR  R5,SARSUB
1542      ;   XXX;XXX=PERCENT
1543      ;RESULT RETURNED IN 'DAC',USES R0,R1,R4
1544 013224 012537 001502 SARSUB: MOV      (R5)+,PERCNT      ;GET PERCENT
1545 013230 006337 001502      ASL      PERCNT
1546 013234 006337 001502      ASL      PERCNT
1547 013240 006337 001502      ASL      PERCNT      ;RESCALE PERCENT FOR 1600.
1548 013244 006337 001502      ASL      PERCNT      ;POINTS PER BURST
1549 013250 012737 000200 001472      MOV      #200,BITPNT      ;INITIALIZE BIT POINTER AT MSB
1550 013256 005037 001464      CLR      DAC      ;INITIALIZE DAC VALUE
1551 013262 005000      TRY:      CLR      R0
1552 013264 063737 001472 001464      ADD      BITPNT,DAC      ;TRY BIT
1553 013272 013777 001464 166056      MOV      DAC,@ADBUFF
1554 013300 012701 003100      MOV      #1600.,R1      ;SET UP FOR 1600. CONVERSIONS
1555 013304 113777 001442 166042 NXTCVT: MOVB     DUMMY,@ADST1      ;PRESET MUX TO DUMMY CHANNEL
1556 013312 012777 001540 166040      MOV      #RETURN,@VECTOR      ;RETURN ADDRESS
1557 013320 052777 000101 166024      BIS      #101,@STREG      ;CONVERSION ON DUMMY CHANNEL
1558 013326 000001      WAIT
1559 013330 017704 166022      MOV      @ADBUFF,R4      ;DUMMY READ
1560 013334 013704 001444      MOV      CHANL,R4
1561 013340 000304      SWAB     R4
1562 013342 052704 000101      BIS      #101,R4      ;INTERRUPT ENABLE START
1563 013346 010477 166000      MOV      R4,@STREG      ;JUMP TO CHANNEL + START CONVERT
1564 013352 000001      WAIT      ;WAIT FOR INTERRUPT
1565 013354 027737 165776 001470      CMP      @ADBUFF,EDGE
1566 013362 002001      BGE     2$
1567 013364 005200      INC     R0      ;COUNT RESULTS .LT. EDGE
1568 013366 005301      2$:     DEC     R1
1569 013370 001345      BNE     NXTCVT
1570 013372 020037 001502      CMP     R0,PERCNT
1571 013376 003003      BGT     SHIFT
1572 013400 163737 001472 001464      SUB     BITPNT,DAC      ;TAKE THE BIT OUT
1573 013406 006237 001472      SHIFT: ASR     BITPNT
1574 013412 001323      BNE     TRY
1575 013414 000205      RTS     R5
1576
1577      ;ROUTINE TO DELAY IF PROCESSER CAN NOT DO SOB INSTRUCTION
1578
1579 013416 005300      DELAY4: DEC     R0      ;DECREMENT R0, IS IT ZERO?
1580 013420 001376      BNE     DELAY4      ;NO
1581 013422 000002      RTI      ;RETURN

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1583      ;:DIFFERENTIAL LINEARITY SUBROUTINE;;
1584 013424 104401 022203  DIFLIN: TYPE      ,MSG20      ;IDENTIFY TEST
1585 013430 004737 026572      JSR      PC,WHICHU    ;DETERMINE UNIT #
1586 013434 013746 001514      MOV      UNITBD,-(SP)
1587 013440 104403      TYPOS      ;TELL OPER. THE #
1588 013442      001      000      .BYTE      1,0
1589 013444 104401 001165      TYPE      ,SCLF
1590 013450 013702 001446      MOV      RNA,R2      ;SET UP RANDOM NUMBER GENERATOR
1591 013454 013704 001450      MOV      RNB,R4
1592 013460 013705 001452      MOV      RNC,R5
1593 013464 012700 031604      MOV      #BUFFER,R0
1594 013470 012701 010000      MOV      #4096.,R1    ;4096 WORDS FOR HISTOGRAM
1595 013474 005020      CLEAR1: CLR      (R0)+  ;CLEAR BUFFER AREA
1596 013476 005301      DEC      R1
1597 013500 001375      BNE      CLEAR1
1598 013502 012700 030764      MOV      #DIST,R0    ;DISTRIBUTION BUFFER POINTER
1599 013506 012701 000310      MOV      #200.,R1    ;200. WORDS FOR DISTRIBUTION
1600 013512 005003      CLR      R3
1601 013514 005037 001504      CLR      OUT
1602 013520 005037 001416      CLR      WIDE
1603 013524 005037 001420      CLR      NARROW
1604 013530 005037 001422      CLR      FIRST
1605 013534 005037 001424      CLR      SKIPST
1606 013540 005020      CLEAR2: CLR      (R0)+  ;CLEAR DISTRIBUTION BUFFER AREA
1607 013542 005301      DEC      R1
1608 013544 001375      BNE      CLEAR2
1609 013546 012700 000003      MOV      #3,R0      ;CHANNEL 3
1610 013552 000300      SWAB     R0          ;LOAD MUX BITS
1611 013554 052700 000100      BIS      #100,R0
1612 013560 010077 165566      MOV      R0,@STREG
1613 013564 012737 001440 001466      MOV      #800.,DELAY ;NOMINAL STATE WIDTH - 1 LSB
1614 013572 012777 001550 165560      MOV      #RET1,@VECTOR
1615 013600 012701 007776      AGAIN: MOV      #4094.,R1
1616 013604 060402      NEXT1: ADD      R4,R2    ;GENERATE A RANDOM NUMBER
1617 013606 060502      ADD      R5,R2
1618 013610 005502      ADC      R2
1619 013612 010200      MOV      R2,R0      ;PUT RANDOM NUMBER IN R0
1620 013614 042700 177770      BIC      #177770,R0  ;MASK IT TO 3 BITS ONLY
1621 013620 001401      BEQ      CONVR1
1622 013622 077001      DELAY1: SOB     R0,DELAY1 ;STALL TIME
1623 013624 005277 165522      CONVR1: INC     @STREG  ;START CONVERSION
1624 013630 000001      WAIT
1625 013632 000240      NOP
1626 013634 017700 165516      MOV      @ADBUFF,R0  ;GET CONVERTED VALUE
1627 013640 001416      BEQ      LODLY1     ;IGNORE IF =0
1628 013642 020027 007777      CMP      R0,#7777   ;IGNORE IF =7777
1629 013646 001416      BEQ      HIDLY1
1630 013650 006300      ASL     R0
1631 013652 005260 031604      INC     BUFFER(R0)  ;MAKE HISTOGRAM
1632 013656 100016      BPL     OKAY1
1633 013660 012760 077777 031604      MOV     #077777,BUFFER(R0) ;PREVENT OVERFLOW
1634 013666 000412      BR      OKAY1
1635 013670 005037 001426      NOTOK1: CLR     TEMP
1636 013674 000407      BR      OKAY1

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1637 013676 020027 007777      LODLY1:  CMP      R0,#7777      ;EQUALIZE LOOP TIME
1638 013702 001400              BEQ      HIDLY1      ;WITH DUMMY INSTR.
1639 013704 005201              HIDLY1:  INC      R1
1640 013706 005263 001426              INC      TEMP(R3)
1641 013712 100766              BMI      NOTOK1
1642 013714 005301              OKAY1:  DEC      R1
1643 013716 001514              BEQ      AROUND
1644 013720 060204              ADD      R2,R4      ;GENERATE A RANDOM NUMBER
1645 013722 060504              ADD      R5,R4
1646 013724 005504              ADC      R4
1647 013726 010400              MOV      R4,R0      ;PUT RANDOM NUMBER IN R0
1648 013730 042700 177770              BIC      #177770,R0 ;MASK IT TO 3 BITS ONLY
1649 013734 001401              BEQ      CONVR2
1650 013736 077001              DELAY2: SOB      R0,DELAY2 ;STALL TIME
1651 013740 005277 165406              CONVR2: INC      @STREG ;START CONVERSION
1652 013744 000001              WAIT
1653 013746 000240              NOP
1654 013750 017700 165402              MOV      @ADBUFF,R0 ;GET CONVERTED VALUE
1655 013754 001416              BEQ      LODLY2      ;IGNORE IF =0
1656 013756 020027 007777              CMP      R0,#7777 ;IGNORE IF =7777
1657 013762 001416              BEQ      HIDLY2
1658 013764 006300              ASL      R0
1659 013766 005260 031604              INC      BUFFER(R0) ;MAKE HISTOGRAM
1660 013772 100016              BPL      OKAY2
1661 013774 012760 077777 031604              MOV      #077777,BUFFER(R0) ;PREVENT OVERFLOW
1662 014002 000412              BR
1663 014004 005037 001426              NOTOK2: CLR      TEMP
1664 014010 000407              BR      OKAY2
1665 014012 020027 007777              LODLY2: CMP      R0,#7777 ;EQUALIZE LOOP TIME
1666 014016 001400              BEQ      HIDLY2      ;WITH DUMMY INSTR.
1667 014020 005201              HIDLY2: INC      R1
1668 014022 005263 001426              INC      TEMP(R3)
1669 014026 100766              BMI      NOTOK2
1670 014030 005301              OKAY2:  DEC      R1
1671 014032 001446              BEQ      AROUND
1672 014034 060205              ADD      R2,R5      ;GENERATE A RANDOM NUMBER
1673 014036 060405              ADD      R4,R5
1674 014040 005505              ADC      R5
1675 014042 010500              MOV      R5,R0      ;PUT RANDOM NUMBER IN R0
1676 014044 042700 177770              BIC      #177770,R0 ;MASK IT TO 3 BITS ONLY
1677 014050 001401              BEQ      CONVR3
1678 014052 077001              DELAY3: SOB      R0,DELAY3 ;STALL TIME
1679 014054 005277 165272              CONVR3: INC      @STREG ;START CONVERSION
1680 014060 000001              WAIT
1681 014062 000240              NOP
1682 014064 017700 165266              MOV      @ADBUFF,R0 ;GET CONVERTED VALUE
1683 014070 001416              BEQ      LODLY3      ;IGNORE IF =0
1684 014072 020027 007777              CMP      R0,#7777 ;IGNORE IF =7777
1685 014076 001416              BEQ      HIDLY3
1686 014100 006300              ASL      R0
1687 014102 005260 031604              INC      BUFFER(R0) ;MAKE HISTOGRAM
1688 014106 100016              BPL      OKAY3
1689 014110 012760 077777 031604              MOV      #077777,BUFFER(R0) ;PREVENT OVERFLOW
1690 014116 000412              BR      OKAY3

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1691 014120 005037 001426      NOTOK3: CLR      TEMP
1692 014124 000407              BR      OKAY3
1693 014126 020027 007777      LODLY3: CMP      R0,#7777      ;EQUALIZE LOOP TIME
1694 014132 001400              BEQ      HIDLY3      ;WITH DUMMY INSTR.
1695 014134 005201              HIDLY3: INC      R1
1696 014136 005263 001426      INC      TEMP(R3)
1697 014142 100766              BMI      NOTOK3
1698 014144 005301              OKAY3: DEC      R1
1699 014146 001216              BNE      NEXT1
1700 014150 005337 001466      AROUND: DEC      DELAY
1701 014154 001211              BNE      AGAIN
1702              ;TAKE THE CONTENTS OF THE ACQUIRED DATA BUFFER AND
1703              ;TEST IF WITHIN CERTAIN LIMITS
1704              ;AND CREATE A STATE DISTRIBUTION BUFFER
1705              ;AND SORT THE VALUES INTO 'BINS'
1706 014156 012700 007776      MOV      #4094.,R0
1707 014162 012701 031606      MOV      #BUFFER+2,R1
1708 014166 012102      READ: MOV      (R1)+,R2      ;GET STATE WIDTH
1709 014170 006202              ASR      R2      ;1 LSB = 800.
1710 014172 006202              ASR      R2
1711 014174 006202              ASR      R2
1712 014176 005502              ADC      R2      ;1 LSB = 100.
1713 014200 020227 000310      CMP      R2,#200.      ;OUT OF RANGE?
1714 014204 002403              BLT      INRNGE
1715 014206 005237 001504              INC      OUT      ;YES - INCREMENT COUNTER
1716 014212 000423              BR      TYPBAD
1717 014214 006302      INRNGE: ASL      R2
1718 014216 005262 030764      INC      DIST(R2)      ;MAKE STATE WIDTH DISTRIBUTION
1719 014222 006202              ASR      R2
1720 014224 020227 000062      CMP      R2,#50.      ;IS IT 1/2 LSB?
1721 014230 002007              BGE      NOTNAR
1722 014232 005237 001420      INC      NARROW
1723 014236 005702              TST      R2      ;IS IT A SKIPPED STATE?
1724 014240 001002              BNE      31$
1725 014242 005237 001424      INC      SKIPST
1726 014246 000405      31$: BR      TYPBAD
1727 014250 020227 000226      NOTNAR: CMP      R2,#150.      ;IS IT 1.5 LSB?
1728 014254 003425              BLE      LAST
1729 014256 005237 001416      INC      WIDE
1730 014262 005737 001422      TYPBAD: TST      FIRST
1731 014266 001004              BNE      60$
1732 014270 005237 001422      INC      FIRST
1733 014274 104401 020771      TYPE    ,STATE
1734 014300 010103      60$: MOV      R1,R3
1735 014302 162703 031606      SUB      #BUFFER+2,R3
1736 014306 006203              ASR      R3
1737 014310 010346      MOV      R3,-(SP)      ;;SAVE R3 FOR TYPEOUT
(1)              ;;TYPE STATE
(1) 014312 104403              TYPOS      ;;GO TYPE--OCTAL ASCII
(1) 014314 004              .BYTE 4      ;;TYPE 4 DIGIT(S)
(1) 014315 001              .BYTE 1      ;;TYPE LEADING ZEROS
1738 014316 104401 020765      TYPE    ,DASH
1739 014322 104416      TYPDC
1740 014324 104401 020756      TYPE    ,LSBMSG

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1741 014330 005300 LAST: DEC RO
1742 014332 001315 BNE READ
1743 :REPORT TO THE OPERATOR THE DIFFERENT STATE VALUES
1744 : IN THE FORM OF A GENERAL STATUS AND INDICATE OK/ERROR
1745 014334 112737 000177 024545 MOVB #177,DECPNT
1746 014342 013702 001424 MOV SKIPST,R2 ;GET NO. OF SKIPPED STATES
1747 014346 104416 TYPDC ;TYPE IT
1748 014350 104401 021623 TYPE ,SKPMSG ;TYPE MESSAGE
1749 014354 005737 001424 TST SKIPST
1750 014360 001407 BEQ 1$
1751 014362 104401 021606 TYPE ,ERMSG ;TYPE 'ERROR'
1752 014366 004737 026564 JSR PC,WHICHV ;INDICATE BAD UNIT
1753 014372 005237 001112 INC $ERTTL ;UPDATE ERROR COUNT
1754 014376 000402 BR NAR
1755 014400 104401 021132 1$: TYPE ,OKMSG ;TYPE #OK#
1756 014404 013702 001420 NAR: MOV NARROW,R2 ;GET NO. OF NARROW STATES
1757 014410 104416 TYPDC ;TYPE IT
1758 014412 104401 021645 TYPE ,NARMSG ;TYPE MESSAGE
1759 014416 013702 001416 MOV WIDE,R2
1760 014422 063702 001504 ADD OUT,R2
1761 014426 104416 TYPDC ;TYPE NO. OF WIDE STATES
1762 014430 104401 021704 TYPE ,WIDMSG ;TYPE MESSAGE
1763 014434 013702 001504 MOV OUT,R2
1764 014440 104416 TYPDC ;TYPE NO. OF STATES OUTSIDE 2 LSB
1765 014442 104401 021743 TYPE ,OUTMSG ;TYPE MESSAGE
1766 014446 005737 001504 TST OUT
1767 014452 001407 BEQ 11$
1768 014454 104401 021606 TYPE ,ERMSG ;TYPE 'ERROR'
1769 014460 004737 026564 JSR PC,WHICHV ;DETERMINE BAD UNIT
1770 014464 005237 001112 INC $ERTTL ;UPDATE ERROR COUNT
1771 014470 000402 BR HALF
1772 014472 104401 021132 11$: TYPE ,OKMSG ;TYPE 'OK'
1773 014476 013702 001420 HALF: MOV NARROW,R2
1774 014502 063702 001416 ADD WIDE,R2
1775 014506 063702 001504 ADD OUT,R2
1776 014512 010200 MOV R2,R0
1777 014514 104416 TYPDC ;TYPE NO. OF STATES OUTSIDE LIMITS
1778 014516 112737 000056 024545 MOVB #56,DECPNT
1779 014524 104401 021776 TYPE ,HAFMSG
1780 014530 020027 000051 CMP RO,#41. ;COMPARE IT TO NOMINAL
1781 014534 003407 BLE 21$
1782 014536 104401 021606 TYPE ,ERMSG ;TYPE 'ERROR'
1783 014542 004737 026564 JSR PC,WHICHV ;INDICATE BAD UNIT
1784 014546 005237 001112 INC $ERTTL ;UPDATE ERROR COUNT
1785 014552 000402 BR SWDIST
1786 014554 104401 021132 21$: TYPE ,OKMSG ;TYPE 'OK'
1787 :DETERMINE IF VT55 TYPE TERMINAL IS CONNECTED
1788 : IF NOT BYPASS THIS SECTION
1789 : IF VT55/VT105 GRAHIC TERMINAL REPORT THE DISTRIBUTION CURVE
1790 014560 005737 001460 SWDIST: TST FLAG ;BIT MAP TERMINAL AVAILABLE?
1791 014564 001426 BEQ RELACC ;BR IF NOT
1792 014566 004737 015254 JSR PC,DELCLR ;WAIT AWHILE, THEN CLEAR BIT MAP TERMINAL
1793 014572 104401 022247 TYPE ,MSG16
1794 014576 104401 023027 TYPE ,BUFF1 ;TYPE BUFF1-PRINT GRID

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1795	014602	012700	030764	MOV	#DIST,R0	;POINTER TO STATE WIDTH DISTRIBUTION
1796	014606	012701	000310	MOV	#200.,R1	;GO 200. TIMES UP TO 2 LSB
1797	014612	012002		NXTY1: MOV	(R0)+,R2	
1798	014614	004737	015750	JSR	PC,LOADY	
1799	014620	005002		CLR	R2	
1800	014622	004737	015750	JSR	PC,LOADY	
1801	014626	005301		DEC	R1	
1802	014630	001370		BNE	NXTY1	
1803	014632	104401	022765	TYPE	,C2	;TYPE ASCII STRING
1804	014636	004737	015254	JSR	PC,DELCLR	

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1806          ;CHANGE HISTOGRAM ERROR TO RELATIVE ACCURACY ERROR
1807
1808 014642 005001 RELACC: CLR R1          ;RUNNING ERROR = 0
1809 014644 005003 CLR R3          ;MAXIMUM ERROR = 0
1810 014646 104401 022574 TYPE ,MSG21
1811 014652 012700 031606 MOV #BUFFER+2,R0
1812 014656 011002 NXTSTA: MOV (R0),R2 ;STATE WIDTH = R2
1813 014660 162702 001440 SUB #800.,R2 ;STATE WIDTH ERROR IN R2
1814 014664 060201 ADD R2,R1 ;UPDATE RUNNING ERROR
1815 014666 010120 MOV R1,(R0)+ ;SAVE IN BUFFER
1816 014670 010104 MOV R1,R4 ;SAVE IN R4 ALSO
1817 014672 100001 BPL PLUS ;IS IT POSITIVE?
1818 014674 005404 NEG R4 ;NO - MAKE IT POSITIVE
1819 014676 020403 PLUS: CMP R4,R3 ;CHECK AGAINST PREVIOUS MAX. ERROR
1820 014700 003405 BLE NOTNEW ;NOT A NEW MAXIMUM
1821 014702 010403 MOV R4,R3 ;UPDATE MAXIMUM IN R3
1822 014704 010005 MOV R0,R5
1823 014706 162705 031606 SUB #BUFFER+2,R5
1824 014712 006205 ASR R5 ;R5=EDGE VALUE AT MAX. RELACC
1825 014714 020027 051602 NOTNEW: CMP R0,#BUFFER+8190. ;DONE?
1826 014720 001356 BNE NXTSTA ;NO - REPEAT
1827 014722 006203 ASR R3 ;RESCALE FROM 1 LSB = 800. SCALING
1828 014724 006203 ASR R3 ;TO 1 LSB = 100. SCALING
1829 014726 006203 ASR R3
1830 014730 005503 ADC R3
1831 014732 010302 MOV R3,R2
1832 014734 104416 TYPDC
1833 014736 104401 022621 TYPE ,LINEA
1834 014742 010546 MOV R5,-(SP) ;:SAVE R5 FOR TYPEOUT
(1) ;:TYPE VALUE
(1) 014744 104403 TYPOS ;:GO TYPE--OCTAL ASCII
(1) 014746 004 .BYTE 4 ;:TYPE 4 DIGIT(S)
(1) 014747 001 .BYTE 1 ;:TYPE LEADING ZEROS
1835 014750 104401 021130 TYPE ,SLASH ;:PRINT '/'
1836 014754 005205 INC R5
1837 014756 010546 MOV R5,-(SP) ;:SAVE R5 FOR TYPEOUT
(1) ;:TYPE VALUE
(1) 014760 104403 TYPOS ;:GO TYPE--OCTAL ASCII
(1) 014762 004 .BYTE 4 ;:TYPE 4 DIGIT(S)
(1) 014763 001 .BYTE 1 ;:TYPE LEADING ZEROS
1838 014764 020337 016366 CMP R3,VLIN
1839 014770 003407 BLE 41$
1840 014772 104401 021606 TYPE ,ERMSG
1841 014776 004737 026564 JSR PC,WHICHV ;INDICATE BAD UNIT
1842 015002 005237 001112 INC $ERTTL ;UPDATE ERROR COUNT
1843 015006 000402 BR 42$
1844 015010 104401 021132 41$: TYPE ,OKMSG
1845 015014 005737 001460 42$: TST FLAG ;BIT MAP TERMINAL ?
1846 015020 001503 BEQ L02 ;BR IF NOT
1847 015022 012700 031604 MOV #BUFFER,R0
1848 015026 012701 010000 MOV #4096.,R1

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1850 015032 011002          GETDAT: MOV      (R0),R2          ;GET RELATIVE ACCURACY ERROR SCALED 1LSB = 800.
1851 015034 006202          ASR      R2              ;RESCALE IT TO 1 LSB = 100.
1852 015036 006202          ASR      R2
1853 015040 006202          ASR      R2
1854 015042 005502          ADC      R2
1855 015044 062702 000166  ADD      #118.,R2          ;AND MOVE IT TO MID-SCREEN
1856 015050 010220          MOV      R2,(R0)+        ;PUT IT BACK INTO BUFFER
1857 015052 005301          DEC      R1
1858 015054 001366          BNE     GETDAT
1859 015056 012700 031604  MOV      #BUFFER,R0
1860 015062 012704 031604  MOV      #BUFFER,R4
1861 015066 012705 031606  MOV      #BUFFER+2,R5
1862 015072 012701 001000  MOV      #512.,R1
1863 015076 012702 000007  NXT8:   MOV      #7.,R2
1864 015102 012003          MOV      (R0)+,R3
1865 015104 010337 001474  MOV      R3,MIN          ;MINIMUM
1866 015110 010337 001500  MOV      R3,MAX          ;MAXIMUM
1867 015114 012003          NXTCMP: MOV      (R0)+,R3
1868 015116 020337 001474  CMP      R3,MIN
1869 015122 002002          BGE     MAXTST
1870 015124 010337 001474  MOV      R3,MIN          ;NEW MINIMUM
1871 015130 020337 001500  MAXTST: CMP      R3,MAX
1872 015134 003402          BLE     TST8
1873 015136 010337 001500  MOV      R3,MAX          ;NEW MAXIMUM
1874 015142 005302          TST8:  DEC      R2
1875 015144 001363          BNE     NXTCMP
1876 015146 013724 001474  MOV      MIN,(R4)+
1877 015152 013725 001500  MOV      MAX,(R5)+
1878 015156 022425          CMP      (R4)+,(R5)+    ;BUMP EACH ONCE MORE
1879 015160 005301          DEC      R1
1880 015162 001345          BNE     NXT8
1881 015164 104401 022143  TYPE     ,MSG18
1882 015170 104401 023055  TYPE     ,BUFF2          ;TYPE BUFF2
1883 015174 012700 031604  MOV      #BUFFER,R0
1884 015200 004737 015232  JSR      PC,LOAD
1885 015204 104401 022773  TYPE     ,C3              ;TYPE ASCIZ STRING
1886 015210 012700 031606  MOV      #BUFFER+2,R0
1887 015214 004737 015232  JSR      PC,LOAD
1888 015220 104401 022765  TYPE     ,C2              ;TYPE ASCIZ STRING
1889 015224 004737 015254  JSR      PC,DELCLR
1890 015230 000207          LO2:   RTS      PC
1891 015232 012701 001000  LOAD:   MOV      #512.,R1
1892 015236 012002          LOAD0: MOV      (R0)+,R2
1893 015240 005720          TST     (R0)+
1894 015242 004737 015750  JSR      PC,LOADY
1895 015246 005301          DEC      R1
1896 015250 001372          BNE     LOAD0
1897 015252 000207          RTS      PC

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1899 015254 032777 002000 163656 DELCLR: BIT #BIT10,@SWR ;TEST FOR HALT FOR DISPLAY
1900 015262 001402 BEQ 1$ ;:DON'T HALT FOR DISPLAY
1901 015264 000000 HALT
1902 015266 000407 BR 3$ ;;
1903 015270 005000 1$: CLR R0
1904 015272 012701 000020 MOV #20,R1 ;DELAY BEFORE CLEANING SCREEN
1905 015276 005300 2$: DEC R0
1906 015300 001376 BNE 2$
1907 015302 005301 DEC R1
1908 015304 001374 BNE 2$
1909 015306 104401 023114 3$: TYPE ,VTINIT
1910 015312 000207 RTS PC
1911 ;;TYPE RMS AND PEAK VALUES;;
1912 015314 005702 TYPRP: TST R2 ;IS NOISE POSITIVE?
1913 015316 100001 BPL POSNOI ;YES
1914 015320 005002 CLR R2 ;R2<0,SET R2=0
1915 015322 104416 POSNOI: TYPDC
1916 015324 104401 023016 TYPE ,MLSBAT ;TYPE " LSB AT "
1917 015330 004737 013122 JSR PC,TYPEDG
1918 015334 104401 021113 TYPE ,CHAN ;TYPE " ON CHANNEL "
1919 015340 013746 001444 MOV CHANL,-(SP) ;;SAVE CHANL FOR TYPEOUT
(1) ;;TYPE CHANL
(1) 015344 104403 TYPOS ;;GO TYPE--OCTAL ASCII
(1) 015346 002 .BYTE 2 ;;TYPE 2 DIGIT(S)
(1) 015347 000 .BYTE 0 ;;SUPPRESS LEADING ZEROS
1920 015350 000207 RTS PC

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1922      ;:ROUTINE TO AVERAGE 8 CONVERSIONS;;
1923 015352 012500      CONVTC: MOV      (R5)+,R0      ;GET CHANNEL VALUE
1924 015354 010037 001444      MOV      R0,CHANL
1925 015360 012777 000200 163770  CONVTC: MOV      #200,@ADBUFF      ;LOAD VERNIER DAC
1926 015366 113700 001444      CONVCD: MOVB     CHANL,R0      ;GET CHANNEL
1927 015372 000300      SWAB      R0      ;SET UP A/D STATUS REGISTER
1928 015374 052700 000100      BIS      #100,R0      ;ENABLE INTERRUPTS
1929 015400 010077 163746      MOV      R0,@STREG
1930 015404 012700 010000      MOV      #10000,R0      ;DAC SETTling DELAY
1931 015410 005300      1$:      DEC      R0
1932 015412 001376      BNE      1$
1933 015414 005037 001426      CLR      TEMP
1934 015420 012777 001540 163732      MOV      #RETURN,@VECTOR      ;LOAD VECTOR
1935 015426 012777 000200 163726      MOV      #200,@VECTR1      ;SET UP NEW PSW
1936 015434 012700 000010      MOV      #10,R0      ;SET UP COUNTER
1937 015440 005277 163706      2$:      INC      @STREG      ;START CONVERSION
1938 015444 000001      WAIT
1939 015446 067737 163704 001426      ADD      @ADBUFF,TEMP      ;READ BUFFER
1940 015454 005300      DEC      R0
1941 015456 001370      BNE      2$      ;DO 8 TIMES
1942 015460 006237 001426      ASR      TEMP      ;AVERAGE VALUE
1943 015464 006237 001426      ASR      TEMP
1944 015470 006237 001426      ASR      TEMP
1945 015474 005537 001426      ADC      TEMP
1946 015500 000205      RTS      R5      ;RETURN
1947
1948      ;COMPARE $GDDAT AND $BDDAT;;
1949 015502 012537 001124      COMPAR: MOV      (R5)+,$GDDAT      ;GET GOOD DATA
1950 015506 013537 001462      MOV      @(R5)+,SPREAD      ;GET SPREAD
1951 015512 013737 001426 001126      MOV      TEMP,$BDDAT      ;GET BAD(ACTUAL) DATA
1952 015520 013701 001126      MOV      $BDDAT,R1
1953 015524 013700 001124      MOV      $GDDAT,R0
1954 015530 160100      SUB      R1,R0      ;GET DIFFERENCE
1955 015532 100001      BPL      7$
1956 015534 005400      NEG      R0
1957 015536 020037 001462      7$:      CMP      R0,SPREAD      ;COMPARE IT TO SPREAD
1958 015542 003001      BGT      10$      ;GO TO ERROR PRINTOUT
1959 015544 005725      TST      (R5)+      ;BUMP RETURN POINTER AROUND ERROR CALL
1960 015546 000205      10$:     RTS      R5

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1962      ;;ROUTINE TO AVERAGE 8 CONVERSIONS ON GOOD AD;;
1963 015550 012500      GCONVT: MOV      (R5)+,R0      ;GET CHANNEL VALUE
1964 015552 010037 001444      MOV      R0,CHANL
1965 015556 000300      SWAB     R0
1966 015560 005037 001426      CLR      TEMP
1967 015564 010077 163606      MOV      R0,@GSTREG      ;LOAD CHANNEL INTO MIX BITS
1968 015570 012700 010000      MOV      #10000,R0
1969 015574 005300      2$:    DEC      R0
1970 015576 001376      BNE     2$
1971 015600 012777 001540 163574      MOV      #RETURN,@GVECT      ;LOAD VECTOR
1972 015606 012777 000200 163570      MOV      #200,@GVECT+2      ;SET UP NEW PRIORITY
1973 015614 012700 000010      MOV      #10,R0      ;SET UP COUNTER
1974 015620 152777 000101 163550 1$:    BISB    #101,@GSTREG      ;SET INTRPT. EN., START CONV.
1975 015626 000001      WAIT    ;WAIT FOR CONVERSION
1976 015630 067737 163544 001426      ADD     @GADBUF,TEMP      ;READ BUFFER
1977 015636 005300      DEC     R0
1978 015640 001367      BNE     1$      ;DO 8 TIMES
1979 015642 006237 001426      ASR     TEMP      ;AVERAGE VALUE
1980 015646 006237 001426      ASR     TEMP
1981 015652 006237 001426      ASR     TEMP
1982 015656 005537 001426      ADC     TEMP
1983 015662 000205      RTS     R5      ;RETURN
1984
1985      ;;SUBROUTINE TO CONVERT 2.60 VOLTS TO 15.00 VOLTS;;
1986      ;;FUNNY NUMBER CALCULATED BY:
1987      ;;      (15*2.56/(VOLTAGE))/0.0025
1988
1989 015664 032703 004000      CONV15: BIT     #BIT11,R3      ;IS RESULT MINUS?
1990 015670 001003      BNE     1$      ;:NO
1991 015672 005403      NEG     R3      ;YES, MAKE IT PLUS
1992 015674 104401 017343      TYPE   ,MINUS      ;TYPE '-'
1993 015700 042703 174000      1$:    BIC     #174000,R3      ;CLEAR UPPER 5 BITS
1994 015704 005002      CLR     R2      ;CLEAR RESULT REGISTER
1995 015706 012701 013424      MOV     #5908.,R1      ;PUT FUNNY NUMBER INTO R1
1996 015712 012700 002000      MOV     #BIT10,R0      ;SETUP TEST BIT
1997 015716 030003      2$:    BIT     R0,R3      ;MULTIPLY TEMP BY FUNNY NUMBER
1998 015720 001401      BEQ     3$      ;;
1999 015722 060102      ADD     R1,R2
2000 015724 006201      3$:    ASR     R1
2001 015726 006200      ASR     R0
2002 015730 001372      BNE     2$      ;:NOT FINISHED YET
2003 015732 006202      ASR     R2      ;SCALE TO .01 VOLTS / BIT
2004 015734 006202      ASR     R2
2005 015736 005502      ADC     R2
2006 015740 104416      TYPDC      ;TYPE RESULTS
2007 015742 104401 020442      TYPE   ,VOLTS      ;TYPE 'VOLTS'
2008 015746 000207      RTS     PC
2009

```

```
2011
2012
2013 015750 005702
2014 015752 100001
2015 015754 005002
2016 015756 020227 000353
2017 015762 002402
2018 015764 012702 000353
2019 015770 010203
2020 015772 042702 177740
2021 015776 052702 000040
2022 016002 105777 163142
2023 016006 100375
2024 016010 110277 163136
2025 016014 006203
2026 016016 006203
2027 016020 006203
2028 016022 006203
2029 016024 006203
2030 016026 042703 177770
2031 016032 052703 000040
2032 016036 105777 163106
2033 016042 100375
2034 016044 110377 163102
2035 016050 000207

;SUBROUTINE LOADY:
LOADY: TST R2 ;ROUTINE TO LOAD VLAUE INTO R2
        BPL PLUSR2 ;AS A VT55 Y-VALUE
        CLR R2
PLUSR2: CMP R2,#235.
        BLT LESS
        MOV #235.,R2
LESS: MOV R2,R3
        BIC #177740,R2
        BIS #40,R2
B10: TSTB @STPS ;PRINT CHARACTER
      BPL B10
      MOVB R2,@STPB
      ASR R3
      ASR R3
      ASR R3
      ASR R3
      BIC #177770,R3
      BIS #40,R3
B11: TSTB @STPS ;PRINT CHARACTER
      BPL B11
      MOVB R3,@STPB
      RTS PC
```

```

2037      ::SUBROUTINE TO TYPE DECIMAL VALUE::
2038      ::IN R2 AS X.XX::
2039 016052 005702      DECTYP: TST      R2      ;TEST VALUE TO BE TYPED
2040 016054 100003      BPL      POS
2041 016056 104401 017343      TYPE      ,MINUS      ;TYPE MINUS SIGN
2042 016062 005402      NEG      R2
2043 016064 020227 023417      POS:  CMP      R2,#9999.      ;>9999. REPLACE IT WITH 9999.
2044 016070 003402      BLE      OKAYD
2045 016072 012702 023417      MOV      #9999.,R2
2046 016076 105037 024547      OKAYD: CLRB     ONES      ;CLEAR ONES
2047 016102 105037 024546      CLRB     TENS      ;CLEAR TENS
2048 016106 105037 024544      CLRB     HUNS      ;CLEAR HUNS
2049 016112 105037 024543      CLRB     THOUS     ;CLEAR THOUS
2050 016116 005702      TESTR2: TST     R2      ;CONVERT VALUE TO A DECIMAL VALUE
2051 016120 001434      BEQ     TYP0UT
2052 016122 005302      DEC     R2
2053 016124 105237 024547      INCB     ONES
2054 016130 123727 024547 000012      CMPB     ONES,#10.
2055 016136 001367      BNE     TESTR2
2056 016140 105037 024547      CLRB     ONES
2057 016144 105237 024546      INCB     TENS
2058 016150 123727 024546 000012      CMPB     TENS,#10.
2059 016156 001357      BNE     TESTR2
2060 016160 105037 024546      CLRB     TENS
2061 016164 105237 024544      INCB     HUNS
2062 016170 123727 024544 000012      CMPB     HUNS,#10.
2063 016176 001347      BNE     TESTR2      ::
2064 016200 105037 024544      CLRB     HUNS
2065 016204 105237 024543      INCB     THOUS
2066 016210 000742      BR      TESTR2
2067 016212 152737 000060 024543      TYP0UT: BISB    #60,THOUS      ;PREPARE FOR TYP0UT
2068 016220 152737 000060 024544      BISB    #60,HUNS
2069 016226 152737 000060 024546      BISB    #60,TENS
2070 016234 152737 000060 024547      BISB    #60,ONES
2071 016242 123727 024543 000060      CMPB    THOUS,#60
2072 016250 001403      BEQ     1$      ::
2073 016252 104401 024543      TYPE    ,THOUS
2074 016256 000002      RTI
2075 016260 104401 024544      1$:    TYPE    ,HUNS      ;TYPE VALUE
2076 016264 000002      RTI

```

```

2078 ;SUBROUTINE TO SENSE THE 'WFTEST' FLAG AND USE WIDE/NARROW ERROR TOLERANCES
2079
2080 016266 012701 016360 WFADJ: MOV #VNR,R1 ;SUBROUTINE TO SET LIMITS
2081 016272 005737 001476 TST WFTEST ;RUNNING ON TESTER ?
2082 016276 100403 BMI 1$ ;:YES
2083 016300 012702 016372 MOV #VARLT1,R2 ;WFTEST NOT MINUS, USE NORMAL LIMITS
2084 016304 000402 BR 2$ ;:
2085 016306 012702 016402 1$: MOV #VARLT2,R2 ;WFTEST MINUS, USE OPTION AREA LIMITS
2086 016312 012221 2$: MOV (R2)+,(R1)+ ;SET UP LIMITS
2087 016314 005711 TST (R1) ;DONE?
2088 016316 100375 BPL 2$ ;:NO
2089 016320 000207 RTS PC
2090
2091 016322 000000 V0: 0 ;TOLERANCE VALUES FOR FUNCTIONAL TESTS
2092 016324 000002 V2: 2
2093 016326 000012 V10: 10.
2094 016330 000012 V12: 12
2095 016332 000062 V50D: 50.
2096 016334 000144 V100D: 100.
2097 016336 000326 V326: 326
2098
2099 ;*VOLTAGE TABLE OF EXPECTED VALUES (SINGLE ENDED) <TEST MODULE>
2100 016340 005560 VTABLE: 5560 ;+2.2 VOLTS <CH10, 20, 30 ETC>
2101 016342 002220 2220 ;:-2.2 VOLTS
2102 016344 004670 4670 ;+1.1 VOLTS
2103 016346 003110 3110 ;:-1.1 VOLTS
2104 016350 007340 7340 ;+4.4 VOLTS <CH14, 24, 34 ETC>
2105 016352 000440 0440 ;:-4.4 VOLTS
2106 016354 006450 6450 ;+3.3 VOLTS
2107 016356 001330 1330 ;:-3.3 VOLTS <CH17, 27, 37 ETC>
2108
2109 016360 000041 VNR: 33. ;.33 LSB,NORMAL LIMITS FOR SYSTEM
2110 016362 000310 VNP: 200. ;2 LSB, INTEGRATION AND FIELD USE ON SPEC TESTS
2111 016364 000144 VSET: 100. ;1 LSB
2112 016366 000175 VLIN: 125. ;1.25 LSB
2113 016370 100000 BIT15
2114
2115 ;LIMITS FOR NON-TESTER
2116
2117 016372 000050 VARLT1: 40. ;.4 LSB, NORMAL LIMITS FOR SYSTEM
2118 016374 000310 200. ;2 LSB, INTEGRATION AND FIELD USE ON SPEC TESTS
2119 016376 000144 100. ;1 LSB
2120 016400 000175 125. ;1.25 LSB
2121
2122 ;LIMITS FOR TESTER
2123
2124 016402 000041 VARLT2: 33. ;.33 LSB RMS NOISE LIMIT
2125 016404 000226 150. ;1.5 LSB PEAK NOISE LIMIT
2126 016406 000132 90. ;.9 LSB INTER-CHANNEL SETTling LIMIT
2127 016410 000144 100. ;1 LSB RELATIVE ACCURACY ERROR LIMIT
2128

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```

2130
(1)
(2)
(1)
(1)
(1)
(1)
(1)
(1)
(1)
(1) 016412
(2) 016412 000240
(1) 016414 005037 001102
(1) 016420 005037 001160
(1) 016424 005237 001176
(1) 016430 042737 100000 001176
(1) 016436 005327
(1) 016440 000001
(1) 016442 003022
(1) 016444 012737
(1) 016446 000001
(1) 016450 016440
(1) 016452 104401 016517
(2) 016456 013746 001176
(2) 016462 104405
(1) 016464 104401 016514
(1) 016470 013700 000042
(1) 016474 001405
(1) 016476 000005
(1) 016500 004710
(1) 016502 000240
(1) 016504 000240
(1) 016506 000240
(1) 016510
(1) 016510 000137
(1) 016512 016534
(1) 016514 377 377 000
(1) 016517 015 042412 042116
(1) 016524 050040 051501 020123
(1) 016532 000043
2131 016534 052777 000100 162402
2132 016542 005737 001112
2133 016546 001415
2134 016550 104401 022721
2135 016554 013746 001112
2136 016560 104405
2137 016562 005737 001440
2138 016566 001405
2139 016570 104401 022750
2140 016574 013746 001510
2141 016600 104406
2142 016602 104401 016514
2143 016606 000137
2144 016610 001556

```

```

.SBTTL END OF PASS ROUTINE
:*****
:*INCREMENT THE PASS NUMBER ($PASS)
:*TYPE 'END PASS #XXXXX' (WHERE XXXXX IS A DECIMAL NUMBER)
:*IF THERES A MONITOR GO TO IT
:*IF THERE ISN'T JUMP TO EXTMSG

$EOP:
NOP
CLR $STNM ::ZERO THE TEST NUMBER
CLR $TIMES ::ZERO THE NUMBER OF ITERATIONS
INC $PASS ::INCREMENT THE PASS NUMBER
BIC #100000,$PASS ::DON'T ALLOW A NEG. NUMBER
DEC (PC)+ ::LOOP?

$EOPCT: .WORD 1
BGT $DOAGN ::YES
MOV (PC)+,@(PC)+ ::RESTORE COUNTER

$ENDCT: .WORD 1
$EOPCT
TYPE , $SENDMG ::TYPE 'END PASS #'
MOV $PASS,-(SP) ::SAVE $PASS FOR TYPEOUT
TYPDS ::GO TYPE--DECIMAL ASCII WITH SIGN
TYPE , $ENULL ::TYPE A NULL CHARACTER
$GET42: MOV @#42,R0 ::GET MONITOR ADDRESS
BEQ $DOAGN ::BRANCH IF NO MONITOR
RESET ::CLEAR THE WORLD
$ENDAD: JSR PC,(R0) ::GO TO MONITOR
NOP ::SAVE ROOM
NOP ::FOR
NOP ::ACT11

$DOAGN: JMP @ (PC)+ ::RETURN
$RTNAD: .WORD EXTMSG
$ENULL: .BYTE -1,-1,0 ::NULL CHARACTER STRING
$SENDMG: .ASCIZ <15><12>/END PASS #/

EXTMSG: BIS #BIT6,@$TKS ::ENABLE KRB INTR.
TST $ERTTL ::ANY ERRORS
BEQ 1$ ::BR IF NOT
TYPE ,ERRTOT ::TYPE TOTAL ERROR COUNT PRIMER
MOV $ERTTL,-(SP) ::GET VALUE
TYPDS ::REPORT IT
TST NMBEXT ::TEST IF MULTIPLE
BEQ 1$ ::BR IF NOT
TYPE ,MESGD ::TYPE BAD UNIT PRIMER
MOV BADUNT,-(SP)
TYPBN
1$: TYPE , $ENULL ::REPORT 1 + 0'S
JMP @ (PC)+ ::ENSURE ALL TEXT GET TYPED
::RETURN
AGTST: BEGIN

```

```
2146 :*
2147 :*THIS ROUTINE WILL PROTECT THE PROGRAM
2148 :*FROM INTERRUPTS (BAD ONES).
2149 :*
2150 :*THE TRAP CATCHER IS SET UP FOR
2151 :*   .WORD   +2
2152 :*   JSR PC,R0
2153 :*
2154 :*ILLEGAL INTERRUPTS OR INTERRUPTS TO THE WRONG VECTOR
2155 :*GOTO THE VECTOR AND PCITK UP THE '.+2' AS AN ADDRESS
2156
2157 :*AND '4700' AS NEW STATUS.
2158 :*THE .+2 AS A PC WILL CAUSE EXECUTION OF THE 'JSR PC,R0' (AN ILLEGAL INSTR.).
2159 :*AND TRAP TO LOCATION '4'. IN LOCATION 4 WE HAVE A
2160 :*POINTER HERE. IF THIS CONDITION CAUSES A TRAP TO LOC. 4.
2161 :*WE WILL REPORT IT IN THE SAME MANNER THAT WER WOULD
2162 :*REPORT ANY OTHER ERROR.
2163 :*IF A BUSS ERROR TRAP DID OCCUT AND CAUSE A TRAP TO 4.
2164 :*WE WILL HALT.
2165
2166 016612 011637 017072 IOTRD: MOV (SP),TRTO ;GET WHERE WE CAME TO.
2167 016616 162737 000004 017072 SUB #4,TRTO ;FORM READ ADDR.
2168 016624 023727 017072 001000 CMP TRTO,#1000 ;DID TRAP FROM LESS THAN ADDR. 1000?
2169 016632 003402 BLE 2$ ;NO-CONTINUE.
2170 016634 000000 1$: HALT ;A BUSS ERROR TIME OUT TRAP BROUGHT US HERE.
2171 ;ADDRESS CONTAINED IN TRTO.
2172 016636 000776 BR 1$ ;DON'T ALLOW CONTINUE.
2173 016640 016637 000004 017074 2$: MOV 4(SP),TRFRO ;GET TRAPPED FROM ADDR.
2174 016646 122737 000021 001102 CMPB #21,$STNM ;LESS THAN INTERRUPT TESTS?
2175 016654 003402 BLE 3$ ;NO MUST BE WRONG VECTOR.
2176 ;////////////////////////////////////
2177 016656 104003 ERROR 3 ;ERROR! ILLEGAL INTERRUPT OR
2178 ;INTERRUPT TO WRONG VECTOR.
2179 ;IF TEST NO. IS LESS THAN 10,ITS
2180 ;LIKELY(BUT NO EXCLUSIVELY)TO BE A
2181 ;DEVICE OTHER THAN THE DEVICE UNDER TEST.
2182 ;IF THE INTERRUPT OCCURED
2183 ;DURING AN INTERRUPT TEST, I'D
2184 ;SUSPECT A PROBLEM WITH THE DEVICE UNDER TEST.
2185 ;IF THE ADDRESS THE INTERRUPT
2186 ;VECTORED TO IS WITHIN THE RANGE OF
2187 ;VECTORS ASSIGNED TO THE DEVICE,
2188 ;THEN I'D SUSPECT THE DEVICE
2189 ;INTERRUPTD ILLEGALLY.
2190 ;IF THE ADDRESS THE INTERRUPT
2191 ;VECTORED TO IS OUTSIDE OF THE
2192 ;RANGE ASSIGNED TO THE DEVICE
2193 ;I'D SUSPECT THAT THE
2194 ;DEVICE PUT THE WRONG INTERRUPT
2195 ;VECTOR ON THE BUS DURING THE INTERRUPT
2196 ;PROCESS.
2197 ; NOTE:
2198 ;FOR THIS ERROR - DON'T USE
2199 ; 'LOOP ON ERROR' OPTION.
```



```

2200                                     ;ALSO EXPECT THAT THE INTERRUPT TEST TO
2201                                     ;WILL REPT THAT THE DEVICE DIDN'T
2202                                     ;INTERRUPT.
2203                                     ;FOLLOW THE RECOMMENDED PROCEEDURE
2204                                     ; IN THE DOCUMENT (ON THIS DIAGNOSTIC)
2205                                     ;FOR LOOPING ON TEST.
2206
2207                                     ;////////////////////////////////////
2208 016660 000002                               RTI
2209 016662 022626                               3S:  CMP      (SP)+,(SP)+      ;POP OFF JSR TRAP
2210 016664 022626                               CMP      (SP)+,(SP)+      ;POP OFF WRONG INTR.
2211 016666 005737 001176                       TST      $PASS            ;IS THIS THE IRST PASS?
2212 016672 001025                               BNE      4S                ;NO, DON'T REPORT
2213 016674 104401 017524                       TYPE     ,VTMSG           ;TYPE 'EXPECTED INTR. AT ''
2214 016700 004737 026572                       JSR      PC,WHICHU        ;DETERMINE THE UNIT #
2215 016704 013746 001202                       MOV      $UNIT,-(SP)
2216 016710 104405                               TYPDS
2217 016712 104401 017550                       TYPE     ,VTMSG3         ;REPORT INTR. TO
2218 016716 013746 001360                       MOV      VECTOR,-(SP)    ;SAVE VECTOR FOR TYPEOUT
(1) 016722 104403                               TYPOS    ;GO TYPE--OCTAL ASCII
(1) 016724 003                                  .BYTE   3                ;TYPE 3 DIGIT(S)
(1) 016725 001                                  .BYTE   1                ;TYPE LEADING ZEROS
2219 016726 104401 017601                       TYPE     ,VTMSG1         ;TYPE '' RECEIVED INTR. AT ''
2220 016732 013746 017072                       MOV      TRTO,-(SP)      ;SAVE TRTO FOR TYPEOUT
(1) 016736 104403                               TYPOS    ;GO TYPE--OCTAL ASCII
(1) 016740 003                                  .BYTE   3                ;TYPE 3 DIGIT(S)
(1) 016741 001                                  .BYTE   1                ;TYPE LEADING ZEROS
2221 016742 104401 017631                       TYPE     ,VTMSG2         ;TYPE 'RESTARTING TEST'
2222 016746 013777 001362 162404                4S:  MOV      VECTR1,@VECTOR
2223 016754 013777 001366 162402                MOV      VECTR3,@VECTR2
2224 016762 012777 004700 162372                MOV      #4700,@VECTR1
2225 016770 012777 004700 162370                MOV      #4700,@VECTR3
2226 016776 013737 017072 001360                MOV      TRTO,VECTOR
2227 017004 042737 000003 001360                BIC      #3,VECTOR
2228 017012 013737 001360 001362                MOV      VECTOR,VECTR1
2229 017020 062737 000002 001362                ADD      #2,VECTR1
2230 017026 013737 001360 001364                MOV      VECTOR,VECTR2
2231 017034 062737 000004 001364                ADD      #4,VECTR2
2232 017042 013737 001364 001366                MOV      VECTR2,VECTR3
2233 017050 062737 000002 001366                ADD      #2,VECTR3
2234 017056 005077 162270                CLR      @STREG
2235 017062 005777 162270                TST      @ADBUFF         ;READ A/D BUFFER TO CLEAR DONE FLAG
2236 017066 000177 162014                JMP      @SLPADR        ;START TEST OVER AGAIN.
2237 017072 000000                               TRTO:  .WORD  0          ;CONTAINS ADDR. WE TRAPPED OR INTERRUPTED TO.
2238 017074 000000                               TRFRO: .WORD  0          ;CONTAINS ADDR. WE TRAPPED OR INTR. FROM.

```

```
2240
2241 017076 051600 040524 052122 .SBTTL ASCII MESSAGES
      017104 047111 020107 047117 SCHAN: .ASCIZ <200>\STARTING ON CHANNEL = \
      017112 041440 040510 047116
      017120 046105 036440 000040
2242 017126 042600 042116 047111 ECHAN: .ASCIZ <200>\ENDING ON CHANNEL = \
      017134 020107 047117 041440
      017142 040510 047116 046105
      017150 036440 000040
2243 017154 005015 047516 051511 NOIMSG: .ASCIZ <15><12>/NOISE TEST ON UNIT # /
      017162 020105 042524 052123
      017170 047440 020116 047125
      017176 052111 021440 000040
2244 017204 005015 042523 052124 SETMSG: .ASCIZ <15><12>/SETTLING TEST ON UNIT # /
      017212 044514 043516 052040
      017220 051505 020124 047117
      017226 052440 044516 020124
      017234 020043 000
2245 017237 200 043117 051506 OFFSET: .ASCIZ <200>/OFFSET TEST ON UNIT # /
      017244 052105 052040 051505
      017252 020124 047117 052440
      017260 044516 020124 020043
      017266 000
2246 017267 111 020123 044124 DWRF: .ASCIZ \IS THE MNCAD (A/D) TEST MODULE CONNECTED ? \
      017274 020105 047115 040503
      017302 020104 040450 042057
      017310 020051 042524 052123
      017316 046440 042117 046125
      017324 020105 047503 047116
      017332 041505 042524 020104
      017340 020077 000
2247 017343 055 000 MINUS: .BYTE 55,0
2248 017345 077 000 QUEST: .BYTE 77,0
2249 017347 040 020055 000 MDASH: .ASCIZ / - /
2250 017353 040 044523 043516 MSE: .ASCIZ / SINGLE ENDED/<15><12>
      017360 042514 042440 042116
      017366 042105 005015 000
2251 017373 040 044504 043106 MDIF: .ASCIZ / DIFFERENTIAL/<15><12>
      017400 051105 047105 044524
      017406 046101 005015 000
2252 017413 040 051120 040505 MPRMP: .ASCIZ / PREAMP/<15><12>
      017420 050115 005015 000
2253 017425 200 047115 040503 MADR: .ASCIZ <200>\MNCAD (A/D) BASE ADDRESS <\
      017432 020104 040450 042057
      017440 020051 040502 042523
      017446 040440 042104 042522
      017454 051523 036040 000
2254 017461 200 047115 040503 MVCT: .ASCIZ <200>\MNCAD (A/D) VECTOR ADDRESS <\
      017466 020104 040450 042057
      017474 020051 042526 052103
      017502 051117 040440 042104
      017510 042522 051523 036040
      017516 000
2255 017517 076 037440 000040 ENCOM: .ASCIZ #> ? #
```

Line	Address	Hex	Hex	Hex	Text
2256	017524	046600	041516	042101	VTMSG: .ASCIZ <200>\MNCAD (A/D) UNIT #\
	017532	024040	027501	024504	
	017540	052440	044516	020124	
	017546	000043			
2257	017550	005015	054105	042520	VTMSG3: .ASCIZ <15><12>/EXPECTED INTERRUPT AT /
	017556	052103	042105	044440	
	017564	052116	051105	052522	
	017572	052120	040440	020124	
	017600	000			
2258	017601	040	042522	042503	VTMSG1: .ASCIZ / RECEIVED INTERRUPT AT /
	017606	053111	042105	044440	
	017614	052116	051105	052522	
	017622	052120	040440	020124	
	017630	000			
2259	017631	200	046120	040505	VTMSG2: .ASCII <200>/PLEASE CHECK VECTOR SWITCHES/
	017636	042523	041440	042510	
	017644	045503	053040	041505	
	017652	047524	020122	053523	
	017660	052111	044103	051505	
2260	017666	005015	051011	051505	.ASCIZ <15><12>/ RESTARTING LOGIC TEST/<15><12>
	017674	040524	052122	047111	
	017702	020107	047514	044507	
	017710	020103	042524	052123	
	017716	005015	000		
2261	017721	015	052012	051505	TCHAN: .ASCIZ <15><12>/TESTING CHANNELS /
	017726	044524	043516	041440	
	017734	040510	047116	046105	
	017742	020123	000		
2262	017745	124	050131	020105	YESNO: .ASCIZ /TYPE Y FOR YES, N FOR NO/<15><12>
	017752	020131	047506	020122	
	017760	042531	026123	047040	
	017766	043040	051117	047040	
	017774	006517	000012		
2263	020000	005015	042524	052123	TSTAD: .ASCIZ <15><12>/TESTING MNCAD/<15><12>
	020006	047111	020107	047115	
	020014	040503	006504	000012	
2264	020022	005015	042524	052123	TSTADM: .ASCIZ <15><12>/TESTING MNCAM/<15><12>
	020030	047111	020107	047115	
	020036	040503	006515	000012	
2265	020044	042523	020124	047115	SADTST: .ASCIZ #SET MNCAD (A/D) FRONT PANEL SWITCHES TO 'TEST'#/<15><12>
	020052	040503	020104	040450	
	020060	042057	020051	051106	
	020066	047117	020124	040520	
	020074	042516	020114	053523	
	020102	052111	044103	051505	
	020110	052040	020117	052042	
	020116	051505	021124	005015	
	020124	000			
2266	020125	015	051412	052105	SDSE: .ASCIZ <15><12>/SET TEST MODULE TO SINGLE ENDED/<15><12>
	020132	052040	051505	020124	
	020140	047515	052504	042514	
	020146	052040	020117	044523	
	020154	043516	042514	042440	
	020162	042116	042105	005015	

2267	020170	000				
	020171	015	051412	052105	SDDIF: .ASCIZ	<15><12>/SET TEST MODULE TO DIFFERENTIAL/<15><12>
	020176	052040	051505	020124		
	020204	047515	052504	042514		
	020212	052040	020117	044504		
	020220	043106	051105	047105		
	020226	044524	046101	005015		
	020234	000				
2268	020235	015	051412	052105	DIFM: .ASCIZ	<15><12>/SET TEST MODULE ON CHANNELS UNDER TEST TO DIFFERENTIAL/<15><12>
	020242	052040	051505	020124		
	020250	047515	052504	042514		
	020256	047440	020116	044103		
	020264	047101	042516	051514		
	020272	052440	042116	051105		
	020300	052040	051505	020124		
	020306	047524	042040	043111		
	020314	042506	042522	052116		
	020322	040511	006514	000012		
2269	020330	005015	051120	051505	EXTST: .ASCIZ	<15><12>\PRESS EXTERNAL START ON MNCAD (A/D) TEST MODULE ON UNIT #\
	020336	020123	054105	042524		
	020344	047122	046101	051440		
	020352	040524	052122	047440		
	020360	020116	047115	040503		
	020366	020104	040450	042057		
	020374	020051	042524	052123		
	020402	046440	042117	046125		
	020410	020105	047117	052440		
	020416	044516	020124	000043		
2270	020424	005015	030453	036465	TP15: .ASCIZ	<15><12>/+15= /
	020432	000				
2271	020433	015	026412	032461	TM15: .ASCIZ	<15><12>/-15= /
	020440	000075				
2272	020442	053040	046117	051524	VOLTS: .ASCIZ	/ VOLTS/
	020450	000				
2273	020451	015	044412	050115	BADID: .ASCIZ	<15><12>/IMPROPER I.D. CODE/
	020456	047522	042520	020122		
	020464	027111	027104	041440		
	020472	042117	000105			
2274	020476	005015	047503	052116	YORNO: .ASCIZ	<15><12>/CONTINUE TESTING? (Y FOR YES, N FOR NO): /
	020504	047111	042525	052040		
	020512	051505	044524	043516		
	020520	020077	054450	043040		
	020526	051117	054440	051505		
	020534	020054	020116	047506		
	020542	020122	047516	035051		
	020550	000				
2275	020551	123	052105	046440	SCM: .ASCIZ	/SET MODE TO CURRENT, /
	020556	042117	020105	047524		
	020564	041440	051125	042522		
	020572	052116	020054	000		
2276	020577	123	052105	046440	SRM: .ASCIZ	/SET MODE TO RESISTANCE, /
	020604	042117	020105	047524		
	020612	051040	051505	051511		
	020620	040524	041516	026105		

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2277 020626 000040
      020630 042523 020124 047515 SVM: .ASCIZ /SET MODE TO VOLTAGE, /
      020636 042504 052040 020117
      020644 047526 052114 043501
      020652 026105 000040
2278 020656 040507 047111 052040 GHLF: .ASCIZ /GAIN TO .5/<15><12>
      020664 020117 032456 005015
      020672 000
2279 020673 123 052105 043440 GAIN5: .ASCIZ /SET GAIN TO 5/<15><12>
      020700 044501 020116 047524
      020706 032440 005015 000
2280 020713 123 052105 043440 GAIN50: .ASCIZ /SET GAIN TO 50/<15><12>
      020720 044501 020116 047524
      020726 032440 006460 000012
2281 020734 042523 020124 040507 GAIN5M: .ASCIZ /SET GAIN TO 500/<15><12>
      020742 047111 052040 020117
      020750 030065 006460 000012
2282 020756 046040 041123 005015 LSBMSG: .ASCIZ / LSB/<15><12>
      020764 000
2283 020765 055 020055 000 DASH: .ASCIZ /-- /
2284 020771 123 040524 042524 STATE: .ASCIZ /STATE-- WIDTH/<15><12>
      020776 026455 053440 042111
      021004 044124 005015 000
2285 021011 103 000110 CH: .ASCIZ /CH/
2286 021014 020040 020040 000 SPACE: .ASCIZ / /
2287 021021 040 051514 020102 LSB: .ASCIZ / LSB ON CH/
      021026 047117 041440 000110
2288 021034 051440 052105 046124 SETCH: .ASCIZ / SETTLING FROM CH/
      021042 047111 020107 051106
      021050 046517 041440 000110
2289 021056 040440 020124 000 ATMSG: .ASCIZ / AT /
2290 021063 122 051515 020040 RMSNOI: .ASCIZ /RMS NOISE /
      021070 047516 051511 020105
      021076 000
2291 021077 120 040505 020113 PKNOI: .ASCIZ /PEAK NOISE /
      021104 047516 051511 020105
      021112 000
2292 021113 040 047117 041440 CHAN: .ASCIZ / ON CHANNEL /
      021120 040510 047116 046105
      021126 000040
2293 021130 000057 SLASH: .ASCIZ #/#
2294 021132 020040 020040 045517 OKMSG: .ASCIZ / OK/<15><12>
      021140 005015 000
2295 021143 015 052012 050131 CCHAN: .ASCIZ <15><12>/TYPE IN OCTAL CHANNEL NUMBER AND DEPRESS 'RETURN': /
      021150 020105 047111 047440
      021156 052103 046101 041440
      021164 040510 047116 046105
      021172 047040 046525 042502
      021200 020122 047101 020104
      021206 042504 051120 051505
      021214 020123 051042 052105
      021222 051125 021116 020072
      021230 000
2296 021231 015 052012 050131 SEL: .ASCIZ <15><12>/TYPE 'O' FOR OFFSET, 'G' FOR GAIN & DEPRESS 'RETURN': /

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	021236	020105	047442	020042			
	021244	047506	020122	043117			
	021252	051506	052105	020054			
	021260	043442	020042	047506			
	021266	020122	040507	047111			
	021274	023040	042040	050105			
	021302	042522	051523	021040			
	021310	042522	052524	047122			
	021316	035042	000040				
2297	021322	005015	042101	052512	XADJ:	.ASCII	<15><12>/ADJUST R83/
	021330	052123	051040	031470			
2298	021336	043040	051117	030040	MOLSB:	.ASCII	/ FOR 0.00 LSB ERROR/
	021344	030056	020060	051514			
	021352	020102	051105	047522			
	021360	122					
2299	021361	015	042012	050105		.ASCIZ	<15><12>/DEPRESS 'RETURN' WHEN ADJUSTED/<15><12>
	021366	042522	051523	021040			
	021374	042522	052524	047122			
	021402	020042	044127	047105			
	021410	040440	045104	051525			
2300	021416	042524	006504	000012			
	021424	005015	047111	052520	IGND:	.ASCII	<15><12>/INPUT A GROUND ON THE CHANNEL/ ;MUST BE JUST BEFORE 'CRWR'
	021432	020124	020101	051107			
	021440	052517	042116	047440			
	021446	020116	044124	020105			
	021454	044103	047101	042516			
	021462	114					
2301	021463	015	042012	050105	CRWR:	.ASCIZ	<15><12>/DEPRESS 'RETURN' WHEN READY/<15><12>
	021470	042522	051523	021040			
	021476	042522	052524	047122			
	021504	020042	044127	047105			
	021512	051040	040505	054504			
	021520	005015	000				
2302	021523	015	044412	050116	IVOLT:	.ASCIZ	<15><.2>/INPUT +5.115 VOLTS ON THE CHANNEL/
	021530	052125	025440	027065			
	021536	030461	020065	047526			
	021544	052114	020123	047117			
	021552	052040	042510	041440			
	021560	040510	047116	046105			
	021566	000					
2303	021567	015	040412	045104	YADJ:	.ASCIZ	<15><12>/ADJUST R84/
	021574	051525	020124	034122			
	021602	000064					
2304	021604	000053			POSITV:	.ASCIZ	/+ /
2305	021606	025040	042452	051122	ERMSG:	.ASCIZ	/ **ERROR**/<15><12>
	021614	051117	025052	005015			
	021622	000					
2306	021623	040	045523	050111	SKPMSG:	.ASCIZ	/ SKIPPED STATE(S)/
	021630	042520	020104	052123			
	021636	052101	024105	024523			
	021644	000					
2307	021645	040	040516	051122	NARMSG:	.ASCIZ	# NARROW (< 1/2 LSB) STATE(S)#<15><12>
	021652	053517	024040	020074			
	021660	027461	020062	051514			

2308 021666 024502 051440 040524
 021674 042524 051450 006451
 021702 000012
 021704 053440 042111 020105 WIDMSG: .ASCIZ # WIDE (> 1 1/2 LSB) STATE(S)#<15><12>
 021712 037050 030440 030440
 021720 031057 046040 041123
 021726 020051 052123 052101
 021734 024105 024523 005015
 021742 000
 2309 021743 040 052123 052101 OUTMSG: .ASCIZ / STATE(S) WIDER THAN 2 LSB/
 021750 024105 024523 053440
 021756 042111 051105 052040
 021764 040510 020116 020062
 021772 051514 000102
 2310 021776 051440 040524 042524 HAFMSG: .ASCIZ # STATE-WIDTH(S) OUTSIDE + OR - 1/2 LSB#
 022004 053455 042111 044124
 022012 051450 020051 052517
 022020 051524 042111 020105
 022026 020053 051117 026440
 022034 030440 031057 046040
 022042 041123 000
 2311 022045 015 042412 042116 MEND: .ASCIZ <15><12>/END OF LOGIC TESTS /
 022052 047440 020106 047514
 022060 044507 020103 042524
 022066 052123 020123 000040
 2312 022074 050200 047522 051107 FOUND1: .ASCIZ <200>/PROGRAM DETECTED /
 022102 046501 042040 052105
 022110 041505 042524 020104
 022116 000
 2313 022117 040 047115 040503 FOUND2: .ASCIZ \ MNCAD (A/D)'S \<15><12>
 022124 020104 040450 042057
 022132 023451 020123 020040
 022140 005015 000
 2314 022143 012 005012 030453 MSG18: .ASCII <12><12><12># +1/2 LSB#<15><12><12><12><12><12><12><12><12><12><12><12><12><12><12><1
 022150 031057 046040 041123
 022156 005015 005012 005012
 022164 005012 005012 005012
 022172 012
 2315 022173 055 027461 046062 .ASCIZ \ -1/2LSB\
 022200 041123 000
 2316 022203 015 042012 043111 MSG20: .ASCIZ <15><12>/DIFFERENTIAL LINEARITY ON UNIT # /
 022210 042506 042522 052116
 022216 040511 020114 044514
 022224 042516 051101 052111
 022232 020131 047117 052440
 022240 044516 020124 020043
 022246 000
 2317 022247 040 020040 020040 MSG16: .ASCII / STATE-WIDTH DISTRIBUTION/<15><12><12><12>
 022254 020040 020040 020040
 022262 020040 020040 020040
 022270 020040 051440 040524
 022276 042524 053455 042111
 022304 044124 042040 051511
 022312 051124 041111 052125

2318	022320	047511	006516	005012					
	022326	012							
	022327	040	021440	047440	.ASCII /	# OF STATES/	<12><12><12><12><12><12><12><12><12><12><12><12><12><12><12><		
	022334	020106	052123	052101					
	022342	051505	005012	005012					
	022350	005012	005012	005012					
	022356	005012	005012	005012					
2319	022364	005012	012		.ASCII /				STATE WIDTH (LSB)/<15>
	022367	040	020040	020040					
	022374	020040	020040	020040					
	022402	020040	020040	020040					
	022410	020040	020040	020040					
	022416	020040	020040	020040					
	022424	020040	020040	020040					
	022432	020040	020040	020040					
	022440	020040	020040	020040					
	022446	020040	052123	052101					
	022454	020105	044527	052104					
	022462	020110	046050	041123					
	022470	006451	012						
2320	022473	040	020060	020040	.ASCIZ # 0		1/2	1	1 1/2 2#
	022500	020040	020040	020040					
	022506	020040	020040	030440					
	022514	031057	020040	020040					
	022522	020040	020040	020040					
	022530	020040	030440	020040					
	022536	020040	020040	020040					
	022544	020040	020040	020061					
	022552	027461	020062	020040					
	022560	020040	020040	020040					
	022566	020040	020040	000062					
2321	022574	042522	040514	044524	MSG21: .ASCIZ /	RELATIVE ACCURACY:/	<15><12>		
	022602	042526	040440	041503					
	022610	051125	041501	035131					
	022616	005015	000						
2322	022621	040	051514	020102	LINEA: .ASCIZ /	LSB MAXIMUM AT /			
	022626	040515	044530	052515					
	022634	020115	052101	000040					
2323	022642	005015	051120	047111	HEAD5: .ASCII <15><12>/	PRINT VALUES--/			
	022650	020124	040526	052514					
	022656	051505	026455						
2324	022662	051440	052105	041440	.ASCIZ /	SET CHANNEL IN SWR LOW BYTE/	<15><12>		
	022670	040510	047116	046105					
	022676	044440	020116	053523					
	022704	020122	047514	020127					
	022712	054502	042524	005015					
	022720	000							
2325	022721	040	052073	052117	ERRTOT: .ASCIZ /	;TOTAL ERROR COUNT = /			
	022726	046101	042440	051122					
	022734	051117	041440	052517					
	022742	052116	036440	000040					
2326	022750	035440	040502	020104	MESGD: .ASCIZ /	;BAD UNITS /			
	022756	047125	052111	020123					
	022764	000							


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2327 022765 033 015462 005110 C2: .ASCIZ <33><62><33><110><12> ;EXIT GRAPH MODE, HOME AND LINE FEED
      022772 000
2328 022773 112 000 C3: .ASCIZ <112> ;ASCII 'I'
2329 022775 015 047412 043106 MOFSET: .ASCIZ <15><12>/OFFSET =/
      023002 042523 020124 000075
2330 023010 046040 041123 000040 MLSB: .ASCIZ / LSB /
2331 023016 046040 041123 040440 MLSBAT: .ASCIZ / LSB AT /
      023024 020124 000
2332 ;CODE TO SETUP BIT MAP VIEWING SCREEN
2333 023027 033 061 BUFF1: .BYTE 33,61 ;GRAPH ON
2334 023031 101 061 .BYTE 101,61 ;ENABLE HISTOGRAM 0
2335 023033 111 062 .BYTE 111,62 ;CLEAR DATA + ENABLE VERT LINES
2336 023035 114 041 060 .BYTE 114,41,60 ;LOAD VERT LINE CORD
2337 023040 045 063 .BYTE 45,63
2338 023042 051 066 .BYTE 51,66
2339 023044 055 071 .BYTE 55,71
2340 023046 061 074 .BYTE 61,74
2341 023050 110 041 040 .BYTE 110,41,40 ;LOAD STARTING CORD.
2342 023053 112 000 .BYTE 112,0 ;LOAD GRAPH 1 COMMAND <DATA TO FOLLOW>
2343 023055 033 061 BUFF2: .BYTE 33,61 ;GRAPH ON
2344 023057 101 047 .BYTE 101,47 ;ENABLE GRAPH 0 AND 1
2345 023061 111 061 .BYTE 111,61 ;ENABLE DISPLAY
2346 023063 104 050 065 .BYTE 104,50,65,44,62 ;LOAD HORIZ CORDINATES
      023066 044 062
2347 023070 110 040 040 .BYTE 110,40,40 ;LOAD STARTING GRAPH CORD.
2348 023073 102 000 .BYTE 102,0 ;LOAD GRAPH 0 <DATA TO FOLLOW>
2349 023075 033 061 INITVT: .BYTE 33,61 ;GRAPH ON
2350 023077 101 040 040 .BYTE 101,40,40 ;DISABLE SCREEN
2351 023102 111 060 040 .BYTE 111,60,40 ;SET RECTANGEL ASPECT RATIO
2352 023105 033 062 .BYTE 33,62 ;EXIT GRAPH MODE
2353 ; .BYTE 33,133,77,62,105 ;ENSURE 'ASCII' <CAUSES HOLD SCREEN ON VT55>
2354 023107 033 110 .BYTE 33,110 ;'HOME'
2355 023111 033 112 000 .BYTE 33,112,0 ;'ERASE SCREEN'
2356 023114 033 110 VTINIT: .BYTE 33,110 ;'HOME'
2357 023116 033 112 .BYTE 33,112 ;'ERASE SCREEN'
2358 023120 033 061 .BYTE 33,61 ;ENTER GRAPHIC MODE
2359 023122 101 040 .BYTE 101,40 ;CLEAR GRAPH DATA
2360 023124 033 062 000 .BYTE 33,62,0 ;EXIT GRAPHIC MODE
2361
2362
2363 .SBTTL ASCII TEXT MESSAGES
2364
2365 023127 200 020114 020075 PRIME1: .ASCII <200>/L = LOGIC TEST/
      023134 047514 044507 020103
      023142 042524 052123
2366 023146 053600 036440 053440 .ASCII <200>/W = WRAPAROUND ANALOG TEST/
      023154 040522 040520 047522
      023162 047125 020104 047101
      023170 046101 043517 052040
      023176 051505 124
2367 023201 200 020101 020075 .ASCII <200>/A = AUTO TEST/
      023206 052501 047524 052040
      023214 051505 124
2368 023217 200 020116 020075 .ASCII <200>/N = NOISE TESTS ON SELECTED CHANNELS/

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023224 047516 051511 020105
023232 042524 052123 020123
023240 047117 051440 046105
023246 041505 042524 020104
023254 044103 047101 042516
023262 051514
2369 023264 053200 036440 053040
023272 042511 047504 041040
023300 052111 046440 050101
023306 047440 052125 052520
023314 020124 053101 044501
023322 040514 046102 020105
023330 044450 020105 052126
023336 030061 026065 053040
023344 032524 024465
2370 023350 050200 036440 050040
023356 044522 052116 041440
023364 047117 042526 052122
023372 042105 040440 040516
023400 047514 020107 040526
023406 052514 020105 047514
023414 050117
2371 023416 041600 036440 041440
023424 046101 041111 040522
023432 044524 047117 046040
023440 047517 020120 047506
023446 020122 047115 040503
023454 104
2372 023455 200 020102 020075
023462 040502 042523 040440
023470 042116 053040 041505
023476 047524 020122 042101
023504 051104 051505 020123
023512 044103 047101 042507
023520 123
2373 023521 200 020107 020075
023526 042507 020124 042516
023534 020127 053523 052111
023542 044103 051040 043505
023550 051511 042524 020122
023556 040526 052514 105
2374 023563 200 020110 020075
023570 042510 050114 052040
023576 042510 047440 042520
023604 040522 047524 020122
023612 047101 020104 042522
023620 054524 042520 052040
023626 044510 020123 044514
023634 052123 020040 020040
023642 000
2375 023643 015 012
2376 023645 124 050131 020105
023652 044124 020105 052042
023660 051505 020124 044103

.ASCII <200>/V = VIEDO BIT MAP OUTPUT AVAILABLE (IE VT105, VT55)/

.ASCII <200>/P = PRINT CONVERTED ANALOG VALUE LOOP/

.ASCII <200>/C = CALIBRATION LOOP FOR MNCAD/

.ASCII <200>/B = BASE AND VECTOR ADDRESS CHANGES/

.ASCII <200>/G = GET NEW SWITCH REGISTER VALUE/

.ASCIIZ <200>/H = HELP THE OPERATOR AND RETYPE THIS LIST /

DOT: .BYTE 15,12
.ASCIIZ /TYPE THE 'TEST CHARACTER' THEN DEPRESS 'RETURN KEY' /

	023666	051101	041501	042524				
	023674	021122	052040	042510				
	023702	020116	042504	051120				
	023710	051505	020123	051042				
	023716	052105	051125	020116				
	023724	042513	021131	020040				
	023732	000						
2377	023733	115	041516	042101	EM1:	.ASCIZ	\MNCAD (A/D)	STATUS REG. ERROR\
	023740	024040	027501	024504				
	023746	051411	040524	052524				
	023754	020123	042522	027107				
	023762	042440	051122	051117				
	023770	000						
2378	023771	115	041516	042101	EM2:	.ASCIZ	\MNCAD (A/D)	FAILED TO INTERRUPT\
	023776	024040	027501	024504				
	024004	043011	044501	042514				
	024012	020104	047524	044440				
	024020	052116	051105	052522				
	024026	052120	000					
2379	024031	115	041516	042101	EM3:	.ASCIZ	\MNCAD (A/D)	UNEXPECTED INTERRUPT\
	024036	024040	027501	024504				
	024044	052411	042516	050130				
	024052	041505	042524	020104				
	024060	047111	042524	051122				
	024066	050125	000124					
2380	024072	047115	040503	020104	EM4:	.ASCIZ	#MNCAD (A/D)	ERROR ON A/D CHANNEL#
	024100	040450	042057	004451				
	024106	051105	047522	020122				
	024114	047117	040440	042057				
	024122	041440	040510	047116				
	024130	046105	000					
2381	024133	115	041516	042101	EM5:	.ASCIZ	\MNCAD (A/D)	EXISTING MNCAD NOW FAIL'S TO RESPOND\
	024140	024040	027501	024504				
	024146	042411	044530	052123				
	024154	047111	020107	047115				
	024162	040503	020104	047516				
	024170	020127	040506	046111				
	024176	051447	052040	020117				
	024204	042522	050123	047117				
	024212	000104						
2382	024214	047115	040503	020104	EM6:	.ASCIZ	\MNCAD (A/D)	DOES NOT EXIST <BUS ERROR> CHECK ADDRESS SWITCHES\
	024222	040450	042057	004451				
	024230	047504	051505	047040				
	024236	052117	042440	044530				
	024244	052123	036040	052502				
	024252	020123	051105	047522				
	024260	037122	041440	042510				
	024266	045503	040440	042104				
	024274	042522	051523	051440				
	024302	044527	041524	042510				
	024310	000123						
2383	024312	047125	052111	042411	DH1:	.ASCIZ	/UNIT ERRPC STREG	EXPECTED ACTUAL/
	024320	051122	041520	020040				
	024326	051440	051124	043505				


```

(1) 025066 001021 BNE 32$ ::BRANCH IF NO
(1) 025070 005077 154050 CLR @STKS ::DISABLE TTY KEYBOARD INTERRUPTS
(1) 025074 005726 TST (SP)+ ::CLEAN CHAR OFF STACK
(1) 025076 105777 154042 31$: TSTB @STKS ::WAIT FOR A CHAR
(1) 025102 100375 BPL 31$ ::LOOP UNTIL ITS THERE
(1) 025104 117746 154036 MOVB @STKB, -(SP) ::GET THE CHARACTER
(1) 025110 042716 177600 BIC #^C177, (SP) ::MAKE IT 7-BIT ASCII
(1) 025114 022627 000021 CMP (SP)+, #21 ::IS IT A CONTROL-Q?
(1) 025120 001366 BNE 31$ ::BRANCH IF NO
(1) 025122 012777 000100 154014 MOV #100, @STKS ::REENABLE TTY KEYBOARD INTERRUPTS
(1) 025130 000002 RTI ::RETURN
(1) 025132 005237 024652 32$: INC $TKCNT ::COUNT THIS CHARACTER
(1) 025136 021627 000140 CMP (SP), #140 ::IS IT UPPER CASE?
(1) 025142 002405 BLT 4$ ::BRANCH IF YES
(1) 025144 021627 000175 CMP (SP), #175 ::IS IT A SPECIAL CHAR?
(1) 025150 003002 BGT 4$ ::BRANCH IF YES
(1) 025152 042716 000040 BIC #40, (SP) ::MAKE IT UPPER CASE
(1) 025156 112677 177472 4$: MOVB (SP)+, @STKQIN ::AND PUT IT IN QUEUE
(1) 025162 005237 024654 INC $TKQIN ::UPDATE THE POINTER
(1) 025166 023727 024654 024720 CMP $TKQIN, $TKQEND ::GO OFF THE END?
(1) 025174 001003 BNE 5$ ::BRANCH IF NO
(1) 025176 012737 024660 024654 MOV #STKQSR, $TKQIN ::RESET THE POINTER
(1) 025204 000002 5$: RTI ::RETURN

```

```

(1)
(2) ::*****
(1) ::*SOFTWARE SWITCH REGISTER CHANGE ROUTINE.
(1) ::*ROUTINE IS ENTERED FROM THE TRAP HANDLER, AND WILL
(1) ::*SERVICE THE TEST FOR CHANGE IN SOFTWARE SWITCH REGISTER TRAP
(1) ::*CALL WHEN OPERATING IN TTY INTERRUPT MODE.

```

```

(1) 025206 022737 000176 001140 $CKSWR: CMP #SWREG, SWR ::IS THE SOFT-SWR SELECTED
(1) 025214 001124 BNE 15$ ::EXIT IF NOT
(1) 025216 105777 153722 TSTB @STKS ::IS A CHAR WAITING?
(1) 025222 100121 BPL 15$ ::IF NOT, EXIT
(1) 025224 117746 153716 MOVB @STKB, -(SP) ::YES
(1) 025230 042716 177600 BIC #^C177, (SP) ::MAKE IT 7-BIT ASCII
(1) 025234 021627 000007 CMP (SP), #7 ::IS IT A CONTROL-G?
(1) 025240 001300 BNE 2$ ::IF NOT, PUT IT IN THE TTY QUEUE
(1) ::AND EXIT

```

```

(1)
(2) ::*****
(1) ::*CONTROL IS PASSED TO THIS POINT FROM EITHER THE TTY INTERRUPT SERVICE
(1) ::*ROUTINE OR FROM THE SOFTWARE SWITCH REGISTER TRAP CALL, AS A RESULT OF A
(1) ::*CONTROL-G BEING TYPED, AND THE SOFTWARE SWITCH REGISTER BEING SELECTED.

```

```

(1) 025242 123727 001134 000001 6$: CMPB $AUTOB, #1 ::ARE WE RUNNING IN AUTO-MODE?
(1) 025250 001674 BEQ 2$ ::BRANCH IF YES
(1) 025252 005726 TST (SP)+ ::CLEAR CONTROL-G OFF STACK
(1) 025254 004737 024720 JSR PC, $TKINT ::FLUSH THE TTY INPUT QUEUE
(1) 025260 005077 153660 CLR @STKS ::DISABLE TTY KEYBOARD INTERRUPTS
(1) 025264 112737 000001 001135 MOVB #1, $INTAG ::SET INTERRUPT MODE INDICATOR
(1)
(1) 025272 104401 026152 $GTSWR: TYPE , $CNTLG ::ECHO THE CONTROL-G (^G)
(1) 025276 104401 026157 TYPE , $MSWR ::TYPE CURRENT CONTENTS
(2) 025302 013746 000176 MOV SWREG, -(SP) ::SAVE SWREG FOR TYPEOUT
(2) 025306 104402 TYPOC ::GO TYPE--OCTAL ASCII(ALL DIGITS)

```

```

(1) 025310 104401 026170          TYPE      ,SMNEW          ;;PROMPT FOR NEW SWR
(1) 025314 005046          19$: CLR      -(SP)          ;;CLEAR COUNTER
(1) 025316 005046          CLR      -(SP)          ;;THE NEW SWR
(1) 025320 105777 153620      7$: TSTB   @STKS          ;;CHAR THERE?
(1) 025324 100375          BPL      7$              ;;IF NOT TRY AGAIN
(1)
(1) 025326 117746 153614      MOVB    @STKB,-(SP)      ;;PICK UP CHAR
(1) 025332 042716 177600      BIC     #^C177,(SP)     ;;MAKE IT 7-BIT ASCII
(1)
(1) 025336 021627 000003      CMP     (SP),#3          ;;IS IT A CONTROL-C?
(1) 025342 001015          BNE     9$              ;;BRANCH IF NOT
(1) 025344 104401 026140      TYPE   ,SCNTLC          ;;YES, ECHO CONTROL-C (^C)
(1) 025350 062706 000006      ADD    #6,SP            ;;CLEAN UP STACK
(1) 025354 123727 001135 000001  CMPB   $INTAG,#1        ;;REENABLE TTY KEYBOARD INTERRUPTS?
(1) 025362 001003          BNE     8$              ;;BRANCH IF NO
(1) 025364 012777 000100 153552  MOV    #100,@STKS       ;;ALLOW TTY KEYBOARD INTERRUPTS
(1) 025372 000137 001564      8$: JMP    BEG2          ;;CONTROL-C RESTART
(1)
(1)
(1) 025376 021627 000025      9$: CMP     (SP),#25      ;;IS IT A CONTROL-U?
(1) 025402 001005          BNE     10$             ;;BRANCH IF NOT
(1) 025404 104401 026145      TYPE   ,SCNTLU          ;;YES, ECHO CONTROL-U (^U)
(1) 025410 062706 000006      20$: ADD    #6,SP            ;;IGNORE PREVIOUS INPUT
(1) 025414 000737          BR      19$             ;;LET'S TRY IT AGAIN
(1)
(1)
(1) 025416 021627 000015      10$: CMP     (SP),#15      ;;IS IT A <CR>?
(1) 025422 001022          BNE     16$             ;;BRANCH IF NO
(1) 025424 005766 000004      TST    4(SP)            ;;YES, IS IT THE FIRST CHAR?
(1) 025430 001403          BEQ    11$             ;;BRANCH IF YES
(1) 025432 016677 000002 153500  MOV    2(SP),@SWR        ;;SAVE NEW SWR
(1) 025440 062706 000006      11$: ADD    #6,SP            ;;CLEAN UP STACK
(1) 025444 104401 001165      14$: TYPE   ,SCRLF          ;;ECHO <CR> AND <LF>
(1) 025450 123727 001135 000001  CMPB   $INTAG,#1        ;;RE-ENABLE TTY KBD INTERRUPTS?
(1) 025456 001003          BNE     15$             ;;BRANCH IF NOT
(1) 025460 012777 000100 153456  MOV    #100,@STKS       ;;RE-ENABLE TTY KBD INTERRUPTS
(1) 025466 000002          RTI                                ;;RETURN
(1) 025470 004737 027622      16$: JSR    PC,$TYPEC        ;;ECHO CHAR
(1) 025474 021627 000060      CMP     (SP),#60        ;;CHAR < 0?
(1) 025500 002420          BLT    18$             ;;BRANCH IF YES
(1) 025502 021627 000067      CMP     (SP),#67        ;;CHAR > 7?
(1) 025506 003015          BGT    18$             ;;BRANCH IF YES
(1) 025510 042726 000060      BIC     #60,(SP)+        ;;STRIP-OFF ASCII
(1) 025514 005766 000002      TST    2(SP)            ;;IS THIS THE FIRST CHAR
(1) 025520 001403          BEQ    17$             ;;BRANCH IF YES
(1) 025522 006316          ASL    (SP)              ;;NO, SHIFT PRESENT
(1) 025524 006316          ASL    (SP)              ;;CHAR OVER TO MAKE
(1) 025526 006316          ASL    (SP)              ;;ROOM FOR NEW ONE.
(1) 025530 005266 000002      17$: INC    2(SP)            ;;KEEP COUNT OF CHAR
(1) 025534 056616 177776      BIS    -2(SP),(SP)      ;;SET IN NEW CHAR
(1) 025540 000667          BR      7$              ;;GET THE NEXT ONE
(1) 025542 104401 001164      18$: TYPE   ,SQUES          ;;TYPE ?<CR><LF>
(1) 025546 000720          BR      20$             ;;SIMULATE CONTROL-U
(1) .DSABL  LSB

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(1)
(1)
(2)
(1)
(1)
(1)
(1)
(1)
(1)
(1)
(1)
(1)
(1) 025550 011646
(1) 025552 016666 000004 000002
(1) 025560 005066 000004
(2) 025564 005046
(2) 025566 012746 025574
(2) 025572 000002
(2) 025574
(1) 025574 005737 024652
(1) 025600 001775
(1) 025602 005337 024652
(1) 025606 117766 177044 000004
(1) 025614 005237 024656
(1) 025620 023727 024656 024720
(1) 025626 001003
(1) 025630 012737 024660 024656
(1) 025636 000002
(2)
(1)
(1)
(1)
(1)
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(1)
(1)
(1)
(1)
(1)
(1) 025640 010346
(1) 025642 005046
(1) 025644 012703 026074
(1) 025650 022703 026134
(1) 025654 101456
(1) 025656 104411
(1) 025660 112613
(1) 025662 122713 000177
(1) 025666 001022
(1) 025670 005716
(1) 025672 001007
(1) 025674 112737 000134 026072
(1) 025702 104401 026072
(1) 025706 012716 177777
(1) 025712 005303
(1) 025714 020327 026074
(1) 025720 103434
(1) 025722 111337 026072
(1) 025726 104401 026072
(1) 025732 000746
(1) 025734 005716

:::*****
:::THIS ROUTINE WILL INPUT A SINGLE CHARACTER FROM THE TTY
:::CALL:
:::RDCHR          ;;GET A CHARACTER FROM THE QUEUE
:::RETURN HERE   ;;CHARACTER IS ON THE STACK
:::              ;;WITH PARITY BIT STRIPPED OFF

$RDCHR: MOV      (SP),-(SP)      ;;PUSH DOWN THE PC AND
        MOV      4(SP),2(SP)    ;;THE PS
        CLR      4(SP)          ;;GET READY FOR A CHARACTER
        CLR      -(SP)         ;;PUT NEW PS ON STACK
        MOV      #64$,-(SP)     ;;PUT NEW PC ON STACK
        RTI                     ;;POP NEW PC AND PS

64$:
1$:    TST      $STKCNT        ;;WAIT ON A CHARACTER
        BEQ      1$
        DEC      $STKCNT        ;;DECREMENT THE COUNTER
        MOV      @STKQOUT,4(SP) ;;GET ONE CHARACTER
        INC      $STKQOUT       ;;UPDATE THE POINTER
        CMP      $STKQOUT,#$STKQEND ;;DID IT GO OFF OF THE END?
        BNE      2$            ;;BRANCH IF NO
        MOV      #$STKQSRT,$STKQOUT ;;RESET THE POINTER
        RTI                     ;;RETURN

2$:
:::*****
:::THIS ROUTINE WILL INPUT A STRING FROM THE TTY
:::CALL:
:::RDLIN          ;;INPUT A STRING FROM THE TTY
:::RETURN HERE   ;;ADDRESS OF FIRST CHARACTER WILL BE ON THE STACK
:::              ;;TERMINATOR WILL BE A BYTE OF ALL 0'S

$RDLIN: MOV      R3,-(SP)      ;;SAVE R3
        CLR      -(SP)         ;;CLEAR THE RUBOUT KEY
1$:    MOV      #$TTYIN,R3     ;;GET ADDRESS
2$:    CMP      #$TTYIN+32.,R3 ;;BUFFER FULL?
        BLOS    4$            ;;BR IF YES
        RDCHR   ;;GO READ ONE CHARACTER FROM THE TTY
        MOV      (SP)+,(R3)    ;;GET CHARACTER
        CMP     #177,(R3)     ;;IS IT A RUBOUT
        BNE     5$            ;;BR IF NO
        TST     (SP)          ;;IS THIS THE FIRST RUBOUT?
        BNE     6$            ;;BR IF NO
        MOV     #'\,9$        ;;TYPE A BACK SLASH
        TYPE    ,9$
        MOV     #-1,(SP)     ;;SET THE RUBOUT KEY
6$:    DEC     R3              ;;BACKUP BY ONE
        CMP     R3,$$TTYIN   ;;STACK EMPTY?
        BLOS    4$            ;;BR IF YES
        MOV     (R3),9$      ;;SETUP TO TYPEOUT THE DELETED CHAR.
        TYPE    ,9$
        BR     2$            ;;GO READ ANOTHER CHAR.
5$:    TST     (SP)          ;;RUBOUT KEY SET?
    
```


(1)	025736	001406			BEQ	7\$::BR IF NO
(1)	025740	112737	000134	026072	MOVB	#'\,9\$::TYPE A BACK SLASH
(1)	025746	104401	026072		TYPE	,9\$	
(1)	025752	005016			CLR	(SP)	::CLEAR THE RUBOUT KEY
(1)	025754	122713	000025		7\$: CMPB	#25,(R3)	::IS CHARACTER A CTRL U?
(1)	025760	001003			BNE	8\$::BR IF NO
(1)	025762	104401	026145		TYPE	,SCNTLU	::TYPE A CONTROL 'U'
(1)	025766	000726			BR	1\$::GO START OVER
(1)	025770	122713	000022		8\$: CMPB	#22,(R3)	::IS CHARACTER A '^R'?
(1)	025774	001011			BNE	3\$::BRANCH IF NO
(1)	025776	105013			CLRB	(R3)	::CLEAR THE CHARACTER
(1)	026000	104401	001165		TYPE	,SCRLF	::TYPE A 'CR' & 'LF'
(1)	026004	104401	026074		TYPE	,STTYIN	::TYPE THE INPUT STRING
(1)	026010	000717			BR	2\$::GO PICKUP ANOTHER CHACTER
(1)	026012	104401	001164		4\$: TYPE	,SQUES	::TYPE A '?'
(1)	026016	000712			BR	1\$::CLEAR THE BUFFER AND LOOP
(1)	026020	111337	026072		3\$: MOVB	(R3),9\$::ECHO THE CHARACTER
(1)	026024	104401	026072		TYPE	,9\$	
(1)	026030	122723	000015		CMPB	#15,(R3)+	::CHECK FOR RETURN
(1)	026034	001305			BNE	2\$::LOOP IF NOT RETURN
(1)	026036	105063	177777		CLRB	-1(R3)	::CLEAR RETURN (THE 15)
(1)	026042	104401	001166		TYPE	,SLF	::TYPE A LINE FEED
(1)	026046	005726			TST	(SP)+	::CLEAN RUBOUT KEY FROM THE STACK
(1)	026050	012603			MOV	(SP)+,R3	::RESTORE R3
(1)	026052	011646			MOV	(SP)-,(SP)	::ADJUST THE STACK AND PUT ADDRESS OF THE
(1)	026054	016666	000004	000002	MOV	4(SP),2(SP)	:: FIRST ASCII CHARACTER ON IT
(1)	026062	012766	026074	000004	MOV	#STTYIN,4(SP)	
(1)	026070	000002			RTI		::RETURN
(1)	026072	000			9\$: .BYTE	0	::STORAGE FOR ASCII CHAR. TO TYPE
(1)	026073	000			.BYTE	0	::TERMINATOR
(1)	026074	000040			STTYIN: .BLKB	32.	::RESERVE 32. BYTES FOR TTY INPUT
(1)	026134	177607	000377		\$BELL: .ASCIZ	<207><377><377>	::CODE FOR BELL
(1)	026140	041536	005015	000	\$CNTLC: .ASCIZ	/^C/<15><12>	::CONTROL 'C'
(1)	026145	136	006525	000012	\$CNTLU: .ASCIZ	/^U/<15><12>	::CONTROL 'U'
(1)	026152	043536	005015	000	\$CNTLG: .ASCIZ	/^G/<15><12>	::CONTROL 'G'
(1)	026157	015	051412	051127	\$MSWR: .ASCIZ	<15><12>/SWR = /	
(1)	026164	036440	000040				
(1)	026170	020040	042516	020127	\$MNEW: .ASCIZ	/ NEW = /	
(1)	026176	020075	000				
(1)		026202			.EVEN		

```
2404      .SBTTL  READ AN OCTAL NUMBER FROM THE TTY
(1)
(2)      ::*****
(1)      ::*THIS ROUTINE WILL READ AN OCTAL (ASCII) NUMBER FROM THE TTY AND
(1)      ::*CHANGE IT TO BINARY.
(1)      ::*CALL:
(1)      ::*      RDOCT          ;;READ AN OCTAL NUMBER
(1)      ::*      RETURN HERE  ;;LOW ORDER BITS ARE ON TOP OF THE STACK
(1)      ::*                ;;HIGH ORDER BITS ARE IN $HIOCT
(1)
(1) 026202 011646      $RDOCT: MOV      (SP),-(SP)      ;;PROVIDE SPACE FOR THE
(1) 026204 016666      MOV      4(SP),2(SP)      ;;INPUT NUMBER
(3) 026212 010046      MOV      R0,-(SP)      ;;PUSH R0 ON STACK
(3) 026214 010146      MOV      R1,-(SP)      ;;PUSH R1 ON STACK
(3) 026216 010246      MOV      R2,-(SP)      ;;PUSH R2 ON STACK
(1) 026220 104412      1$:      RDLIN      ;;READ AN ASCII LINE
(1) 026222 012600      MOV      (SP)+,R0      ;;GET ADDRESS OF 1ST CHARACTER
(1) 026224 005001      CLR      R1          ;;CLEAR DATA WORD
(1) 026226 005002      CLR      R2
(1) 026230 112046      2$:      MOVB      (R0)+,-(SP)      ;;PICKUP THIS CHARACTER
(1) 026232 001412      BEQ      3$          ;;IF ZERO GET OUT
(1) 026234 006301      ASL      R1          ;;*2
(1) 026236 006102      ROL      R2
(1) 026240 006301      ASL      R1          ;;*4
(1) 026242 006102      ROL      R2
(1) 026244 006301      ASL      R1          ;;*8
(1) 026246 006102      ROL      R2
(1) 026250 042716      BIC      #^C7,(SP)      ;;STRIP THE ASCII JUNK
(1) 026254 062601      ADD      (SP)+,R1      ;;ADD IN THIS DIGIT
(1) 026256 000764      BR       2$          ;;LOOP
(1) 026260 005726      3$:      TST      (SP)+      ;;CLEAN TERMINATOR FROM STACK
(1) 026262 010166      MOV      R1,12(SP)      ;;SAVE THE RESULT
(1) 026266 010237      MOV      R2,$HIOCT
(3) 026272 012602      MOV      (SP)+,R2      ;;POP STACK INTO R2
(3) 026274 012601      MOV      (SP)+,R1      ;;POP STACK INTO R1
(3) 026276 012600      MOV      (SP)+,R0      ;;POP STACK INTO R0
(1) 026300 000002      RTI
(1) 026302 000000      $HIOCT: .WORD 0      ;;HIGH ORDER BITS GO HERE
```



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(1) 026512 105237 001102          $SVLAD: INCB      $STSTM          ;;COUNT TEST NUMBERS
(1) 026516 113737 001102 001174   MOV      $STSTM,$TESTN ;;SET TEST NUMBER IN APT MAILBOX
(1) 026524 011637 001106          MOV      (SP),$LPADR  ;;SAVE SCOPE LOOP ADDRESS
(1) 026530 011637 001110          MOV      (SP),$LPERR  ;;SAVE ERROR LOOP ADDRESS
(1) 026534 005037 001162          CLR      $ESCAPE     ;;CLEAR THE ESCAPE FROM ERROR ADDRESS
(1) 026540 112737 000001 001115   MOV      #1,$ERMAX   ;;ONLY ALLOW ONE(1) ERROR ON NEXT TEST
(1) 026546 013777 001102 152366   $OVER:  MOV      $STSTM,@DISPLAY ;;DISPLAY TEST NUMBER
(1) 026554 013716 001106          MOV      $LPADR,(SP) ;;FUDGE RETURN ADDRESS
(1) 026560 000002          RTI              ;;FIXES PS
(1) 026562 003720          $MXCNT: 2000.    ;;MAX. NUMBER OF ITERATIONS
2407 026564 053737 001512 001510   WHICHV: BIS      MASKNM,BADUNT ;;SET CURRENT UNIT INTO BAD FIELD
2408 026572 013737 001512 026622   WHICHU: MOV      MASKNM,11$    ;;GET CURRENT UNIT
2409 026600 012737 000000 001514   MOV      #0,UNITBD   ;;PRIME THE VALUE
2410 026606 006237 026622          10$:  ASR      11$      ;;CONVERT
2411 026612 001404          BEQ      12$         ;;BR WHEN DONE
2412 026614 005237 001514          INC      UNITBD     ;;BUMP POINTER
2413 026620 000772          BR       10$
2414 026622 000000          11$:  0
2415 026624 000207          12$:  RTS      PC          ;;EXIT
2419          .SBTTL  ERROR HANDLER ROUTINE

(1)          ;;*****
(2)          ;;*THIS ROUTINE WILL INCREMENT THE ERROR FLAG AND THE ERROR COUNT,
(1)          ;;*SAVE THE ERROR ITEM NUMBER AND THE ADDRESS OF THE ERROR CALL
(1)          ;;*AND GO TO $ERRTYP ON ERROR
(1)          ;;*THE SWITCH OPTIONS PROVIDED BY THIS ROUTINE ARE:
(1)          ;;*SW15=1      HALT ON ERROR
(1)          ;;*SW13=1      INHIBIT ERROR TYPEOUTS
(1)          ;;*SW09=1      LOOP ON ERROR
(1)          ;;*CALL
(1)          ;;*      ERROR      N      ;;ERROR=EMT AND N=ERROR ITEM NUMBER

(1) 026626          $ERROR:
(1) 026626 104410          CKSWR          ;;TEST FOR CHANGE IN SOFT-SWR
(3) 026630 004737 026564          JSR      PC,WHICHV  ;;INDICATE BAD UNIT
(1) 026634 105237 001103          7$:  INCB      $ERFLG   ;;SET THE ERROR FLAG
(1) 026640 001775          BEQ      7$         ;;DON'T LET THE FLAG GO TO ZERO
(1) 026642 013777 001102 152272   MOV      $STSTM,@DISPLAY ;;DISPLAY TEST NUMBER AND ERROR FLAG
(1) 026650 005237 001112          INC      $ERTTL    ;;INC THE ERROR COUNT
(1) 026654 011637 001116          MOV      (SP),$ERRPC ;;GET ADDRESS OF ERROR INSTRUCTION
(1) 026660 162737 000002 001116   SUB      #2,$ERRPC
(1) 026666 117737 152224 001114   MOV      @ $ERRPC,$ITEMB ;;STRIP AND SAVE THE ERROR ITFM CODE
(1) 026674 032777 020000 152236   BIT      #BIT13,@SWR  ;;SKIP TYPEOUT IF SET
(1) 026702 001004          BNE      20$       ;;SKIP TYPEOUTS
(1) 026704 004737 027016          JSR      PC,$ERRTYP ;;GO TO USER ERROR ROUTINE
(1) 026710 104401 001165          TYPE      $CRLF

(1) 026714          20$:
(1) 026714 122737 000001 001210   CMPB     #APTENV,$ENV ;;RUNNING IN APT MODE
(1) 026722 001007          BNE      2$        ;;NO,SKIP APT ERROR REPORT
(1) 026724 113737 001114 026736   MOV      $ITEMB,21$  ;;SET ITEM NUMBER AS ERROR NUMBER
(1) 026732 004737 030134          JSR      PC,$ATY4   ;;REPORT FATAL ERROR TO APT
(1) 026736 000          21$:  .BYTE  0
(1) 026737 000          .BYTE  0
(1) 026740 000777          22$:  BR       22$      ;;APT ERROR LOOP

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(1) 026742 005777 152172 2$: TST @SWR ;;HALT ON ERROR
(1) 026746 100002 BPL 3$ ;;SKIP IF CONTINUE
(1) 026750 000000 HALT ;;HALT ON ERROR!
(1) 026752 104410 CKSWR ;;TEST FOR CHANGE IN SOFT-SWR
(1) 026754 032777 001000 152156 3$: BIT #BIT09,@SWR ;;LOOP ON ERROR SWITCH SET?
(1) 026762 001402 BEQ 4$ ;;BR IF NO
(1) 026764 013716 001110 MOV $LPERR,(SP) ;;FUDGE RETURN FOR LOOPING
(1) 026770 005737 001162 4$: TST $ESCAPE ;;CHECK FOR AN ESCAPE ADDRESS
(1) 026774 001402 BEQ 5$ ;;BR IF NONE
(1) 026776 013716 001162 MOV $ESCAPE,(SP) ;;FUDGE RETURN ADDRESS FOR ESCAPE
(1) 027002 5$: CMP #SENDAD,@#42 ;;ACT-11 AUTO-ACCEPT?
(1) 027002 022737 016500 000042 BNE 6$ ;;BRANCH IF NO
(1) 027010 001001 HALT ;;YES
(1) 027012 000000
(1) 027014 6$: RTI ;;RETURN
(1) 027014 000002 .SBTTL ERROR MESSAGE TYPEOUT ROUTINE
2420

```

```

*****
*THIS ROUTINE USES THE "ITEM CONTROL BYTE" ($ITEMB) TO DETERMINE WHICH
*ERROR IS TO BE REPORTED. IT THEN OBTAINS, FROM THE "ERROR TABLE" ($ERRTB),
*AND REPORTS THE APPROPRIATE INFORMATION CONCERNING THE ERROR.

```

\$ERRTYP:

```

(1) 027016 104401 001165 TYPE , $CRLF ;;'CARRIAGE RETURN' & 'LINE FEED'
(1) 027022 010046 MOV RO,-(SP) ;;SAVE RO
(1) 027024 005000 CLR RO ;;PICKUP THE ITEM INDEX
(1) 027026 153700 001114 BISB @#$ITEMB,RO
(1) 027032 001004 BNE 1$ ;;IF ITEM NUMBER IS ZERO, JUST
(1) 027034 013746 001116 MOV $ERRPC,-(SP) ;;TYPE THE PC OF THE ERROR
(2) 027040 104402 TYPOC ;;SAVE $ERRPC FOR TYPEOUT
(1) 027042 000445 BR 10$ ;;ERROR ADDRESS
(1) 027044 005300 1$: DEC RO ;;GO TYPE--OCTAL ASCII(ALL DIGITS)
(1) 027046 006300 ASL RO ;;GET OUT
(1) 027050 006300 ASL RO ;;ADJUST THE INDEX SO THAT IT WILL
(1) 027052 006300 ASL RO ;; WORK FOR THE ERROR TABLE
(1) 027054 062700 001252 ADD #ERRTB,RO ;;FORM TABLE POINTER
(1) 027060 012037 027070 MOV (RO)+,2$ ;;PICKUP 'ERROR MESSAGE' POINTER
(1) 027064 001404 BEQ 3$ ;;SKIP TYPEOUT IF NO POINTER
(1) 027066 104401 TYPE ;;TYPE THE 'ERROR MESSAGE'
(1) 027070 000000 2$: .WORD 0 ;;'ERROR MESSAGE' POINTER GOES HERE
(1) 027072 104401 001165 TYPE , $CRLF ;;'CARRIAGE RETURN' & 'LINE FEED'
(1) 027076 012037 027106 3$: MOV (RO)+,4$ ;;PICKUP 'DATA HEADER' POINTER
(1) 027102 001404 BEQ 5$ ;;SKIP TYPEOUT IF 0
(1) 027104 104401 TYPE ;;TYPE THE 'DATA HEADER'
(1) 027106 000000 4$: .WORD 0 ;;'DATA HEADER' POINTER GOES HERE
(1) 027110 104401 001165 TYPE , $CRLF ;;'CARRIAGE RETURN' & 'LINE FEED'
(1) 027114 010146 5$: MOV R1,-(SP) ;;SAVE R1
(1) 027116 012001 MOV (RO)+,R1 ;;PICKUP 'DATA TABLE' POINTER
(1) 027120 001415 BEQ 9$ ;;BR IF NO DATA TO BE TYPED
(1) 027122 012000 MOV (RO)+,RO ;;PICKUP 'DATA FORMAT' POINTER
(1) 027124 105720 6$: TSTB (RO)+ ;;'OCTAL' OR 'DECIMAL'

```

```

(1) 027126 001003 BNE 7$ ::BR IF DECIMAL
(2) 027130 013146 MOV @ (R1)+,-(SP) ::SAVE @ (R1)+ FOR TYPEOUT
(2) 027132 104402 TYPOC ::GO TYPE--OCTAL ASCII(ALL DIGITS)
(1) 027134 000402 BR 8$
(2) 027136 7$: MOV @ (R1)+,-(SP) ::SAVE @ (R1)+ FOR TYPEOUT
(2) 027136 013146 TYPDS ::GO TYPE--DECIMAL ASCII WITH SIGN
(2) 027140 104405 8$: TST (R1) ::IS THERE ANOTHER NUMBER?
(1) 027142 005711 BEQ 9$ ::BR IF NO
(1) 027144 001403 TYPE ,11$ ::TYPE TWO(2) SPACES
(1) 027146 104401 027166 BR 6$ ::LOOP
(1) 027152 000764
(1) 027154 012601 9$: MOV (SP)+,R1 ::RESTORE R1
(1) 027156 012600 10$: MOV (SP)+,R0 ::RESTORE R0
(1) 027160 104401 001165 TYPE , $CRLF ::'CARRIAGE RETURN' & 'LINE FEED'
(1) 027164 000207 RTS PC ::RETURN
(1) 027166 020040 000 11$: .ASCIZ / / ::TWO(2) SPACES
(1) 027172 027172 .EVEN
    
```

2421 .SBTTL POWER DOWN AND UP ROUTINES

```

(1) *****
(2) ::POWER DOWN ROUTINE
(1) $PWRDN: MOV # $ILLUP,@#PWRVEC ::SET FOR FAST UP
(1) 027172 012737 027336 000024 MOV #340,@#PWRVEC+2 ::PRIO:7
(1) 027200 012737 000340 000026 MOV R0,-(SP) ::PUSH R0 ON STACK
(3) 027206 010046 MOV R1,-(SP) ::PUSH R1 ON STACK
(3) 027210 010146 MOV R2,-(SP) ::PUSH R2 ON STACK
(3) 027212 010246 MOV R3,-(SP) ::PUSH R3 ON STACK
(3) 027214 010346 MOV R4,-(SP) ::PUSH R4 ON STACK
(3) 027216 010446 MOV R5,-(SP) ::PUSH R5 ON STACK
(3) 027220 010546 MOV @SWR,-(SP) ::PUSH @SWR ON STACK
(3) 027222 017746 151712 MOV SP,$SAVR6 ::SAVE SP
(1) 027226 010637 027342 MOV # $PWRUP,@#PWRVEC ::SET UP VECTOR
(1) 027232 012737 027244 000024 HALT
(1) 027240 000000 BR -.2 ::HANG UP
(1) 027242 000776
(1) *****
(2) ::POWER UP ROUTINE
(1) $PWRUP: MOV # $ILLUP,@#PWRVEC ::SET FOR FAST DOWN
(1) 027244 012737 027336 000024 MOV $SAVR6,SP ::GET SP
(1) 027252 013706 027342 CLR $SAVR6 ::WAIT LOOP FOR THE TTY
(1) 027256 005037 027342 1$: INC $SAVR6 ::WAIT FOR THE INC
(1) 027262 005237 027342 BNE 1$ ::OF WORD
(3) 027270 012677 151644 MOV (SP)+,@SWR ::POP STACK INTO @SWR
(3) 027274 012605 MOV (SP)+,R5 ::POP STACK INTO R5
(3) 027276 012604 MOV (SP)+,R4 ::POP STACK INTO R4
(3) 027300 012603 MOV (SP)+,R3 ::POP STACK INTO R3
(3) 027302 012602 MOV (SP)+,R2 ::POP STACK INTO R2
(3) 027304 012601 MOV (SP)+,R1 ::POP STACK INTO R1
(3) 027306 012600 MOV (SP)+,R0 ::POP STACK INTO R0
(1) 027310 012737 027172 000024 MOV # $PWRDN,@#PWRVEC ::SET UP THE POWER DOWN VECTOR
(1) 027316 012737 000340 000026 MOV #340,@#PWRVEC+2 ::PRIO:7
(1) 027324 104401 TYPE ::REPORT THE POWER FAILURE
(1) 027326 027344 $PWMSG: .WORD PWMSG ::POWER FAIL MESSAGE POINTER
    
```

```
(1) 027330 012716  
(1) 027332 001556  
(1) 027334 000002  
(1) 027336 000000  
(1) 027340 000776  
(1) 027342 000000  
2422 027344 051200 051505 040524  
      027352 052122 047111 020107  
      027360 043101 042524 020122  
      027366 020101 047520 042527  
      027374 020122 040506 046111  
      027402 051125 020105 000040
```

MOV (PC)+,(SP) ;:RESTART AT BEGIN
\$PWRAD: .WORD BEGIN ;:RESTART ADDRESS
RTI
\$ILLUP: HALT ;:THE POWER UP SEQUENCE WAS STARTED
BR .-2 ;: BEFORE THE POWER DOWN WAS COMPLETE
\$SAVR6: 0 ;:PUT THE SP HERE
PWRMSG: .ASCIZ <200>/RESTARTING AFTER A POWER FAILURE /


```
(1) 027564 004737 027622 JSR PC,$TYPEC ;;GO TYPE A NULL
(1) 027570 105337 027666 DECB $CHARCNT ;;DO NOT COUNT AS A COUNT
(1) 027574 000770 BR 7$ ;;LOOP
(1)
(1) ;HORIZONTAL TAB PROCESSOR
(1)
(1) 027576 112716 000040 8$: MOVB #' ,(SP) ;;REPLACE TAB WITH SPACE
(1) 027602 004737 027622 9$: JSR PC,$TYPEC ;;TYPE A SPACE
(1) 027606 132737 000007 027666 BITB #7,$CHARCNT ;;BRANCH IF NOT AT
(1) 027614 001372 BNE 9$ ;;TAB STOP
(1) 027616 005726 TST (SP)+ ;;POP SPACE OFF STACK
(1) 027620 000724 BR 2$ ;;GET NEXT CHARACTER
(1) 027622 105777 151322 $TYPEC: TSTB @$TSPS ;;WAIT UNTIL PRINTER IS READY
(1) 027626 100375 BPL $TYPEC
(1) 027630 116677 000002 151314 MOVB 2(SP),@$TPB ;;LOAD CHAR TO BE TYPED INTO DATA REG.
(1) 027636 122766 000015 000002 CMPB #CR,2(SP) ;;IS CHARACTER A CARRIAGE RETURN?
(1) 027644 001003 BNE 1$ ;;BRANCH IF NO
(1) 027646 105037 027666 CLRB $CHARCNT ;;YES--CLEAR CHARACTER COUNT
(1) 027652 000406 BR $TYPEX ;;EXIT
(1) 027654 122766 000012 000002 1$: CMPB #LF,2(SP) ;;IS CHARACTER A LINE FEED?
(1) 027662 001402 BEQ $TYPEX ;;BRANCH IF YES
(1) 027664 105227 INCB (PC)+ ;;COUNT THE CHARACTER
(1) 027666 000000 $CHARCNT: .WORD 0 ;;CHARACTER COUNT STORAGE
(1) 027670 000207 $TYPEX: RTS PC
(1)
2425 .SBTTL CONVERT BINARY TO DECIMAL AND TYPE ROUTINE
(1)
(2)
(1) ;*****
(1) ;*THIS ROUTINE IS USED TO CHANGE A 16-BIT BINARY NUMBER TO A 5-DIGIT
(1) ;*SIGNED DECIMAL (ASCII) NUMBER AND TYPE IT. DEPENDING ON WHETHER THE
(1) ;*NUMBER IS POSITIVE OR NEGATIVE A SPACE OR A MINUS SIGN WILL BE TYPED
(1) ;*BEFORE THE FIRST DIGIT OF THE NUMBER. LEADING ZEROS WILL ALWAYS BE
(1) ;*REPLACED WITH SPACES.
(1) ;*CALL:
(1) ;* MOV NUM,-(SP) ;;PUT THE BINARY NUMBER ON THE STACK
(1) ;* TYPDS ;;GO TO THE ROUTINE
(1)
(2) $TYPDS:
(3) 027672 MOV R0,-(SP) ;;PUSH R0 ON STACK
(3) 027674 MOV R1,-(SP) ;;PUSH R1 ON STACK
(3) 027676 MOV R2,-(SP) ;;PUSH R2 ON STACK
(3) 027700 MOV R3,-(SP) ;;PUSH R3 ON STACK
(3) 027702 MOV R5,-(SP) ;;PUSH R5 ON STACK
(1) 027704 MOV #20200,-(SP) ;;SET BLANK SWITCH AND SIGN
(1) 027710 MOV 20(SP),R5 ;;GET THE INPUT NUMBER
(1) 027714 BPL 1$ ;;BR IF INPUT IS POS.
(1) 027716 NEG R5 ;;MAKE THE BINARY NUMBER POS.
(1) 027720 MOVB #'-,1(SP) ;;MAKE THE ASCII NUMBER NEG.
(1) 027726 005000 000055 000001 1$: CLR R0 ;;ZERO THE CONSTANTS INDEX
(1) 027730 012703 030106 MOV #$DBLK,R3 ;;SETUP THE OUTPUT POINTER
(1) 027734 112723 000040 MOVB #' ,(R3)+ ;;SET THE FIRST CHARACTER TO A BLANK
(1) 027740 005002 2$: CLR R2 ;;CLEAR THE BCD NUMBER
(1) 027742 016001 030076 MOV $DTBL(R0),R1 ;;GET THE CONSTANT
(1) 027746 160105 3$: SUB R1,R5 ;;FORM THIS BCD DIGIT
```

```

(1) 027750 002402          BLT      4$          ;;BR IF DONE
(1) 027752 005202          INC      R2          ;;INCREASE THE BCD DIGIT BY 1
(1) 027754 000774          BR       3$
(1) 027756 060105          4$:  ADD     R1,R5      ;;ADD BACK THE CONSTANT
(1) 027760 005702          TST     R2          ;;CHECK IF BCD DIGIT=0
(1) 027762 001002          BNE     5$          ;;FALL THROUGH IF 0
(1) 027764 105716          TSTB   (SP)        ;;STILL DOING LEADING 0'S?
(1) 027766 100407          BMI     7$          ;;BR IF YES
(1) 027770 106316          5$:  ASLB   (SP)        ;;MSD?
(1) 027772 103003          BCC     6$          ;;BR IF NO
(1) 027774 116663 000001 177777  MOVB   1(SP),-1(R3) ;;YES--SET THE SIGN
(1) 030002 052702 000060 6$:  BIS    #'0,R2      ;;MAKE THE BCD DIGIT ASCII
(1) 030006 052702 000040 7$:  BIS    #' ,R2      ;;MAKE IT A SPACE IF NOT ALREADY A DIGIT
(1) 030012 110223          MOVB   R2,(R3)+    ;;PUT THIS CHARACTER IN THE OUTPUT BUFFER
(1) 030014 005720          TST    (R0)+      ;;JUST INCREMENTING
(1) 030016 020027 000010  CMP    R0,#10     ;;CHECK THE TABLE INDEX
(1) 030022 002746          BLT    2$          ;;GO DO THE NEXT DIGIT
(1) 030024 003002          BGT    8$          ;;GO TO EXIT
(1) 030026 010502          MOV    R5,R2      ;;GET THE LSD
(1) 030030 000764          BR     6$          ;;GO CHANGE TO ASCII
(1) 030032 105726          8$:  TSTB   (SP)+    ;;WAS THE LSD THE FIRST NON-ZERO?
(1) 030034 100003          BPL    9$          ;;BR IF NO
(1) 030036 116663 177777 177776  MOVB   -1(SP),-2(R3) ;;YES--SET THE SIGN FOR TYPING
(1) 030044 105013          9$:  CLRB   (R3)      ;;SET THE TERMINATOR
(3) 030046 012605          MOV    (SP)+,R5   ;;POP STACK INTO R5
(3) 030050 012603          MOV    (SP)+,R3   ;;POP STACK INTO R3
(3) 030052 012602          MOV    (SP)+,R2   ;;POP STACK INTO R2
(3) 030054 012601          MOV    (SP)+,R1   ;;POP STACK INTO R1
(3) 030056 012600          MOV    (SP)+,R0   ;;POP STACK INTO R0
(1) 030060 104401 030106  TYPE    $DBLK      ;;NOW TYPE THE NUMBER
(1) 030064 016666 000002 000004  MOV    2(SP),4(SP) ;;ADJUST THE STACK
(1) 030072 012616          MOV    (SP)+,(SP)
(1) 030074 000002          RTI
(1) 030076 023420          $DTBL: 10000.    ;;RETURN TO USER
(1) 030100 001750          1000.
(1) 030102 000144          100.
(1) 030104 000012          10.
(1) 030106 000004          $DBLK: .BLKW 4
2426 .SBTTL APT COMMUNICATIONS ROUTINE
(1)
(2)
(1) 030116 112737 000001 030362 $ATY1: MOVB   #1,$FFLG      ;;TO REPORT FATAL ERROR
(1) 030124 112737 000001 030360 $ATY3: MOVB   #1,$MFLG      ;;TO TYPE A MESSAGE
(1) 030132 000403          BR     $ATYC
(1) 030134 112737 000001 030362 $ATY4: MOVB   #1,$FFLG      ;;TO ONLY REPORT FATAL ERROR
(2) 030142          $ATYC:
(3) 030142 010046          MOV    R0,-(SP)   ;;PUSH R0 ON STACK
(3) 030144 010146          MOV    R1,-(SP)   ;;PUSH R1 ON STACK
(1) 030146 105737 030360          TSTB   $MFLG      ;;SHOULD TYPE A MESSAGE?
(1) 030152 001450          BEQ    5$          ;;IF NOT: BR
(1) 030154 122737 000001 001210  CMPB   #APTENV,$ENV ;;OPERATING UNDER APT?
(1) 030162 001031          BNE    3$          ;;IF NOT: BR
(1) 030164 132737 000100 001211  BITB   #APTPOOL,$ENVM ;;SHOULD SPOOL MESSAGES?
(1) 030172 001425          BEQ    3$          ;;IF NOT: BR
    
```


2431
 (1)
 (2)
 (1)
 (1)
 (1)
 (1)
 (1)
 (1) 030666 010046
 (1) 030670 016600 000002
 (1) 030674 005740
 (1) 030676 111000
 (1) 030700 006300
 (1) 030702 016000 030722
 (1) 030706 000200
 (1)
 (1)
 (1)
 (1) 030710 011646
 (1) 030712 016666 000004 000002
 (1) 030720 000002
 (1)
 (3)
 (3)
 (3)
 (3)
 (3)
 (3) 030722 030710
 (3) 030724 027410
 (3) 030726 030410
 (3) 030730 030364
 (3) 030732 030424
 (3) 030734 027672
 (3) 030736 030612
 (1)
 (3) 030740 025276
 (1)
 (3) 030742 025206
 (3) 030744 025550
 (3) 030746 025640
 (3) 030750 026202
 2432 030752 005362
 2433 030754 005354
 2434 030756 016052
 2435 030760 010532
 2436 030762 013416
 2437 030764 000310
 2438 031604 010000
 2439
 2440 000001

.SBTTL TRAP DECODER

 ;*THIS ROUTINE WILL PICKUP THE LOWER BYTE OF THE 'TRAP' INSTRUCTION
 ;*AND USE IT TO INDEX THROUGH THE TRAP TABLE FOR THE STARTING ADDRESS
 ;*OF THE DESIRED ROUTINE. THEN USING THE ADDRESS OBTAINED IT WILL
 ;*GO TO THAT ROUTINE.

```
$TRAP:  MOV    R0,-(SP)      ;;SAVE R0
        MOV    2(SP),R0    ;;GET TRAP ADDRESS
        TST   -(R0)       ;;BACKUP BY 2
        MOVB  (R0),R0      ;;GET RIGHT BYTE OF TRAP
        ASL   R0           ;;POSITION FOR INDEXING
        MOV   $TRPAD(R0),R0 ;;INDEX TO TABLE
        RTS   R0           ;;GO TO ROUTINE
```

;;THIS IS USE TO HANDLE THE 'GETPRI' MACRO

```
$TRAP2: MOV   (SP),-(SP)    ;;MOVE THE PC DOWN
        MOV   4(SP),2(SP)  ;;MOVE THE PSW DOWN
        RTI                          ;;RESTORE THE PSW
```

.SBTTL TRAP TABLE

;*THIS TABLE CONTAINS THE STARTING ADDRESSES OF THE ROUTINES CALLED
 ;*BY THE 'TRAP' INSTRUCTION.

```
ROUTINE
-----
$TRPAD: .WORD  $TRAP2
        $TYPE  ;;CALL=TYPE      TRAP+1(104401)  TTY TYPEOUT ROUTINE
        $TYPOC ;;CALL=TYPOC    TRAP+2(104402)  TYPE OCTAL NUMBER (WITH LEADING ZEROS)
        $TYPOS ;;CALL=TYPOS    TRAP+3(104403)  TYPE OCTAL NUMBER (NO LEADING ZEROS)
        $TYPON ;;CALL=TYPON    TRAP+4(104404)  TYPE OCTAL NUMBER (AS PER LAST CALL)
        $TYPDS ;;CALL=TYPDS    TRAP+5(104405)  TYPE DECIMAL NUMBER (WITH SIGN)
        $TYPBN ;;CALL=TYPBN    TRAP+6(104406)  TYPE BINARY (ASCII) NUMBER

        $GTSWR ;;CALL=GTSWR    TRAP+7(104407)  GET SOFT-SWR SETTING

        $CKSWR ;;CALL=CKSWR    TRAP+10(104410) TEST FOR CHANGE IN SOFT-SWR
        $RDCHR ;;CALL=RDCHR    TRAP+11(104411) TTY TYPEIN CHARACTER ROUTINE
        $RDLIN ;;CALL=RDLIN    TRAP+12(104412) TTY TYPEIN STRING ROUTINE
        $RDOCT ;;CALL=RDOCT    TRAP+13(104413) READ AN OCTAL NUMBER FROM TTY
        TEST   ;;CALL=CHECK    TRAP+14(104414)
        TESTIT ;;CALL=CHKIT    TRAP+15(104415)
        DECTYP ;;CALL=TYPDC    TRAP+16(104416)
        TPRMP  ;;CALL=TESTID   TRAP+17(104417)
        DELAY4 ;;CALL=DELY     TRAP+20(104420)
        DIST: .BLKW 200.      ;STATE-WIDTH DISTRIBUTION
        BUFFER: .BLKW 4096.   ;BUFFER AREA
```

.END

GTSWR =	104407	203	276	2431#					
GVECT	001402	126#	1971*	1972*					
HAFMSG	021776	1779	2310#						
HALF	014476	1771	1773#						
HEAD5	022642	1154	2323#						
HIDLY1	013704	1629	1638	1639#					
HIDLY2	014020	1657	1666	1667#					
HIDLY3	014134	1685	1694	1695#					
HT =	000011	21#	2424						
HUNS	024544	2048*	2061*	2062	2064*	2068*	2075	2389#	
IGND	021424	1069	2300#						
INITVT	023075	202	2349#						
INRNGE	014214	1714	1717#						
IOTRD	016612	47	312	1450	2166#				
IOTVEC=	000020	21#	191*						
IVOLT	021523	1083	2302#						
KBVECT	001374	123#							
LAST	014330	1728	1741#						
LEND	007666	986	988	990#					
LESS	015770	2017	2019#						
LF =	000012	21#	2424						
LINEA	022621	1833	2322#						
LOAD	015232	1884	1887	1891#					
LOADY	015750	1798	1800	1894	2013#				
LOADO	015236	1892#	1896						
LODLY1	013676	1627	1637#						
LODLY2	014012	1655	1665#						
LODLY3	014126	1683	1693#						
LO2	015230	1846	1890#						
LSB	021021	1479	2287#						
LSBMSG	020756	1740	2282#						
MADR	017425	1414	2253#						
MASKNM	001512	162#	1435*	1437	1451*	2407	2408		
MAX	001500	157#	1866*	1871	1873*	1877			
MAXTST	015130	1869	1871#						
MDASH	017347	1312	1386	1395	2249#				
MDIF	017373	1327	2251#						
MEND	022045	1202	2311#						
MESGD	022750	2139	2326#						
MIN	001474	155#	1865*	1868	1870*	1876			
MINUS	017343	1521	1992	2041	2247#				
MLSB	023010	1025	2330#						
MLSBAT	023016	1916	2331#						
MNCADO	001332	104#	1442	1444					
MOFSET	022775	794	2329#						
MPRMP	017413	1329	2252#						
MSE	017353	1323	2250#						
MSG16	022247	1793	2317#						
MSG18	022143	1881	2314#						
MSG20	022203	1584	2316#						
MSG21	022574	1810	2321#						
MTEST	002470	231#	273						
MTEST0	002532	228	239	241#					
MTEST1	002536	226	242#	277	289	1430			

THOUS	024543	2049*	2065*	2067*	2071	2073	2388#	
TKVEC =	000060	21#	2402*					
TM15	020433	371	2271#					
TOFF	007772	795	1021#	1075	1099			
TPRMP	010532	1138#	2435					
TPVEC =	000064	21#						
TP15	020424	354	2270#					
TRAPVE=	000034	21#	191*					
TRFRO	017074	2173*	2238#					
TRTO	017072	2166*	2167*	2168	2220	2226	2237#	
TRTVEC=	000014	21#						
TRY	013262	1551#	1574					
TSTAD	020000	1251	2263#					
TSTADM	020022	1257	2264#					
TSTDAC	007744	861	865	890	894	1010#	1505	1510
TST1	003266	344#	346					
TST10	003700	418#						
TST11	003714	423#						
TST12	003730	428#						
TST13	003744	432#						
TST14	004002	440#						
TST15	004034	447#						
TST16	004104	457#						
TST17	004146	467#						
TST2	003424	365	370#					
TST20	004176	476#						
TST21	004242	484	486#					
TST22	004320	495	497#					
TST23	004500	524#						
TST24	004654	549#						
TST25	004734	564#						
TST26	005024	577#						
TST27	005110	590#						
TST3	003516	348	350	382	388#			
TST30	005174	605#						
TST31	005406	651#	652					
TST32	005464	661#						
TST33	005514	669#						
TST34	005544	676#						
TST35	005606	687#						
TST36	005636	695#						
TST37	005666	704#						
TST4	003604	400#						
TST40	005774	712	723#					
TST41	006240	730	732	765	772#			
TST42	006326	785#						
TST43	006440	804	807#					
TST44	006566	835#						
TST45	007366	936#						
TST46	007636	976	984#					
TST5	003620	404#						
TST6	003650	410#						
TST7	003664	414#						
TST8	015142	1872	1874#					

CVMNA-A MNCAD/MNCAM CVMNAA.P11		DIAGNOSTIC CROSS REFERENCE TABLE		MACY11 27(654)	19-SEP-78	K 9 08:51	PAGE 47-13		SEQ 0114								
\$ERROR	026626	191	2419#														
\$ERRPC	001116	59#	2395	2396	2397	2398	2399	2419*	2420								
\$ERRTB	001252	59#	2420														
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COMMEN	21#															
DUMWRN	64#															
ENDCOM	21#															
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	693	701	717	756	784	828	833	1118	1121	1124	1127	2177				
ESCAPE	21#	165														
GETPRI	21#															
GETSWR	21#	203#														
MULT	21#															
NEWTST	21#	344	370	388	400	404	410	414	418	423	428	432	440	447	457	
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POP	21#	2404	2421	2425	2426											
PUSH	21#	2404	2421	2425	2426											
REPORT	21#															
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SETPRI	21#	2402														
SETTRA	2431#	2432	2433	2434	2435	2436										
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SPACE	21#															
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TRMTRP	2431#															
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TYPNAM	21#	203														
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	1616	1617	1644	1645	1672	1673	1760	1774	1775	1814	1855	1939	1976	1999	2229
	2231	2233	2402	2404	2420	2424	2425	2426	2428						
ASL	397	1435	1545	1546	1547	1548	1630	1658	1686	1717	2402	2404	2420	2431	
ASLB	2425														
ASR	746	1438	1573	1709	1710	1711	1719	1736	1824	1827	1828	1829	1851	1852	1853
	1942	1943	1944	1979	1980	1981	2000	2001	2003	2004	2025	2026	2027	2028	2029
	2410	2426													
BCC	2425														
BEQ	191	203	223	273	330	395	484	495	572	585	589	607	849	855	884
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	1900	1998	2051	2072	2130	2133	2138	2402	2404	2406	2411	2419	2420	2424	2426
	2428	2429													
BGE	820	1566	1721	1869	2406										
BGT	872	901	1492	1571	1958	2130	2402	2425	2428						
BHI	1318	2406													
BHIS	742														
BIC	237	393	453	499	526	570	584	614	628	759	1146	1159	1253	1259	1314
	1360	1378	1459	1620	1648	1576	1993	2020	2030	2130	2227	2402	2404	2428	
BICB	253	1270	1402												
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BISB	1974	2067	2068	2069	2070	2420									
BIT	299	588	1160	1172	1248	1325	1337	1354	1372	1899	1989	1997	2406	2419	
BITB	191	2424	2426												
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BLO	712	732	751	925	976	2402									
BLOS	2402														
BLT	1714	2017	2402	2424	2425	2428									
BMI	228	298	315	317	923	1022	1039	1641	1669	1697	2082	2425			
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	1875	1880	1896	1906	1908	1932	1941	1970	1978	1990	2002	2055	2059	2063	2212
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CLC	2429														
CLR	168	177	189	191	244	247	248	249	292	333	337	338	351	433	458
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CVMNA-A MNCAD/MNCAM DIAGNOSTIC CROSS REFERENCE TABLE
CVMNAA.P11

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	1966	1994	2015	2130	2234	2402	2404	2406	2420	2421	2425	2428			
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CMP	170	191	203	212	301	304	394	398	494	515	540	571	644	711	719
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	2425														
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DEC	335	451	557	598	615	822	1030	1179	1181	1382	1447	1568	1579	1596	1607
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DECB	2424	2428													
EMT	21														
HALT	1276	1901	2170	2419	2421	2424									
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INCB	240	813	2053	2057	2061	2065	2406	2419	2424						
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MOV	165	179	180	181	182	183	184	185	188	191	193	194	195	196	204
	205	206	208	209	210	213	214	215	216	218	219	220	235	245	252
	291	293	312	313	319	326	339	344	345	346	352	357	370	374	389
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SEC	2429														
SOB	207	1622	1650	1678											
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TRAP	2431	2432	2433	2434	2435	2436									
TST	198	200	203	225	227	294	297	305	307	310	314	316	322	324	347
	349	522	537	542	591	602	606	610	620	627	631	653	677	724	729
	736	761	769	845	911	918	943	969	985	987	1010	1015	1043	1071	1086
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	1790	1845	1893	1912	1959	2013	2039	2050	2081	2087	2132	2137	2211	2235	2402
	2404	2406	2419	2420	2424	2425	2426	2428	2431						
TSTB	222	460	470	481	491	507	552	581	618	814	1038	1143	1308	1357	1375
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WAIT	1170	1558	1564	1624	1652	1680	1938	1975							
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	2370	2371	2372	2373											
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CVMNA-A MNCAD/MNCAM
CVMNA.P11

DIAGNOSTIC
CROSS REFERENCE TABLE

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ERRORS DETECTED: 0

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